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**eg40-Makefile**

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```
#
# An example Makefile that ties example eg39
# together.
#
# This also introduces the use of variables in
# Makefile, and auto-compilation of .c -> .o
#
CC=gcc
CFLAGS=-g -Wall

OBJECTS=eg39-main.o eg39-module.o

eg39: $(OBJECTS)
    $(CC) $(CFLAGS) $(OBJECTS) -o eg39
    eg39

clean:
    rm $(OBJECTS)
```

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**eg43-Makefile**

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```
#
# Introduces static library.
#
OBJECTS=eg39-module.o
AR=ar

all: main

lib2281.a: $(OBJECTS)
    $(AR) r lib2281.a $(OBJECTS)

main: lib2281.a eg39-main.c
    gcc -L. -o main eg39-main.c -l2281
```

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**eg39-module.h**

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```
#ifndef FOO_H  
#define FOO_H  
  
void foo(char *);  
  
#endif
```