

Debugging of Evolving Programs

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Evolving Programs

- Code Evolution
 - Consider a banking system
 - Features: Login, Logout, View Balance, ...
 - Version 1, P1
 - Customer wants new feature, produce new version P2
 - New feature: Funds transfer
 - This breaks the functionality of “View Balance”
 - No longer see the latest balance correctly!
 - Example of regression due to code evolution
 - Different from “requirements evolution” – intended meaning of “view balance” is unchanged from P1 to P2.

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Problem Statement

why?

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Change Analysis?

Search among subsets !

```

...
if (x > 0) {
  y = x + 1;
  z = x;
  w = x + 2;
} else {
  y = x;
}
...
                
```

```

...
if (x > 0) {
  y = x;
  z = x;
  w = x + 1;
} else {
  y = x + 1;
}
...
                
```

Requires defining the set of all changes.

Question:

 What if the two programs are completely different implementations of the same protocol?

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Trace Comparison?

Compare failing test with a **similar, successful** test.

Requirement:

 How do we find such an execution?

Question :

 why ignore the evolution?

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Adapting Trace Comparison

Directly Compare σ and π

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How to obtain the new test?

- We have:
 - Two versions of the program. (P and P').
 - A test t that fails on P' but passes on P .
- Key requirement: Similarity
 - Test t and t' are **similar** if they induce
 - same control flow path in P but
 - different paths in P' .

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How to obtain the new test?

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Our Approach

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Path conditions

- Quantifier free first order logic formula
- Obtained from a path
 - input $x, y;$
 - if $(x > 1)\{ \quad \quad \quad x > 1$
 - if $(y < 20)\{ \quad \quad \quad x > 1 \wedge y < 20$
 -
- Conjunction of primitive constraints with program variables.
 - All program variables implicitly existentially quantified.

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Exercise

- Given a program and an input, develop an automated method to compute path conditions.
 - input $x, y;$
 - if $(x > 1)\{ \quad \quad \quad x > 1$
 - $z = x + y$
 - if $(z < 20)\{ \quad \quad \quad x > 1 \wedge x + y < 20$
 -

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Generating New Input

- Compute f , the path condition of t in P .
- Compute f' , the path condition of t in P' .
- Solve for $f' \wedge \neg f$
 - Many solutions: Compare the trace of each t' in P' with the trace of t in P . **Return bug report from P' .**
 - No solution: go to next step.
- Solve for $f' \wedge \neg f$
 - Many solutions: Compare the trace of each t' in P with the trace of t in P . **Return bug report from P .**
 - No solution: Impossible, since then $f \Leftrightarrow f'$

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Simple Example

```

int inp, outp;
scanf("%d", &inp);
if (inp >= 1){
  outp = g(inp);
  if (inp > 9){
    outp = h(inp);
  }
} else {
  outp = h(inp);
}
printf("%d", outp);

```

```

int inp, outp;
scanf("%d", &inp);
if (inp >= 1){
  outp = g(inp);
  if (inp > 9){
    outp = h(inp);
  }
} else {
  outp = h(inp);
}
printf("%d", outp);

```

1,2,...9 10,11,... Explain $inp == 100$ 1,2,...,9, 10,11,...

0,-1,-2,... using ?? 9 0,-1,-2,...

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```

int inp, outp;
scanf("%d", &inp);
if (inp >= 1){
  outp = g(inp);
  if (inp > 9){
    outp = h(inp);
  }
} else {
  outp = h(inp);
}
printf("%d", outp);

```

$inp == 100$

P

```

int inp, outp;
scanf("%d", &inp);
if (inp >= 1){
  outp = g(inp);
  /* inp > 9 */
  outp = h(inp);
} else {
  outp = h(inp);
}
printf("%d", outp);

```

P'

Path condition f
 $(inp >= 1) \ \&\& \ (inp > 9)$

Path condition f'
 $(inp >= 1)$

$f \wedge \neg f' = (inp > 9) \ \&\& \ (inp \leq 9)$ STP Solver **No soln.**

$f' \wedge \neg f = (inp >= 1) \ \&\& \ (inp \leq 9)$ STP Solver **inp==9**

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$inp == 100$

```

1 int inp, outp;
2 scanf("%d", &inp);
3 if (inp >= 1){
4   outp = g(inp);
5   if (inp > 9){
6     outp = h(inp);
7   }
8 } else {
9   outp = h(inp);
10 }
11 printf("%d", outp);

```

P

$inp == 9$

```

1 int inp, outp;
2 scanf("%d", &inp);
3 if (inp >= 1){
4   outp = g(inp);
5   if (inp > 9){
6     outp = h(inp);
7   }
8 } else {
9   outp = h(inp);
10 }
11 printf("%d", outp);

```

P'

Trace Alignment

1	2	3	4	5	6	7	11
1	2	3	4	5	-	-	11

Bug Report : Line 5 `if (inp > 9){`

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Overview of our Solution

1. Compute f , the path condition of t in P .
2. Compute f' , the path condition of t in P' .
3. Solve for $f \wedge \neg f'$.
 - Many solutions: Compare the trace of each t' in P' with the trace of t in P . Return bug report from P' .
 - No solution: go to next step.
4. Solve for $f' \wedge \neg f$.
 - Many solutions: Compare the trace of each t' in P with the trace of t in P' . Return bug report from P .
 - No solution: Impossible, since then $(f \Leftrightarrow f')$

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Choosing Alternative Inputs

Solve $f \wedge \neg f'$

$$f' = (\psi_1 \wedge \psi_2 \wedge \dots \wedge \psi_m)$$

Check for satisfiability of

$$f \wedge \neg \psi_1$$

$$f \wedge \psi_1 \wedge \neg \psi_2$$

$$f \wedge \psi_1 \wedge \psi_2 \wedge \neg \psi_3$$

.....

At most m alternate inputs !!

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Bug report for one alternate input

t_{new} = input obtained by solving

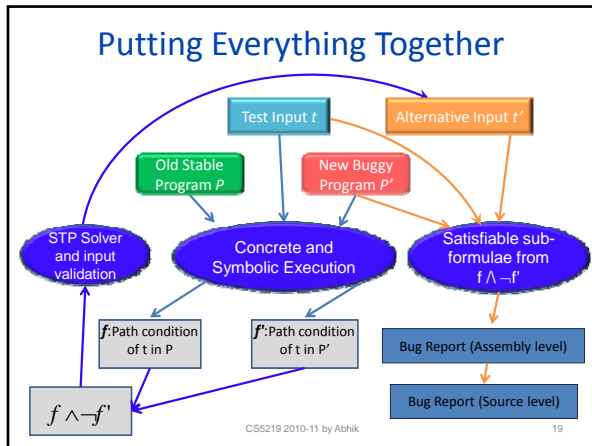
$$f \wedge \psi_1 \wedge \psi_2 \wedge \neg \psi_3$$

Bug report by comparing traces of t_{bug} and t_{new} , should be the branch b_3 !!

At most m alternate inputs \Rightarrow at most m lines in bug report.

Comparing traces with deviation in one branch – simple trace comparison, or even remove trace comparison altogether

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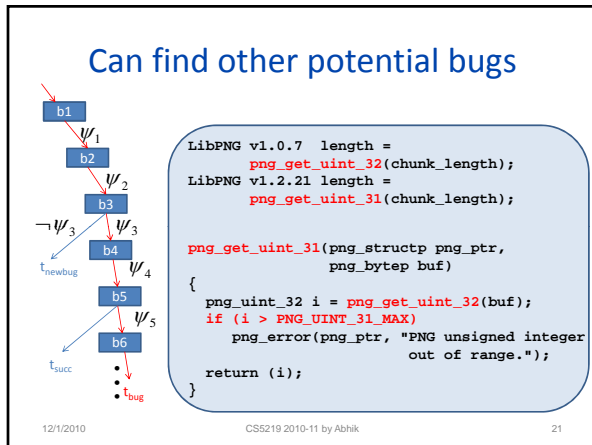


Results

Buggy Program	Stable program	Time taken	Bug report size
LibPNG v1.0.7 (31164 loc)	LibPNG v1.2.21 (36776 loc)	13 m 34 s	9
TCPflow (patched)	TCPflow (unpatched)	31m	6
Miniweb (2838 loc)	Apache (358379 loc)	14s	5
Savant (8730 loc)	Apache httpd (358379 loc)	9m	46


If we require the alternative input to behave the same in buggy program and reference program (passing test) - the bug report size is 1 in all three cases.


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- ### Summary so far
- Novel approach for debugging evolving programs
 - Semantic analysis to generate alternative similar inputs
 - Can be applied to two totally different implementations.
 - Implementation and Evaluation
 - DARWIN tool : built on BitBlaze platform.
 - Accurate bug reports for various real-life examples.
 - Extensions
 - Found new bugs via debugging of a given observable error
 - More detailed path conditions via pred. instrumentation.
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An experiment

Validate Embedded Linux 

AGAINST 

Linux (GNU Core-utils, net -tools)

Busybox distribution is 121 KLOC.
Various errors to be root-caused in tr, arp, top, printf.

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- ### Trying on Embedded Linux
- The concept
 - Golden: GNU Coreutils, net-tools
 - Buggy: Busybox
 - De-facto distribution for embedded devices.
 - Aims for low code size
 - Less checks and more errors.
 - Try DARWIN!
 - The practice
 - Failing input takes logically equivalent paths in Busybox and Core-utils.
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Going beyond

P

```
input x;
y = 2 * x;
output y
```

P'

```
input x;
y = 2*x + 1; // bug
output y
```

Observable error: Input $x == 0$, Expected output $y == 0$
Observed output $y == 1$

Employ DARWIN:
In program P, path condition $f == \text{true}$
path condition $f' == \text{true}$
 $f \wedge \neg f' == \text{false}$ also $f' \wedge \neg f == \text{false}$.
No Bug report generated !!

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What went wrong?

- The effect of the bug is not observable in terms of change in program paths.

P

```
input x;
y = 2 * x;
output y
```

P'

```
input x;
y = 2*x+1; // bug
output y
```

- Reasoning with path conditions does not expose the bug location either!

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A more direct approach

P

```
input x;
y = 2 * x;
output y
```

P'

```
input x;
y = 2*x+1; // bug
output y
```

- Characterize observable error (obs)
 - $y != 0$
- Weakest pre-condition along failing path w.r.t. obs
 - $2*x != 0$
 - $2*x + 1 != 0$
- Compare the WPs and find differing constraints.
- Map differing constraints to the lines contributing them.

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Weakest pre-condition

Along a program path.

```
input x, y;
x = x + 1;
if (x + y > 0){
  z = z - 1;
}
else {...}
print z;
```

$z - 1 < 0 \wedge x + 1 + y > 0$
 $z - 1 < 0 \wedge x + y > 0$
 $z - 1 < 0$

$z < 0$
 $z < 0$

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WP along a path

- Along a path
 - Start with a primitive constraint
 - You are seeking to explain under what situations it will hold at the end of the program path.
 - Proceed along the path from the end.
 - For every branch, conjoin the branch condition.
 - For every assignment, replace occurrences of the lhs by rhs in the existing formula.
 - Stop when you reach the beginning of the path.
 - What kind of a constraint will you end up with?

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Entire failing trace is not needed

```
1. ... // input inp1, inp2
2. if (inp1 > 0)
3.   x = f1(inp1); // bug
4. else x = g1(inp1);
5. if (inp2 > 0)
6.   y = f2(inp2);
7. else y = g2(inp2);
8. ... // output x, y observe unexpected x < 0 for inp1 == inp2 == 1
```

Observable error: $x < 0$ at line 8.
 WP along the trace of $\text{inp1} == \text{inp2} == 1$ gives us
 $\text{inp2} > 0 \wedge \text{inp1} > 0 \wedge f1(\text{inp1}) < 0$
 Points to lines { 2, 3, 5 }
Line 5 is clearly not relevant since inp2 does not contribute to computing x.

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What is the issue?

- Inherent parallelism exists in sequential programs
 - inp1 helps compute x
 - inp2 helps compute y
- Exploit the inherent parallelism to project the “relevant” part of the trace.
 - Dynamic slicing (from last lecture!)**
 - Symbolic execution (WP computation) along the dynamic slice only.
- Crucial for scalability of our method!

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Approach 2 – in action (simplified)

<ol style="list-style-type: none"> 1. ... // input inp1, inp2 2. if (inp1 > 0) 3. x = f1(inp1); // bug 4. else x = g1(inp1); 5. if (inp2 > 0) 6. y = f2(inp2) 7. else y = g2(inp2); 8. ... // output x, y 	$f1(inp1) < 0 \wedge inp1 > 0$ $f1(inp1) < 0 \wedge inp1 > 0$ (control dep.) $f1(inp1) < 0$ (data dep.) observe unexpected $x < 0$ for $inp1 == inp2 == 1$
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Approach 2 - summary

- Set observable error: $x < 0$
- Set slicing criterion: value of x at line 8
- Simultaneously perform
 - Dynamic slicing – Control and Data dependencies
 - Symbolic execution – along the slice
 - WP computation along the slice
- The above is performed on both P, P'
 - Produces WP, WP' – conjunction of constraints
 - Find differing constraints in WP, WP'
 - Map differing constraints to contributing LOC – this is the bug-report.

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Comparing WP, WP'

- WP = $(\phi_1 \wedge \phi_2 \wedge \dots \wedge \phi_n)$
- WP' = $(\phi'_1 \wedge \phi'_2 \wedge \dots \wedge \phi'_m)$
- Check
 - WP $\Rightarrow \phi'_1 \dots$
 - WP' $\Rightarrow \phi_1 \dots$
 - Solver may choke!
- Instead, we can perform pair-wise comparison
 - Too costly ??

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Comparing WP, WP'

- WP = $(\phi_1 \wedge \phi_2 \wedge \dots \wedge \phi_n)$
- WP' = $(\phi'_1 \wedge \phi'_2 \wedge \dots \wedge \phi'_m)$
- Pair-wise comparison of constraints can blow-up.
- Tautology elimination – more than 90% reduction!

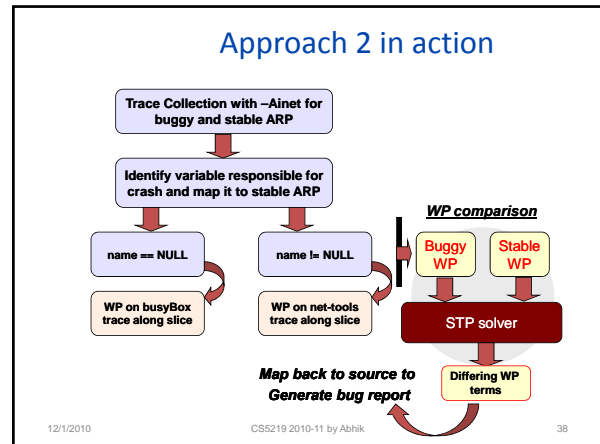
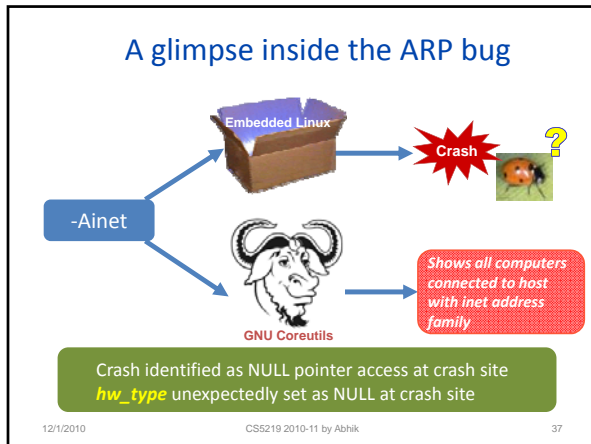
<pre style="margin: 0;">X = 1 ... if (X > 0) { ... printf("%d", Y); }</pre>	<p style="font-size: x-small; margin: 0;"><u>WP computation along slice:</u></p> $1 > 0 \wedge Y < 0$ // due to assignment of X $X > 0 \wedge Y < 0$ // due to the branch $Y < 0$ // the constraint we start with
--	---

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So, what do we do then?

- WP = Conjunction of n constraints
 - Remove tautologies
 - WP = $(\phi_1 \wedge \phi_2 \wedge \dots \wedge \phi_x)$ $x < n$
- WP' = conjunction of m constraints
 - Remove tautologies
 - WP' = $(\phi'_1 \wedge \phi'_2 \wedge \dots \wedge \phi'_y)$ $y < m$
- For each ϕ'_i check if there is a ϕ_j s.t. $\phi_j \Rightarrow \phi'_i$
- For each ϕ_i check if there is a ϕ'_j s.t. $\phi'_j \Rightarrow \phi_i$

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- ### Summarizing
- Debugging evolving programs (code evolution)
 - Program Versions
 - Embedded SW against non-embedded version
 - Two implementations of same specification
 - Web-servers implementing http protocol
 - Use of formal techniques into debugging
 - Beyond a “black art”.
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For more ...

[FSE09] DARWIN: An Approach for Debugging Evolving Programs Dawei Qi, Abhik Roychoudhury, Zhenkai Liang, Kapil Vaswani, ACM SIGSOFT Symposium on the Foundations of Software Engineering (FSE), ESEC-FSE 2009.
 – <http://www.comp.nus.edu.sg/~abhik/pdf/fse09.pdf>

Also see:
 Yesterday my program worked. Today it does not. Why?
 Andreas Zeller, [ESEC-FSE 1999].
 – <http://www.infosun.fim.uni-passau.de/st/papers/tr-99-01/ese99-talk.pdf>

[FSE10] Golden Implementation Driven Software Debugging Ansuman Banerjee, Abhik Roychoudhury, Johannes A. Harlie, Zhenkai Liang, ACM SIGSOFT Symposium on Foundations of Software Engineering (FSE) 2010.
 – <http://www.comp.nus.edu.sg/~abhik/pdf/fse10.pdf>

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