

# Shape Analysis via Second-Order Bi-Abduction (Technical Report)

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**Abstract.** We present a new modular shape analysis that can synthesize heap memory specification on a per method basis. We rely on a *second-order bi-abduction* mechanism that can give interpretations to unknown shape predicates. There are several novel features in our shape analysis. Firstly, it is grounded on second-order bi-abduction. Secondly, we distinguish unknown pre-predicates in pre-conditions, from unknown post-predicates in post-condition; since the former may be strengthened, while the latter may be weakened. Thirdly, we provide a new *heap guard* mechanism to support more precise preconditions for heap specification. Lastly, we formalise a set of derivation and normalization rules to give concise definitions for unknown predicates. Our approach has been proven sound and is implemented on top of an existing automated verification system. We show its versatility in synthesizing a wide range of intricate shape specifications.

## 1 Introduction

An important challenge for automatic program verifiers lies in inferring shapes describing abstractions for data structures used by each method. In the context of heap manipulating programs, determining the shape abstraction is crucial for proving memory safety and is a precursor to supporting functional correctness.

However, discovering shape abstractions can be rather challenging, as linked data structures span a wide variety of forms, from singly-linked lists, doubly-linked lists, circular lists, to tree-like data structures. Previous shape analysis proposals have made great progress in solving this problem. However, the prevailing approach relies on using a predefined vocabulary of shape definitions (typically limited to singly-linked list segments) and trying to determine if any of the pre-defined shapes fit the data structures used. This works well with programs that use simpler shapes, but would fail for programs which use more intricate data structures. An example is the method below (written in C and adapted from [19]) to build a tree whose leaf nodes are linked as a list.

```
struct tree { struct tree* parent; struct tree* l; struct tree* r; struct tree* next }
struct tree* tll(struct tree* x, struct tree* p, struct tree* t)
{ x->parent = p;
  if (x->r==NULL) { x->next=t; return x; }
  else{ struct tree* lm = tll(x->r, x, t); return tll(x->l, x, lm); } }
```

Our approach to modular shape analysis would introduce an unknown pre-predicate  $H$  (as the pre-condition), and an unknown post-predicate  $G$  (as the post-condition), as shown below, where  $res$  is the method's result.

requires  $H(x, p, t)$       ensures  $G(x, p, res, t)$

Using Hoare-style verification and a new second-order bi-abduction entailment procedure, we would derive a set of relational assumptions for the two unknown predicates. These derived assumptions are to ensure memory safety, and can be systematically transformed into concise predicate definitions for the unknown predicates, such as:

$$\begin{aligned} H(x,p,t) &\equiv x \mapsto \text{tree}(\mathcal{D}_p, \mathcal{D}_1, r, \mathcal{D}_n) \wedge r = \text{NULL} \\ &\quad \vee x \mapsto \text{tree}(\mathcal{D}_p, l, r, \mathcal{D}_n) * H(l, x, lm) * H(r, x, t) \wedge r \neq \text{NULL} \\ G(x,p, \text{res}, t) &\equiv x \mapsto \text{tree}(p, \mathcal{D}_1, r, t) \wedge \text{res} = x \wedge r = \text{NULL} \\ &\quad \vee x \mapsto \text{tree}(p, l, r, \mathcal{D}_n) * G(l, x, \text{res}, lm) * G(r, x, lm, t) \wedge r \neq \text{NULL} \end{aligned}$$

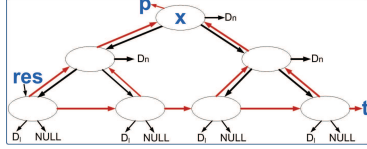


Fig. 1. An example of  $G(x,p,\text{res},t)$

The derived pre-predicate  $H$  captures a binary tree-like shape that would be traversed by the method.  $x \mapsto \text{tree}(\mathcal{D}_p, \mathcal{D}_1, r, \mathcal{D}_n)$  denotes that  $x$  refers to a tree node with its parent,  $l, r$  and next fields being  $\mathcal{D}_p, \mathcal{D}_1, r$  and  $\mathcal{D}_n$ , respectively. We use dangling references, such as  $\mathcal{D}_1, \mathcal{D}_p, \mathcal{D}_n$ , as generic

markers that denote field pointers that are not traversed by the method. Thus no assertion can be made on any of the  $\mathcal{D}$  pointers. The post-predicate  $G$ , illustrated in Fig 1, adds parent field links for all nodes, and next field links for just the leaves.<sup>1</sup>

Current shape analysis mechanisms [12,4,6] are unable to infer pre/post specifications that ensure memory-safety for such complex examples. In this paper, we propose a fresh approach to shape analysis that can synthesize, from scratch, a set of shape abstractions that ensure memory safety. The central concept behind our proposal is the use of *unknown predicates* (or *second-order variables*) as place holders for shape predicates that are to be synthesized directly from proof obligations gathered by our verification process. Our proposal is based on a novel *bi-abductive entailment* that supports *second-order* variables. The core of the new entailment procedure generates a set of relational assumptions on unknown predicates to ensure memory safety. These assumptions are then refined into predicate definitions, by predicate *derivation* and *normalization* steps.

By building the generation of the required *relational assumptions* over unknown predicates directly into the new entailment checker, we were able to integrate our shape analysis into an existing program verifier with changes made only to the entailment process, rather than the program verification/analysis itself. Our proposed shape analysis thus applies an almost standard set of Hoare rules in constructing proof obligations which are discharged through the use of a new *second-order* bi-abductive entailment.

This paper makes the following four primary contributions.

- A novel *second-order bi-abduction* guided by an annotation scheme to infer relational assumptions (over unknown predicates) as part of Hoare-style verification.
- A set of formal rules for *deriving* and *normalizing* each unknown predicate definition from the relational assumptions with heap guard conditions.
- A *sound* and *modular* shape analysis, that is applied on a per method basis<sup>2</sup>.
- Our implementation and experiments on *shape inference*, closely integrated into an automated verification system. The tool is available for online use (see Appendix

<sup>1</sup> Note that new links formed by the method are colored in red.

<sup>2</sup> Most existing shape analyses require either global analyses or re-verification after analysis. For example, bi-abduction in [6] requires its method's inferred pre-condition to be re-verified due to its use of over-approximation on heap pre-condition which can be unsound.

C to download a virtual machine including S2's source code) at  
<http://loris-7.ddns.comp.nus.edu.sg/~project/s2/beta>

## 2 Logic Syntax for Shape Specification

Separation logic is an extension of Hoare logic for reasoning with heap-based programs [20,27]. We outline below the fragment underlying the proposed analysis:

Disj. formula	$\Phi ::= \Delta \mid \Phi_1 \vee \Phi_2$
Guarded Disj.	$\Phi^g ::= \Delta \mid (\Delta @ (\kappa \wedge \pi)) \mid \Phi^g_1 \vee \Phi^g_2$
Conj. formula	$\Delta ::= \exists \bar{v}. (\kappa \wedge \pi)$
Spatial formula	$\kappa ::= \mathbf{emp} \mid \top \mid v \mapsto c(\bar{v}) \mid P(\bar{v}) \mid U(\bar{v}) \mid \kappa_1 * \kappa_2$
Pure formula	$\pi ::= \alpha \mid \neg \alpha \mid \pi_1 \wedge \pi_2$
Var (Dis)Equality	$\alpha ::= v \mid v_1 = v_2 \mid v = \mathbf{NULL} \mid v_1 \neq v_2 \mid v \neq \mathbf{NULL}$
Pred. Defn.	$P^{\mathbf{def}} ::= P(\bar{v}) \equiv \Phi^g$
Pred. Dict.	$\Gamma ::= \{P_1^{\mathbf{def}}, \dots, P_n^{\mathbf{def}}\}$
	$P \in \text{Known Predicates} \quad U \in \text{Unknown Predicates}$
	$c \in \text{Data Nodes} \quad v \in \text{Variables} \quad \bar{v} \equiv v_1 \dots v_n$

We introduce  $\Delta @ (\kappa \wedge \pi)$ , a special syntactic form called *guarded heap* that capture a heap context  $\kappa \wedge \pi$  in which  $\Delta$  holds. Thus,  $\Delta @ (\kappa \wedge \pi)$  holds for heap configurations that satisfy  $\Delta$  and that can be extended such that they satisfy  $\Delta * \kappa \wedge \pi$ . In Sec.5 we will describe its use in allowing our shape inference to incorporate path sensitive information in the synthesized predicates. The assertion language is also extended with the following formula for describing heaps:  $\mathbf{emp}$  denoting the empty heap;  $\top$  denoting an arbitrary heap (pointed by dangling reference); points-to assertion,  $x \mapsto c(\bar{v})$ , specifying the heap in which  $x$  points to a data structure of type  $c$  whose fields contain the values  $\bar{v}$ ; known predicate,  $P(\bar{v})$ , which holds for heaps in which the shape of the memory locations reachable from  $\bar{v}$  can be described by the  $P$  predicate; unknown predicates,  $U(\bar{v})$ , with no prior given definitions. Separation conjunction  $\kappa_1 * \kappa_2$  holds for heaps that can be partitioned in two disjoint components satisfying  $\kappa_1$  and  $\kappa_2$ , respectively. The pure formula captures only pointer equality and disequality. We allow a special constant  $\mathbf{NULL}$  to denote a pointer which does not point to any heap location. Known predicates  $P(\bar{v})$  are defined inductively through disjunctive formulas  $\Phi^g$ . Their definitions are either user-given or synthesised by our analysis. We will use  $\Gamma$  to denote the repository (or set) of available predicate definitions. Through our analysis, we shall construct an inductive definition for each unknown predicate, where possible. Unknown predicates that have *not* been instantiated would not have any definition. They denote data fields that are not accessed by their methods, and would be marked as *dangling pointers*.

## 3 Overview of Our Approach

Our approach comprises three main steps: (i) inferring relational assumptions for unknown predicates via Hoare-style verification, (ii) deriving predicates from relational assumptions, (iii) normalizing predicates. For (i), a key machinery is the entailment procedure that must work with second-order variables (unknown predicates). Previous

bi-abduction entailment proposals, pioneered by [6], would take an antecedent  $\Delta_{\text{ante}}$  and a consequent  $\Delta_{\text{conseq}}$  and return a frame residue  $\Delta_{\text{frame}}$  and the precondition  $\Delta_{\text{pre}}$ , such that the following holds:  $\Delta_{\text{pre}} * \Delta_{\text{ante}} \models \Delta_{\text{conseq}} * \Delta_{\text{frame}}$ . Here, all four components use separation logic formulas based on known predicates with prior definitions.

Taking a different tact, we start with an existing entailment procedure for separation logic with user-defined predicates, and extend it to accept formulas with second-order variables such that given an antecedent  $\Delta_{\text{ante}}$  and a consequent  $\Delta_{\text{conseq}}$  the resulting entailment procedure infers both the frame residue  $\Delta_{\text{frame}}$  and a set (or conjunction) of relational assumptions (on unknowns) of the form  $\mathcal{R} = \bigwedge_{i=1}^n (\Delta_i \Rightarrow \Phi^g_i)$  such that:

$$\mathcal{R} \wedge \Delta_{\text{ante}} \models \Delta_{\text{conseq}} * \Delta_{\text{frame}}$$

The inferred  $\mathcal{R}$  ensures the entailment's validity. We shall use the following notation  $\Delta_{\text{ante}} \vdash \Delta_{\text{conseq}} \rightsquigarrow (\mathcal{R}, \Delta_{\text{frame}})$  for this second-order bi-abduction process.

There are two scenarios to consider for unknown predicates: (1)  $\Delta_{\text{ante}}$  contains an *unknown* predicate instance that matched with a points-to or known predicate in  $\Delta_{\text{conseq}}$ ; (2)  $\Delta_{\text{conseq}}$  contains an *unknown* predicate instance. An example of the first scenario is:

$$U(x) \vdash x \mapsto \text{snode}(n) \rightsquigarrow (U(x) \Rightarrow x \mapsto \text{snode}(n) * U_0(n), U_0(n))$$

Here, we generated a relational assumption to denote an *unfolding* (or instantiation) for the unknown predicate  $U$  to a heap node  $\text{snode}$  followed by another unknown  $U_0(n)$  predicate. The data structure  $\text{snode}$  is defined as `struct snode { struct snode* next}`. A simple example of the second scenario is shown next.

$$x \mapsto \text{snode}(\text{NULL}) * y \mapsto \text{snode}(\text{NULL}) \vdash U_1(x) \rightsquigarrow (x \mapsto \text{snode}(\text{NULL}) \Rightarrow U_1(x), y \mapsto \text{snode}(\text{NULL}))$$

The generated relational assumption depicts a *folding* process for unknown  $U_1(x)$  which captures a heap state traversed from the pointer  $x$ . Both folding and unfolding of unknown predicates are crucial for second-order bi-abduction. To make it work properly for unknown predicates with multiple parameters, we shall later provide a novel  $\#$ -annotation scheme to guide these processes. For the moment, we shall use this annotation scheme implicitly. Consider the following method which traverses a singly-linked list and converts it to a doubly-linked list (let us ignore the states  $\alpha_1, \dots, \alpha_5$  for now):

```
struct node { struct node* prev; struct node* next }
void s112dll(struct node* x, struct node* q)
{ ( $\alpha_1$ ) if (x==NULL) ( $\alpha_2$ ) return; ( $\alpha_3$ ) x->prev = q; ( $\alpha_4$ ) s112dll(x->next, x); ( $\alpha_5$ ) }
```

To synthesize the shape specification for this method, we introduce two unknown predicates,  $H$  for the pre-condition and  $G$  for the post-condition, as below.

$$\text{requires } H(x, q) \quad \text{ensures } G(x, q)$$

We then apply code verification using these pre/post specifications with unknown predicates and attempt to collect a set of relational assumptions (over the unknown predicates) that must hold to ensure memory-safety. These assumptions would also ensure that the pre-condition of each method call is satisfied, and that the corresponding post-condition is ensured at the end of the method body. For example, our analysis can infer four relational assumptions for the `s112dll` method as shown in Fig. 2(a).

These relational assumptions include two new unknown predicates,  $H_p$  and  $H_n$ , created during the code verification process. All relational assumptions are of the form

$$\begin{array}{ll}
(A1). H(x, q) \wedge x = \text{NULL} \Rightarrow G(x, q) & (\alpha_1). H(x, q) \\
(A2). H(x, q) \wedge x \neq \text{NULL} \Rightarrow & (\alpha_2). H(x, q) \wedge x \neq \text{NULL} \\
\quad x \mapsto \text{node}(x_p, x_n) * H_p(x_p, q) * H_n(x_n, q) & (\alpha_3). x \mapsto \text{node}(x_p, x_n) * H_p(x_p, q) * H_n(x_n, q) \wedge x \neq \text{NULL} \\
(A3). H_n(x_n, q) \Rightarrow H(x_n, x) \text{ @ } x \mapsto \text{node}(q, x_n) & (\alpha_4). x \mapsto \text{node}(q, x_n) * H_p(x_p, q) * H_n(x_n, q) \wedge x \neq \text{NULL} \\
(A4). x \mapsto \text{node}(q, x_n) * G(x_n, x) \Rightarrow G(x, q) & (\alpha_5). x \mapsto \text{node}(q, x_n) * H_p(x_p, q) * \mathbf{G}(x_n, x) \wedge x \neq \text{NULL} \\
(a) & (b)
\end{array}$$

**Fig. 2.** Relational assumptions (a) and program states (b) for `s112d11`

$\Delta_{\text{lhs}} \Rightarrow \Delta_{\text{rhs}}$ , except for (A3) which has the form  $\Delta_{\text{lhs}} \Rightarrow \Delta_{\text{rhs}} \text{ @ } \Delta_g$  where  $\Delta_g$  denotes a heap guard condition. Such heap guard condition allows more precise pre-conditions to be synthesized (e.g.  $H_n$  in (A3)), and is shorthand for  $\Delta_{\text{lhs}} * \Delta_g \Rightarrow \Delta_{\text{rhs}} * \Delta_g$ .

Let us look at how relational assumptions are inferred. At the start of the method, we have  $(\alpha_1)$ , shown in Fig. 2 (b), as our program state. Upon exit from the then branch, the verification requires that the postcondition  $G(x, q)$  be established by the program state  $(\alpha_2)$ , generating the relational assumption (A1) via the following entailment:

$$(\alpha_2) \vdash G(x, q) \rightsquigarrow (A1, \text{emp} \wedge x = \text{NULL}) \quad (E1)$$

To get ready for the field access  $x \rightarrow \text{prev}$ , the following entailment is invoked to unfold the unknown  $H$  predicate to a heap node, generating the relational assumption (A2):

$$H(x, q) \wedge x \neq \text{NULL} \vdash x \mapsto \text{node}(x_p, x_n) \rightsquigarrow (A2, H_p(x_p, q) * H_n(x_n, q) \wedge x \neq \text{NULL}) \quad (E2)$$

Two new unknown predicates  $H_p$  and  $H_n$  are added to capture the `prev` ( $x_p$ ) and `next` ( $x_n$ ) fields of  $x$  (i.e. they represent heaps referred to by  $x_p$  and  $x_n$  respectively). After binding, the verification now reaches the state  $(\alpha_3)$ , which is then changed to  $(\alpha_4)$  by the field update  $x \rightarrow \text{prev} = q$ . Relational assumption (A3) is inferred from proving the precondition  $H(x_n, x)$  of the recursive call `s112d11(x  $\rightarrow$  next, x)` at the program state  $(\alpha_4)$ :

$$(\alpha_4) \vdash H(x_n, x) \rightsquigarrow (A3, x \mapsto \text{node}(q, x_n) * H_p(x_p, q) \wedge x \neq \text{NULL}) \quad (E3)$$

Note that the heap guard  $x \mapsto \text{node}(q, x_n)$  from  $(\alpha_4)$  is recorded in (A3), and is crucial for predicate derivation. The program state at the end of the recursive call,  $(\alpha_5)$ , is required to establish the post-condition  $G(x, q)$ , generating the relational assumption (A4):

$$(\alpha_5) \vdash G(x, q) \rightsquigarrow (A4, H_p(x_p, q) \wedge x \neq \text{NULL}) \quad (E4)$$

These relational assumptions are automatically inferred symbolically during code verification. Our next step (ii) uses a predicate derivation procedure to transform (by either equivalence-preserving or abductive steps) the set of relational assumptions into a set of predicate definitions. Sec. 5 gives more details on predicate derivation. For our `s112d11` example, we initially derive the following predicate definitions (for  $H$  and  $G$ ):

$$\begin{aligned}
H(x, q) &\equiv \text{emp} \wedge x = \text{NULL} \vee x \mapsto \text{node}(x_p, x_n) * H_p(x_p, q) * H(x_n, x) \\
G(x, q) &\equiv \text{emp} \wedge x = \text{NULL} \vee x \mapsto \text{node}(q, x_n) * G(x_n, x)
\end{aligned}$$

In the last step (iii), we use a normalization procedure to simplify the definition of predicate  $H$ . Since  $H_p$  is discovered as a dangling predicate, the special variable  $\mathcal{D}_p$  corresponds to a *dangling reference* introduced:  $H(x, q) \equiv \text{emp} \wedge x = \text{NULL} \vee x \mapsto \text{node}(\mathcal{D}_p, x_n) * H(x_n, x)$ . Furthermore, we can synthesize a more concise  $H_2$  from  $H$  by eliminating its

useless q parameter:  $H(x, q) \equiv H_2(x)$  and  $H_2(x) \equiv \text{emp} \wedge x = \text{NULL} \vee x \mapsto \text{node}(\mathcal{D}_p, x_n) * H_2(x_n)$ .

**The t11 example.** Let us revisit the t11 example shown in Sec 1. To synthesize the shape specification for this method, we introduce two unknown predicates, H for the pre-condition and G for the post-condition, as mentioned earlier.

requires  $H(x, p, t)$       ensures  $G(x, p, \text{res}, t)$

We then apply code verification using these pre/post specifications with unknown predicates and attempt to collect a set of relational assumptions (over the unknown predicates) that must hold to ensure memory-safety. These assumptions would also ensure that the pre-condition of each method call is satisfied, and that the post-condition is ensured at the end of its method body.

For example, our analysis can infer the following five relational assumptions for the t11 method:

- (1).  $H(x, p, t) \Rightarrow x \mapsto \text{tree}(x_p, l, r, n) * H_p(x_p, p, t) * H_1(l, p, t) * H_r(r, p, t) * H_n(n, p, t)$
- (2).  $H_r(r, p, t) \wedge r \neq \text{NULL} \ @ \ x \mapsto \text{tree}(p, l, r, n) \Rightarrow H(r, x, t)$
- (3).  $H_1(l, p, t) \Rightarrow H(l, x, lm) \ @ \ (x \mapsto \text{tree}(p, l, r, n) \wedge r \neq \text{NULL})$
- (4).  $H_1(l, p, t) * H_r(r, p, t) * x \mapsto \text{tree}(p, l, r, t) \wedge r = \text{NULL} \wedge \text{res} = x \Rightarrow G(x, p, \text{res}, t)$
- (5).  $H_n(n, p, t) * x \mapsto \text{tree}(p, l, r, n) * G(r, x, lm, t) * G(l, x, \text{res}, lm) \wedge r \neq \text{NULL} \Rightarrow G(x, p, \text{res}, t)$

Our relational assumptions include four new unknown predicates,  $H_p$ ,  $H_1$ ,  $H_r$  and  $H_n$ , that were created during the code verification process.

Let us look at how relational assumptions are inferred. For illustration, we annotate program states into the t11 example as follows:

```
struct tree* t11(struct tree* x, struct tree* p, struct tree* t) {
  (S1) x->parent = p;
  (S2) if (x->r == NULL) { (S3) x->next = t; return x; (S6) }
      else { (S4) struct tree* lm = t11(x->r, x, t); (S5) return t11(x->l, x, lm); (S7) }
}
```

- (S1).  $H(x, p, t)$
- (S2).  $x \mapsto \text{tree}(p, l, r, n) * H_1(l, p, t) * H_r(r, p, t) * H_n(n, p, t) * H_p(x_p, p, t)$
- (S3).  $x \mapsto \text{tree}(p, l, r, n) * H_1(l, p, t) * H_r(r, p, t) * H_n(n, p, t) \wedge r = \text{NULL}$
- (S4).  $x \mapsto \text{tree}(p, l, r, n) * H_1(l, p, t) * H_r(r, p, t) * H_n(n, p, t) \wedge r \neq \text{NULL}$
- (S5).  $x \mapsto \text{tree}(p, l, r, n) * H_1(l, p, t) * G(r, x, lm, t) * H_n(n, p, t) \wedge r \neq \text{NULL}$
- (S6).  $x \mapsto \text{tree}(p, l, r, t) * H_1(l, p, t) * H_r(r, p, t) \wedge r = \text{NULL} \wedge \text{res} = x$
- (S7).  $x \mapsto \text{tree}(p, l, r, n) * G(l, x, \text{res}, lm) * G(r, x, lm, t) * H_n(n, p, t) \wedge r \neq \text{NULL}$

At the start of the method, we have (S1), shown above, as our program state. Due to a field update,  $x \mapsto \text{parent}$ , relational assumption (1) was inferred which lead to (S2). The conditional evaluation led to (S3) and (S4), as the program states at the start of the then-branch and else-branch, respectively. Relational assumption (2) was then inferred from proving pre-condition  $H(r, x, t)$  of recursive call  $t11(x \mapsto r, x, t)$  under (S4), yielding program state (S5). Also, (3) was inferred from proving pre-condition  $H(l, x, lm)$  of the second recursive call  $t11(x \mapsto l, x, lm)$ . When inferring (3), heap guard  $x \mapsto \text{tree}(p, l, r, n) \wedge r \neq \text{NULL}$  from the program state (S5) was used, since

$H_1(l, p, t) \Rightarrow H(l, x, lm)$ , by itself, neither capture a connected context  $r \neq \text{NULL}$  from the then-branch, nor properly instantiate the back (parent) pointer  $x$ . The program state (S6) at the end of then-branch was then used to prove post-condition  $G(x, p, res, t)$ . This proving lead to relational assumption (4). Similarly, program state (S7) at the end of the else-branch, which assumed  $G(r, x, lm, t) * G(l, x, res, lm)$  from the two recursive calls, would infer (5) when proving the post-condition of the method itself.

These relational assumptions are inferred symbolically during code verification with the help of second-order bi-abduction mechanism that we are proposing. They are also being *modularly* inferred on a per method basis, using automatically generated template pre/post conditions with unknown predicates.

Our next phase uses a predicate synthesis and normalization procedure to transform (by either equivalence-preserving or abductive steps) the set of relational assumptions into a set of predicate definitions. For our running example, we synthesize the following predicate definitions (for  $H$  and  $G$ ):

$$\begin{aligned} H(x, p, t) &\equiv x \mapsto \text{tree}(\mathcal{D}_p, \mathcal{D}_1, r, \mathcal{D}_n) \wedge r = \text{NULL} \\ &\quad \vee x \mapsto \text{tree}(\mathcal{D}_p, l, r, \mathcal{D}_n) * H(l, x, lm) * H(r, x, t) \wedge r \neq \text{NULL} \\ G(x, p, res, t) &\equiv x \mapsto \text{tree}(p, \mathcal{D}_1, r, t) \wedge res = x \wedge r = \text{NULL} \\ &\quad \vee x \mapsto \text{tree}(p, l, r, \mathcal{D}_n) * G(l, x, res, lm) * G(r, x, lm, t) \wedge r \neq \text{NULL} \end{aligned}$$

The variables  $\mathcal{D}_1$ ,  $\mathcal{D}_p$  and  $\mathcal{D}_n$  correspond to *dangling predicates*. Note that for memory safety, the input tree  $x$  must contain at least one node and that  $x \rightarrow l$  must be non-null when  $x \rightarrow r$  is non-null. These requirements are captured by our synthesized pre-predicate. Compared to some prior shape analyses, such as [12,16], which requires the entire program to be available for analysis, our approach can perform this task modularly on a per method basis instead.

Our approach currently works only for shape abstractions of tree-like data structures with forward and back pointers. (We are unable to infer specifications for graph-like or overlaid data structures yet.) These abstractions are being inferred *modularly* on a per method basis. The inferred preconditions are typically the weakest ones that would ensure memory safety, and would be applicable to all contexts of use. Furthermore, the normalization step aims to ensure concise and re-useable predicate definitions. We shall next elaborate and formalise on our second-order bi-abduction process.

## 4 Second-Order Bi-Abduction with an Annotation Scheme

We have seen the need for a bi-abductive entailment procedure to systematically handle unknown predicates. To cater to predicates with multiple parameters, we shall use an automatic *#-annotation* scheme to support both unfolding and folding of unknown predicates. Consider a predicate  $U(v_1, \dots, v_n, w_1\#, \dots, w_m\#)$ , where parameters  $v_1, \dots, v_n$  are unannotated and parameters  $w_1, \dots, w_m$  are *#-annotated*. From the perspective of unfolding, we permit each variable from  $v_1, \dots, v_n$  to be instantiated at most once (we call them *instantiatable*), while variables  $w_1, \dots, w_m$  are *disallowed* from instantiation (we call them *non-instantiatable*). This scheme ensures that each pointer is instantiated at most once, and avoids formulae, like  $U_3(y, y)$  or  $U_2(x, y) * U_3(y, x\#)$ , from being formed. Such formulae, where a variable may be repeatedly instantiated, may cause a

trivial FALSE pre-condition to be inferred. Though sound, it is imprecise. From the perspective of folding, we allow heap traversals to start from variables  $v_1, \dots, v_n$  and would stop whenever references to  $w_1, \dots, w_m$  are encountered. This allows us to properly infer segmented shape predicates and back pointers. Our annotation scheme is fully automated, as we would infer the #-annotation of pre-predicates based on which parameters could be field accessed; while parameters of post-predicates are left unannotated. For our running example, since  $q$  parameter is not field accessed (in its method's body), our automatic annotation scheme would start with the following pre/post specification:

$$\text{requires } H(x, q\#) \quad \text{ensures } G(x, q)$$

**Unfold.** The entailment below results in an unfolding of the unknown  $H$  predicate. It is essentially (E2) in Sec 3, except that  $q$  is marked explicitly as non-instantiatable.

$$H(x, q\#) \wedge x \neq \text{NULL} \vdash x \mapsto \text{node}(x_p, x_n) \rightsquigarrow (A2, \Delta_1) \quad (E2')$$

With non-instantiatable variables explicitly annotated, the assumption (A2) becomes:

$$A2 \equiv H(x, q\#) \wedge x \neq \text{NULL} \Rightarrow x \mapsto \text{node}(x_p, x_n) * H_p(x_p, q\#) * H_n(x_n, q\#)$$

As mentioned earlier, we generated a new unknown predicate for each pointer field ( $H_p$  for  $x_p$ , and  $H_n$  for  $x_n$ ), so as to allow the full recovery of the shape of the data structure being traversed or built. Note that each  $x, x_p, x_n$  appears only once in unannotated forms, while the annotated  $q\#$  remains annotated throughout to prevent the pointer from being instantiated. If we allow  $q$  to be instantiatable in (E2') above, we will instead obtain:

$$H(x, q) \wedge x \neq \text{NULL} \vdash x \mapsto \text{node}(x_p, x_n) \rightsquigarrow (A2', \Delta_1')$$

We get  $A2' \equiv H(x, q) \wedge x \neq \text{NULL} \Rightarrow x \mapsto \text{node}(x_p, x_n) * H_p(x_p, q) * H_n(x_n, q) * U_2(q, x\#)$ , where the unfolding process creates extra unknown predicate  $U_2(q, x\#)$  to capture shape for  $q$ .

Our proposal for instantiating unknown predicates is also applicable when known predicates appear in the RHS. These known predicates may have parameters that act as *continuation fields* for the data structure. An example is the list segment  $\text{lseg}(x, p)$  predicate where the parameter  $p$  is a continuation field.

$$\begin{aligned} \text{ll}(x) &\equiv \text{emp} \wedge x = \text{NULL} \vee x \mapsto \text{snode}(n) * \text{ll}(n) \\ \text{lseg}(x, p) &\equiv \text{emp} \wedge x = p \vee x \mapsto \text{snode}(n) * \text{lseg}(n, p) \end{aligned}$$

Where  $\text{snode}$  (defined in the previous section) denotes singly-linked list node. Note that continuation fields play the same role as fields for data nodes. Therefore, for such parameters, we also generate new unknown parameters to capture the connected data structure that may have been traversed. We illustrate this with two examples:

$$U(x) \vdash \text{ll}(x) \rightsquigarrow (U(x) \Rightarrow \text{ll}(x), \text{emp}) \quad U(x) \vdash \text{lseg}(x, p) \rightsquigarrow (U(x) \Rightarrow \text{lseg}(x, q) * U_2(q), U_2(p))$$

The first predicate  $\text{ll}(x)$  did not have a continuation field. Hence, we did not generate any extra unknown predicate. The second predicate  $\text{lseg}(x, p)$  did have a continuation field  $p$ , and we generated an extra unknown predicate  $U_2(p)$  to capture a possible extension of the data structure beyond this continuation field.

**Fold.** A second scenario that must be handled by second-order entailment involves unknown predicates in the consequent. For each unknown predicate  $U_1(\bar{v}, \bar{w}\#)$  in the consequent, a corresponding assumption  $\Delta \Rightarrow U_1(\bar{v}, \bar{w}\#) \text{ @ } \Delta_g$  is inferred where  $\Delta$  contains unknown predicates with at least one instantiatable parameters from  $\bar{v}$ , or heaps *reachable* from  $\bar{v}$  (via either any data fields or parameters of known predicates) but stopping at non-instantiatable variables  $\bar{w}\#$ ; a residual frame is also inferred from the antecedent (but added with pure approximation of footprint heaps [9]). For example, consider the following entailment:



$$x \mapsto \text{snode}(q) * q \mapsto \text{snode}(\text{NULL}) \wedge q \neq \text{NULL} \vdash U_1(x, q\#) \rightsquigarrow (A_{f1}, \Delta_1)$$

The output of this entailment is:

$$A_{f1} \equiv x \mapsto \text{snode}(q) \wedge q \neq \text{NULL} \Rightarrow U_1(x, q\#) \quad \Delta_1 \equiv q \mapsto \text{snode}(\text{NULL}) \wedge x \neq \text{NULL} \wedge x \neq q$$

As a comparison, let us consider the scenario where  $q$  is unannotated, as follows:

$$x \mapsto \text{snode}(q) * q \mapsto \text{snode}(\text{NULL}) \wedge q \neq \text{NULL} \vdash U_1(x, q) \rightsquigarrow (A_{f2}, \Delta_2)$$

In this case, the output of the entailment becomes:

$$A_{f2} \equiv x \mapsto \text{snode}(q) * q \mapsto \text{snode}(\text{NULL}) \Rightarrow U_1(x, q) \quad \Delta_2 \equiv x \neq \text{NULL} \wedge q \neq \text{NULL} \wedge x \neq q$$

Moreover, the folding process also captures *known* heaps that are reachable from  $\#$ -parameters as *heap guard conditions*, e.g.  $x \mapsto \text{node}(q, x_n)$  in our running example (E3):

$$\begin{aligned} & x \mapsto \text{node}(q, x_n) * H_p(x_p, q\#) * H_n(x_n, q\#) \wedge x \neq \text{NULL} \vdash H(x_n, x\#) \\ & \rightsquigarrow (H_n(x_n, q\#) \Rightarrow H(x_n, x\#)) \text{ @ } x \mapsto \text{node}(q, x_n), x \mapsto \text{node}(q, x_n) * H_p(x_p, q\#) \wedge x \neq \text{NULL} \quad (\text{E3}') \end{aligned}$$

Such heap guards help with capturing the relations of heap structures and recovering those relationships when necessary (e.g. back-pointer  $x\#$ ).

**Formalism.** Bi-abductive unfold is formalized in Fig. 3. Here,  $\text{slice}(\bar{w}, \pi)$  is an auxiliary function that existentially quantifies in  $\pi$  all free variables that are not in the set  $\bar{w}$ .

$$\begin{array}{l} \boxed{\text{SO-ENT-UNFOLD}} \\ \kappa_s \equiv r \mapsto c(\bar{p}) \text{ or } \kappa_s \equiv P(r, \bar{p}) \\ \kappa_{\text{fields}} = *_{p_j \in \bar{p}} U_j(p_j, \bar{v}_i\#, \bar{v}_n\#), \text{ where } U_j: \text{fresh preds} \\ \kappa_{\text{rem}} = U_{\text{rem}}(\bar{v}_i, \bar{v}_n\#, r\#), \text{ where } U_{\text{rem}}: \text{a fresh pred} \\ \pi_a = \text{slice}(\{r, \bar{v}_i, \bar{v}_n, \bar{p}\}, \pi_1) \quad \pi_c = \text{slice}(\{\bar{p}\}, \pi_2) \\ \sigma \equiv (U(r, \bar{v}_i, \bar{v}_n\#) \wedge \pi_a \Rightarrow \kappa_s * \kappa_{\text{fields}} * \kappa_{\text{rem}} \wedge \pi_c) \\ \kappa_1 * \kappa_{\text{fields}} * \kappa_{\text{rem}} \wedge \pi_1 \vdash \kappa_2 \wedge \pi_2 \rightsquigarrow (\mathcal{R}, \Delta_R) \\ \hline U(r, \bar{v}_i, \bar{v}_n\#) * \kappa_1 \wedge \pi_1 \vdash \kappa_s * \kappa_2 \wedge \pi_2 \rightsquigarrow (\sigma \wedge \mathcal{R}, \Delta_R) \end{array}$$

**Fig. 3.** Bi-Abductive Unfolding.

Thus it eliminates from  $\pi$  all subformulas not related to  $\bar{w}$  (e.g.  $\text{slice}(\{x, q\}, q = \text{NULL} \wedge y > 3)$  returns  $q = \text{NULL}$ ). In the first line, a RHS assertion, either a points-to assertion  $r \mapsto c(\bar{p})$  or a known predicate instance  $P(r, \bar{p})$  is paired through the parameter  $r$  with the unknown predicate  $U$ .

Second, the unknown predicates  $U_j$  are generated for the data fields/parameters of  $\kappa_s$ . Third, the unknown predicate  $U_{\text{rem}}$  is generated for the instantiatable parameters  $\bar{v}_i$  of  $U$ . The fourth and fifth lines compute relevant pure formulas and generate the assumption, respectively. Finally, the unknown predicates  $\kappa_{\text{fields}}$  and  $\kappa_{\text{rem}}$  are combined in the residue of LHS to continue discharging the remaining formula in RHS.

Bi-abductive fold is formalized in Fig. 4. The function  $\text{reach}(\bar{w}, \kappa_1 \wedge \pi_1, \bar{z}\#)$  extracts portions from the antecedent heap ( $\kappa_1$ ) that are (1) unknown predicates containing at least one instantiatable parameter from  $\bar{w}$ ; or (2) point-to or known predicates reachable from  $\bar{w}$ , but not reachable from  $\bar{z}$ . In our running example (the entailment (E3') on last page), the function  $\text{reach}(\{x_n\}, x \mapsto \text{node}(q, x_n) * H_p(x_p, q\#) * H_n(x_n, q\#) \wedge x \neq \text{NULL}, \{x\#})$  is used to obtain  $H_n(x_n, q\#)$ . More detail on this function is in the appendix. The  $\text{heaps}(\Delta)$  function enumerates all known predicate instances (of the form  $P(\bar{v})$ ) and points-to instances (of the form  $r \mapsto c(\bar{v})$ ) in  $\Delta$ . The function  $\text{root}(\kappa)$  is defined as:  $\text{root}(r \mapsto c(\bar{v})) = \{r\}$ ,  $\text{root}(P(r, \bar{v})) = \{r\}$ . In the first line, heaps of LHS are separated into the assumption

$$\begin{array}{l} \boxed{\text{SO-ENT-FOLD}} \\ \kappa_{11} = \text{reach}(\bar{w}, \kappa_1 \wedge \pi_1, \bar{z}\#) \quad \exists \kappa_{12} \cdot \kappa_1 = \kappa_{11} * \kappa_{12} \\ \kappa_g = * \{ \kappa \mid \kappa \in \text{heaps}(\kappa_{12}) \wedge \text{root}(\kappa) \subseteq \bar{z}\# \} \quad \bar{r} = \bigcup_{\kappa \in \kappa_g} \text{root}(\kappa) \\ \sigma \equiv (\kappa_{11} \wedge \text{slice}(\bar{w}, \pi_1) \Rightarrow U_c(\bar{w}, \bar{z}\#) \text{ @ } \kappa_g \wedge \text{slice}(\bar{r}, \pi_1)) \\ \kappa_{12} \wedge \pi_1 \vdash \kappa_2 \wedge \pi_2 \rightsquigarrow (\mathcal{R}, \Delta_R) \\ \hline \kappa_1 \wedge \pi_1 \vdash U_c(\bar{w}, \bar{z}\#) * \kappa_2 \wedge \pi_2 \rightsquigarrow (\sigma \wedge \mathcal{R}, \Delta_R) \end{array}$$

**Fig. 4.** Bi-Abductive Folding.

$\kappa_{11}$  and the residue  $\kappa_{12}$ . Second, heap guards (and their root pointers) are inferred based on  $\kappa_{12}$  and the  $\#$ -annotated parameters

$\bar{z}$ . The assumption is generated in the third line and finally, the residual heap is

used to discharge the remaining heaps of RHS.

**Hoare Rules.** We shall now present Hoare rules to show how second-order entailment is used there. For simplicity, we consider a core imperative language (Fig. 5) that supports heap-based data structures (*datat*) and methods (*meth*).

$\begin{aligned} \text{Prog} &::= \text{datat}^* \text{meth}^* & \text{datat} &::= \text{data } c \{ \text{field}^* \} \\ \text{field} &::= t \ v \ t & t &::= \text{int} \mid \text{bool} \mid \text{void} \mid c \mid \dots \\ \text{meth} &::= t \ mn \ ((\text{ref} \ t \ v)^*) & \Phi_{pr} \ \Phi_{po}; \ \{e\} \\ e &::= \text{NULL} \mid k^\tau \mid v \mid v.f \mid v=e \mid v.f=e \mid \text{new } c(v^*) \\ & & & \mid e_1; e_2 \mid t \ v; \ e \mid mn(v^*) \mid \text{if } v \ \text{then } e_1 \ \text{else } e_2 \end{aligned}$	<p>A method declaration includes a header with pre-/post-condition and its body. Methods can have call-by-reference parameters (prefixed with <code>ref</code>). Loops, including nested loops, are transformed to tail-recursive methods</p>
---	---

**Fig. 5.** The Core Language

with `ref` parameters to capture mutable variables. To support shape analysis, code verification is formalized as a proof of quadruple:  $\vdash \{ \Delta_{pre} \} e \{ \mathcal{R}, \Delta_{post} \}$ , where  $\mathcal{R}$  accumulates the set of relational assumptions generated by the entailment procedure. The specification may contain unknown predicates in preconditions and postconditions. We list in Fig. 6 the rules for field access, method calls and method declaration. Note that primed variable (e.g.  $x'$ ) denotes the latest value (of the program variable  $x$ ). The formula  $\Delta_1 *_v \Delta_2$  denotes  $\exists \bar{x}. ([\bar{x}/\bar{v}'] \Delta_1) * ([\bar{x}/\bar{v}] \Delta_2)$  (see [9]).

$$\begin{array}{c}
\frac{
\begin{array}{c}
\text{[SA-CALL]} \\
t_0 \ mn \ ((\text{ref } t_i \ v_i)_{i=1}^{m-1}, (t_j \ v_j)_{j=m}^n) \ \Phi_{pr} \ \Phi_{po}; \ \{e\} \in \text{Prog} \\
\rho = [v'_k/v_k]_{k=1}^n \ \Phi'_{pr} = \rho(\Phi_{pr}) \ \bar{w} = \{v_1, \dots, v_{m-1}\} \ \bar{v} = \{v_m, \dots, v_n\} \\
\Delta \vdash \Phi'_{pr} \rightsquigarrow (\mathcal{R}, \Delta_2) \ \Delta_3 = (\Delta_2 \wedge \bigwedge_{i=m}^n (v'_i = v_i)) *_v \cup \bar{w} \ \Phi_{po}
\end{array}
}{
\vdash \{ \Delta \} \ mn(v_1, \dots, v_{m-1}, v_m, \dots, v_n) \ \{ \mathcal{R}, \Delta_3 \}
} \\
\frac{
\begin{array}{c}
\text{[SA-FLD-RD]} \\
\text{data } c \ \{ t_1 \ f_1, \dots, t_n \ f_n \} \in \text{Prog} \\
\Delta_1 \vdash x' \mapsto c(v_1..v_n) \rightsquigarrow (\mathcal{R}, \Delta_3) \\
\Delta_4 = \exists v_1..v_n. (\Delta_3 * x' \mapsto c(v_1..v_n) \wedge \text{res} = v_i)
\end{array}
}{
\vdash \{ \Delta_1 \} \ x.f_i \ \{ \mathcal{R}, \Delta_4 \}
} \\
\frac{
\begin{array}{c}
\text{[SA-METH]} \\
\vdash \{ \Phi_{pr} \wedge \bigwedge (u' = u)^* \} e \ \{ \mathcal{R}_1, \Delta_1 \} \\
\Delta_1 \vdash \Phi_{po} \rightsquigarrow (\mathcal{R}_2, \Delta_2) \\
\Gamma = \text{solve}(\mathcal{R}_1 \cup \mathcal{R}_2)
\end{array}
}{
t_0 \ mn \ ((t \ u)^*) \ \Phi_{pr} \ \Phi_{po} \ \{e\}
}
\end{array}$$

**Fig. 6.** Several Hoare Rules

The key outcome is that if a solution for the set of relational assumptions  $\mathcal{R}$  can be found, the program is memory-safe and all the methods abide by their specifications. Furthermore, we propose a bottom-up verification process which is able to incrementally build suitable predicate instantiations one method at a time by solving the collected relational assumptions  $\mathcal{R}$  progressively. The predicate definition synthesis (`solve`) consists of two separate operations: predicate synthesis, `PRED_SYN`, and predicate normalization, `PRED_NORM`. That is `solve`( $\mathcal{R}$ ) = `PRED_NORM`(`PRED_SYN`( $\mathcal{R}$ )). After the method is successfully verified, the resulting predicate definitions  $\Gamma$  provide an interpretation for the unknown predicates appearing in the specifications such that memory safety is guaranteed. By returning  $\Gamma$ , the method verification allows the inferred definitions and specifications to be consistently reused in the verification of the remaining methods.

## 5 Derivation of Shape Predicates

Once the relational assumptions have been inferred, we proceed to apply a series of refinement steps to derive predicate definitions for each pre- and post-predicate. Fig. 7

```

function PRED_SYN( $\mathcal{R}$ )
   $\Gamma \leftarrow \emptyset$ 
   $\mathcal{R} \leftarrow$  exhaustively apply  $[\text{syn-base}]$  on  $\mathcal{R}$ 
   $\mathcal{R}_{\text{pre}}, \mathcal{R}_{\text{post}} \leftarrow \text{sort-group}(\mathcal{R})$ 
  while  $\mathcal{R}_{\text{pre}} \neq \emptyset$  do
     $\mathcal{U}^{\text{pre}}, \sigma \leftarrow$  pick unknown & assumption in  $\mathcal{R}_{\text{pre}}$ 
     $\mathcal{U}_{\text{def}}^{\text{pre}} \leftarrow$  apply  $[\text{syn-case}]$ ,  $[\text{syn-group-pre}]$ , and
       $[\text{syn-pre-def}]$  on  $\sigma$ 
     $\mathcal{R}_{\text{pre}}, \mathcal{R}_{\text{post}} \leftarrow$  inline  $\mathcal{U}_{\text{def}}^{\text{pre}}$  in  $(\mathcal{R}_{\text{pre}} \setminus \sigma)$ ,  $\mathcal{R}_{\text{post}}$ 
    discharge  $\mathcal{U}^{\text{pre}}$  obligations
     $\Gamma \leftarrow \Gamma \cup \{\mathcal{U}_{\text{def}}^{\text{pre}}\}$ 
  end while
  while  $\mathcal{R}_{\text{post}} \neq \emptyset$  do
     $\mathcal{U}^{\text{post}}, \sigma \leftarrow$  pick unknown & assumption in  $\mathcal{R}_{\text{post}}$ 
     $\mathcal{U}_{\text{def}}^{\text{post}} \leftarrow$  apply  $[\text{syn-group-post}]$ ,  $[\text{syn-post-def}]$  on  $\sigma$ 
    discharge  $\mathcal{U}^{\text{post}}$  obligations
     $\mathcal{R}_{\text{post}} \leftarrow \mathcal{R}_{\text{post}} \setminus \sigma$      $\Gamma \leftarrow \Gamma \cup \{\mathcal{U}_{\text{def}}^{\text{post}}\}$ 
  end while
  return  $\Gamma$ 
end function

```

Fig. 7. Shape Derivation Outline

post-predicate has been derived; (iii) *inline* to unfold synthesized predicates in the remaining assumptions.

### 5.1 Base Splitting of Pre/Post-Predicates

We first deal with relational assumptions of the form  $\mathcal{U}^{\text{pre}}(\dots) * \Delta \Rightarrow \mathcal{U}^{\text{post}}(\dots)$ , which capture constraints on both a pre-predicate and a post-predicate. To allow greater flexibility in applying specialized techniques for pre-predicates or post-predicates, we split the assumption into two assumptions such that pre-predicate  $\mathcal{U}^{\text{pre}}$  is separated from post-predicate  $\mathcal{U}^{\text{post}}$ . Base splitting can be formalized as follows:

$$\begin{array}{c}
 \begin{array}{l}
 \sigma : \mathcal{U}^{\text{pre}}(\bar{x}) * \kappa \wedge \pi \Rightarrow \mathcal{U}^{\text{post}}(\bar{y}) \quad \sigma_1 : \mathcal{U}^{\text{pre}}(\bar{x}) \wedge \text{slice}(\bar{x}, \pi) \Rightarrow \text{emp} \quad \sigma_2 : \kappa \wedge \pi \Rightarrow \mathcal{U}^{\text{post}}(\bar{y}) \\
 \kappa_g = * \{ \kappa_1 \mid \kappa_1 \in \text{heaps}(\kappa) \wedge \text{pars}(\kappa_1) \cap \bar{x} \neq \emptyset \} \quad \bar{w} = \bigcup \{ \text{pars}(\kappa_1) \mid \kappa_1 \in \kappa_g \} \\
 \sigma_3 : \mathcal{U}^{\text{pre}}(\bar{x}) \Rightarrow \mathcal{U}^{\text{fr}}(\bar{x}) \ @ \ \kappa_g \wedge \text{slice}(\bar{x} \cup \bar{w}, \pi) \quad \sigma_4 : \mathcal{U}^{\text{fr}}(\bar{x}) \Rightarrow \top
 \end{array} \\
 \hline
 \text{if is\_base}(\bar{x}, \pi) = \text{true then } (\sigma_1 \wedge \sigma_2) \text{ else } (\sigma \wedge \sigma_3 \wedge \sigma_4)
 \end{array}$$

The premise contains an assumption ( $\sigma$ ) which could be split. The conclusion captures the new relational assumptions. There are two scenarios:

(1) The first scenario takes place when the test  $\text{is\_base}(\bar{x}, \pi)$  holds. It signifies that  $\pi$  contains a base case formula for some pointer(s) in  $\bar{x}$ . Note that  $\text{is\_base}(\bar{x}, \pi)$  holds if and only if  $(\exists v \in \bar{x}. \pi \vdash v = \text{NULL})$  or  $(\exists v_1, v_2 \in \bar{x}. \pi \vdash v_1 = v_2)$ . In such a situation, the assumption  $\sigma$  is split into  $\sigma_1$  and  $\sigma_2$ . This reflects the observation that a pre-predicate

outlines our strategy for predicate synthesis. We use the  $[\text{syn-*}]$  notation to name refinement rules. For space reasons, we describe some rules and leave the rest to the appendix B. Steps that are left out include: (i) *sort-group* to decide on the transformation order of relational assumptions; (ii)  $[\text{syn-Pre/Post-Obi}]$  to process some relational assumptions as proof obligations. For example, if the result of the recursive method is field-accessed after the recursive call, the post-predicate would appear as an unknown predicate for heap instantiation. This must be processed as an entailment obligation, after the definition of its

guard will likely constrain the pre-predicate to a base-case with empty heap. This scenario happens in our running example where the assumption (A1) is split to:

$$(A1a). H(x, q) \wedge x=NULL \Rightarrow \text{emp} \quad (A1b). \text{emp} \wedge x=NULL \Rightarrow G(x, q)$$

(2) If the test  $\text{is\_base}(\bar{x}, \pi)$  fails, there is no base case information available for us to instantiate  $U^{\text{pre}}(\bar{x})$ . The assumption  $\sigma$  is not split and kept in the result. To have a more precise derivation, we would also record the fact that  $U^{\text{pre}}(\bar{x})$  has no instantiation under the current context. To do this, in the second line we record in  $\kappa_g$  such a heap context (related to  $\bar{x}$ ), extract in  $\bar{w}$  related pointers from the context, and introduce a fresh unknown predicate  $U^{\text{tr}}$  as the instantiation for  $U^{\text{pre}}$ , as indicated by the assumption  $\sigma_3$  in the third line. Note the heap guard specifies the context under which such an assumption holds. We also add  $\sigma_4$  into the result, where the new predicate  $U^{\text{tr}}$  is instantiated to the aforementioned memory locations (encapsulated by  $\top$ ). Assumptions of the form  $U^{\text{tr}}(p) \Rightarrow \top$  are being used to denote dangling pointers. We also note that introducing the dangling predicate  $U^{\text{tr}}$  into the guarded assumption  $\sigma_3$  is essential to help relate non-traversed pointer fields between the pre-predicate  $U^{\text{pre}}$  and the post-predicate  $U^{\text{post}}$ . The function  $\text{pars}(\kappa)$  (the 2nd line) retrieves parameters:  $\text{pars}(r \mapsto c(\bar{v})) = \bar{v}$ ,  $\text{pars}(P(r, \bar{v})) = \bar{v}$ .

As an example, consider splitting  $(\sigma_5) : U^{\text{pre}}(p) * x \mapsto \text{node}(p, n) \wedge n=NULL \Rightarrow U^{\text{post}}(x)$ . The test  $\text{is\_base}(\{p\}, n=NULL)$  fails. In addition to  $(\sigma_5)$ , the splitting returns also

$$(\sigma_6) : U^{\text{pre}}(p) \Rightarrow U^{\text{tr}}(p) \ @ \ (x \mapsto \text{node}(p, n) \wedge n=NULL) \quad (\sigma_7) : U^{\text{tr}}(p) \Rightarrow \top$$

For the `t11` example in Sec 3, the `[syn-base]` transformation can be applied to assumption (4) yielding the following three new assumptions:

$$\begin{aligned} (4a) \text{res} \mapsto \text{tree}(p, l, r, t) * H_1(l, p, t) \wedge r=NULL \wedge \text{res}=x &\Rightarrow G(x, p, \text{res}, t) \\ (4b) H_r(r, p, t) \wedge r=NULL &\Rightarrow \text{emp} \\ (4c) H_1(l, p, t) &\Rightarrow H_1^f(l, p, t) \ @ \ (\text{res} \mapsto \text{tree}(p, l, r, t) \wedge r=NULL) \\ (4d) H_1^f(l, p, t) &\Rightarrow \top \end{aligned}$$

Pre-predicate  $H_r$  captures  $r=NULL$  as its base-case split. Pre-predicate  $H_1$  uses a heap guard for its base context, and  $\top$  to denote an un-accessed dangling heap residue encapsulated in  $H_1^f$ .

## 5.2 Deriving Pre-Predicates

Pre-predicates typically appear in relational assumptions under pure guards  $\pi$ , of the form  $U^{\text{pre}}(\dots) \wedge \pi \Rightarrow \Delta$ . To derive definitions for these pre-predicates, the first step is to transform relational assumptions that overlap on their guards by forcing a case analysis that generates a set of relational assumptions with disjoint guard conditions:

$$\frac{\begin{array}{c} \text{[syn-case]} \\ U(\bar{x}) \wedge \pi_1 \Rightarrow \Delta_1 \ @ \ \Delta_{1g} \quad U(\bar{x}) \wedge \pi_2 \Rightarrow \Delta_2 \ @ \ \Delta_{2g} \quad \pi_1 \wedge \pi_2 \not\Rightarrow \text{FALSE} \\ \Delta_1 \wedge \Delta_2 \Rightarrow_{\bar{x}} \Delta_3 \quad \Delta_{1g} \wedge \Delta_{2g} \Rightarrow_{\bar{x}} \Delta_{3g} \quad \text{SAT}(\Delta_{3g}) \end{array}}{U(\bar{x}) \wedge \pi_1 \wedge \neg \pi_2 \Rightarrow \Delta_1 \ @ \ \Delta_{3g} \quad U(\bar{x}) \wedge \pi_2 \wedge \neg \pi_1 \Rightarrow \Delta_2 \ @ \ \Delta_{3g} \quad U(\bar{x}) \wedge \pi_1 \wedge \pi_2 \Rightarrow \Delta_3 \ @ \ \Delta_{3g}}$$

For brevity, we assume a renaming of free variables to allow  $\bar{x}$  to be used as arguments in both assumptions. Furthermore, we use the  $\Rightarrow_{\bar{x}}^{\wedge}$  operator to denote a normalization of overlapping conjunction,  $\Delta_1 \wedge \Delta_2$  [27]. Informally, in order for  $\Delta_1 \wedge \Delta_2$  to

hold, it is necessary that the shapes described by  $\Delta_1$  and  $\Delta_2$  agree when describing the same memory locations. Normalization thus determines the overlapping locations,  $\Delta_c$  such that  $\Delta_1 = \Delta_c * \Delta'_1$  and  $\Delta_2 = \Delta_c * \Delta'_2$  and returns  $\Delta_c * \Delta'_1 * \Delta'_2$ . We leave a formal definition of  $\Rightarrow_{\bar{x}}$  to the appendix B.5. Once all the relational assumptions for a given pre-predicate have been transformed such that the pure guards do not overlap, we may proceed to combine them using the rule  $\boxed{\text{syn-group-pre}}$  shown below. We shall perform this exhaustively until a single relational assumption for  $\mathbb{U}$  is derived. If the assumption RHS is independent of any post-predicate, it becomes the unknown pre-predicate definition, as shown in the rule  $\boxed{\text{syn-pre-def}}$  below.

$$\frac{\mathbb{U}(\bar{x}) \wedge \pi_1 \Rightarrow \Phi_1^g \quad \boxed{\text{syn-group-pre}} \quad \mathbb{U}(\bar{x}) \wedge \pi_2 \Rightarrow \Phi_2^g \quad \pi_1 \wedge \pi_2 \Rightarrow \text{FALSE}}{\mathbb{U}(\bar{x}) \wedge (\pi_1 \vee \pi_2) \Rightarrow \Phi_1^g \wedge \pi_1 \vee \Phi_2^g \wedge \pi_2} \quad \frac{\boxed{\text{syn-pre-def}} \quad \mathbb{U}^{\text{pre}}(\bar{x}) \Rightarrow \Phi^g \quad \text{no\_post}(\Phi^g)}{\mathbb{U}^{\text{pre}}(\bar{x}) \equiv \Phi^g}$$

For the `s112d11` example, applying the  $\boxed{\text{syn-group-pre}}$  rule to (A2) and (A1a) yields:

$$(A5). \text{H}(\mathbf{x}, \mathbf{q}) \Rightarrow \mathbf{x} \mapsto \text{node}(\mathbf{x}_p, \mathbf{x}_n) * \text{H}_p(\mathbf{x}_p, \mathbf{q}) * \text{H}_n(\mathbf{x}_n, \mathbf{q}) \vee \text{emp} \wedge \mathbf{x} = \text{NULL}$$

For the `t11` example, by the above rules, (2) and (4b) yield:

$$(6). \text{H}_r(\mathbf{r}, \mathbf{p}, \mathbf{t}) \equiv \text{H}(\mathbf{r}, \mathbf{x}, \mathbf{t}) \wedge \mathbf{r} \neq \text{NULL} \ @ \ \mathbf{x} \mapsto \text{tree}(\mathbf{p}, \mathbf{l}, \mathbf{r}, \mathbf{n}) \vee \text{emp} \wedge \mathbf{r} = \text{NULL}$$

Similarly, (3) and (4c) derives  $\text{H}_1$ :

$$(7). \text{H}_1(\mathbf{l}, \mathbf{p}, \mathbf{t}) \equiv \text{H}(\mathbf{l}, \mathbf{x}, \mathbf{l}_m) \ @ \ \mathbf{x} \mapsto \text{tree}(\mathbf{p}, \mathbf{l}, \mathbf{r}, \mathbf{n}) \wedge \mathbf{r} \neq \text{NULL} \\ \vee \text{H}_1^t(\mathbf{l}, \mathbf{p}, \mathbf{t}) \ @ \ \mathbf{x} \mapsto \text{tree}(\mathbf{p}, \mathbf{l}, \mathbf{r}, \mathbf{n}) \wedge \mathbf{r} = \text{NULL}$$

This is then trivially converted into a definition for its pre-predicate, without any weakening, thus ensuring soundness of our pre-conditions.

### 5.3 Deriving Post-Predicates

We start the derivation for a post-predicate after all pre-predicates have been derived. We can incrementally group each pair of relational assumptions on a post-predicate via the  $\boxed{\text{syn-group-post}}$  rule shown below. By exhaustively applying  $\boxed{\text{syn-group-post}}$  rule all assumptions relating to predicate  $\mathbb{U}^{\text{post}}$  get condensed into an assumption of the form:  $\Delta_1 \vee \dots \vee \Delta_n \Rightarrow \mathbb{U}^{\text{post}}(\bar{x})$ . This may then be used to confirm the post-predicate by generating the predicate definition via the  $\boxed{\text{syn-post-def}}$  rule.

$$\frac{\boxed{\text{syn-group-post}} \quad \Delta_a \Rightarrow \mathbb{U}^{\text{post}}(\bar{x}) \quad \Delta_b \Rightarrow \mathbb{U}^{\text{post}}(\bar{x})}{\Delta_a \vee \Delta_b \Rightarrow \mathbb{U}^{\text{post}}(\bar{x})} \quad \frac{\boxed{\text{syn-post-def}} \quad \Delta_1 \vee \dots \vee \Delta_n \Rightarrow \mathbb{U}^{\text{post}}(\bar{x})}{\mathbb{U}^{\text{post}}(\bar{x}) \equiv \Delta_1 \vee \dots \vee \Delta_n}$$

Using these rules, we can combine (A4) and (A1b) in the `s112d11` example to obtain:

$$\text{G}(\mathbf{x}, \mathbf{q}) \equiv \text{emp} \wedge \mathbf{x} = \text{NULL} \vee \mathbf{x} \mapsto \text{node}(\mathbf{q}, \mathbf{x}_n) * \text{G}(\mathbf{x}_n, \mathbf{x})$$

Using these rules, we can combine (4a) and (5) in the `t11` example to give:

$$\text{G}(\mathbf{x}, \mathbf{p}, \text{res}, \mathbf{t}) \equiv \mathbf{x} \mapsto \text{tree}(\mathbf{p}, \mathbf{l}, \mathbf{r}, \mathbf{t}) * \text{H}_1(\mathbf{l}, \mathbf{x}_h, \mathbf{t}_h) \wedge \text{res} = \mathbf{x} \wedge \mathbf{r} = \text{NULL} \\ \vee \mathbf{x} \mapsto \text{tree}(\mathbf{p}, \mathbf{l}, \mathbf{r}, \mathbf{n}) * \text{G}(\mathbf{r}, \mathbf{x}, \mathbf{l}_m, \mathbf{t}) * \text{G}(\mathbf{l}, \mathbf{x}, \text{res}, \mathbf{l}_m) * \text{H}_n(\mathbf{n}, \mathbf{x}_n, \mathbf{t}_h) \wedge \mathbf{r} \neq \text{NULL}$$

## 5.4 Predicate Normalization for Concise Definitions

After we have synthesized suitable predicate definitions, we proceed with predicate normalization to convert each predicate definition to its most concise form. Our current method, PRED\_NORM, uses four key steps: (i) eliminate dangling predicates, (ii) eliminate useless parameters, (iii) re-use predicate definitions and (iv) perform predicate splitting. We briefly explain the normalization steps and leave details in the appendix B.7. The first step deals with dangling predicates which do not have any definition. Though it is safe to drop such predicates (by frame rule), our normalization procedure replaces them by special variables, to help capture linking information between pre- and post-conditions. The second step eliminates predicate arguments that are not used in their synthesized definitions, with the help of second-order entailment. The third step leverage on our entailment procedure to conduct an equivalence proof to try to match a newly inferred definition with a definition previously provided or inferred. Lastly, to increase the chance for such predicate reuse, we allow predicates to be split into smaller predicates. This is again done with the help of second-order entailment procedure, allowing us to undertake such normalization tasks soundly and easily.

## 6 Soundness Lemmas and Theorem

Here we briefly state several key soundness results, and leave the proof details to the appendix E. For brevity, we introduce the notation  $\mathcal{R}(\Gamma)$  to denote a set of predicate instantiations  $\Gamma = \{U_1(\bar{v}_1) \equiv \Delta_1, \dots, U_n(\bar{v}_n) \equiv \Delta_n\}$  satisfying the set of assumptions  $\mathcal{R}$ . That is, for all assumptions  $\Delta \Rightarrow \Phi^g \in \mathcal{R}$ , (i)  $\Gamma$  contains a predicate instantiation for each unknown predicate appearing in  $\Delta$  and  $\Phi^g$ ; (ii) by interpreting all unknown predicates according to  $\Gamma$ , then it is provable that  $\Delta$  implies  $\Phi^g$ , written as  $\Gamma : \Delta \vdash \Phi^g$ .

**Soundness of bi-abductive entailment.** Abduction soundness requires that if all the relational assumptions generated are satisfiable, then the entailment is valid.

**Lemma 1.** *Given the entailment judgement  $\Delta_a \vdash \Delta_c \rightsquigarrow (\mathcal{R}, \Delta_f)$ , if there exists  $\Gamma$  such that  $\mathcal{R}(\Gamma)$ , then the entailment  $\Gamma : \Delta_a \vdash \Delta_c * \Delta_f$  holds.*

**Derivation soundness.** For derivation soundness, if a set of predicate definitions is constructed then those definitions must satisfy the initial set of assumptions. We argue that (i) assumption refinement does not introduce spurious instantiations, (ii) the generated predicates satisfy the refined assumptions, (iii) normalization is meaning preserving.

**Lemma 2.** *Given a set of relational assumptions  $\mathcal{R}$ , let  $\mathcal{R}'$  be the set obtained by applying any of the refinement steps, then for any  $\Gamma$  such that  $\mathcal{R}'(\Gamma)$ , we have  $\mathcal{R}(\Gamma)$ .*

**Lemma 3.** *If  $\mathcal{R}$  contains only one pre-assumption on predicate  $U^{\text{pre}}, U^{\text{pre}}(\bar{v}) \Rightarrow \Phi^g$  and if our algorithm returns a solution  $\Gamma$ , then  $(U^{\text{pre}}(\bar{v}) \equiv \Phi^g) \in \Gamma$ . Similarly, if  $\mathcal{R}$  has a sole post-assumption on  $U^{\text{post}}, \Phi \Rightarrow U^{\text{post}}$  and if solution  $\Gamma$  is returned, then  $(U^{\text{post}}(\bar{v}) \equiv \Phi) \in \Gamma$ .*

**Lemma 4.** *Given a set of assumptions  $\mathcal{R}$ , if PRED\_SYN( $\mathcal{R}$ ) returns a solution  $\Gamma$  then  $\mathcal{R}(\Gamma)$ . Furthermore, if PRED\_NORM( $\Gamma$ ) returns a solution  $\Gamma'$  then  $\mathcal{R}(\Gamma')$ .*

**Theorem 6.1 (Soundness)** *If  $\Delta_a \vdash \Delta_c \rightsquigarrow (\mathcal{R}, \Delta_f)$  and  $\Gamma = \text{PRED\_NORM}(\text{PRED\_SYN}(\mathcal{R}))$  then  $\Gamma : \Delta_a \vdash \Delta_c * \Delta_f$ .*

## 7 Implementation and Experimental Results

We have implemented the proposed shape analysis within HIP [9], a separation logic verification system. The resulting verifier, called S2, uses an available CIL-based [26] translator<sup>3</sup> from C to the expression-oriented core language. Our analysis modularly infers the pre/post specification for each method. It attempts to provide the weakest possible precondition to ensure memory safety (from null dereferencing and memory leaks), and the strongest possible post-condition on heap usage patterns, where possible. **Expressivity.** We have explored the generality and efficiency of the proposed analy-

Example	w/o norm.		w/ norm.		Veri.	Example	w/o norm.		w/ norm.		Veri.
	size	Syn.	size	Syn.			size	Syn.	size	Syn.	
SLL (delete)	9	0.23	2	0.29	0.22	CSLL (t)	8	0.22	5	0.23	0.24
SLL (reverse)	20	0.21	8	0.22	0.2	CSLL of CSLLs (c)	18	0.24	4	0.23	0.22
SLL (insert)	13	0.2	11	0.21	0.2	SLL2DLL	18	0.19	2	0.2	0.18
SLL (setTail)	7	0.16	2	0.18	0.16	DLL (check)	8	0.21	2	0.23	0.19
SLL (get-last)	20	0.7	17	0.75	0.21	DLL (append)	11	0.2	8	0.2	0.2
SLL-sorted (c)	11	0.26	2	0.27	0.22	CDLL (c)	23	0.22	8	0.26	0.21
SLL (bubblesort)	13	0.28	9	0.36	0.26	CDLL of 5CSLLs (c)	28	0.39	4	0.66	1.3
SLL (insertsort)	15	0.3	11	0.3	0.27	CDLL of CSLLs <sub>2</sub> (c)	29	0.33	4	0.44	0.29
SLL (zip)	20	0.27	2	0.32	0.24	btree (search)	33	0.23	2	0.24	0.23
SLL-zip-leq	20	0.27	2	0.27	0.25	btree-parent (t)	11	0.23	2	0.29	0.24
SLL + head (c)	12	0.24	2	0.71	0.2	rose-tree (c)	14	0.28	14	0.3	0.23
SLL + tail (c)	10	0.19	2	0.72	0.18	swl (t)	19	0.23	13	0.27	0.22
skip-list <sub>2</sub> (c)	9	0.28	1	0.32	0.25	mcf (c)	19	0.26	17	0.28	0.26
skip-list <sub>3</sub> (c)	9	0.36	1	0.46	0.3	tll (t)	21	0.23	2	0.25	0.21
SLL of 0/1 SLLs	8	0.25	1	0.26	0.23	tll (c)	21	0.29	2	0.32	0.19
CSLL (c)	17	0.18	2	0.23	0.21	tll (set-parent)	39	0.24	2	0.35	0.24

**Table 1.** Experimental Results (**c** for *check* and **t** for *traverse*)

sis through a number of small but challenging examples. We have evaluated programs which manipulate lists, trees and combinations (e.g. `tll`: trees whose leaves are chained in a linked list). The experiments were performed on a machine with the Intel i7-960 (3.2GHz) processor and 16 GB of RAM. Table 1 presents our experimental results. For each test, we list the name of the manipulated data structure and the effect of the verified code under the `Example` column. Here we used SLL, DLL, CLL, CDLL for singly-, doubly-, cyclic-singly-, cyclic-doubly- linked lists. SLL + head/tail for an SLL where each element points to the SLL’s head/tail. SLL of 0/1 SLLs uses an SLL nested in a SLL of size 0 or 1, CSLL of CSLLs for CSLL nested in CSLL, CDLL of 5CSLLs for an CDLL where each node is a source of five CSLL, and CDLL of CSLLs<sub>2</sub> for CDLL where each node is a nested CSLL. The skip lists subscript denotes the number of skip pointers. The `swl` procedure implements list traversal following the DeutschSchorr-Waite style. `rose-trees` are trees with nodes that are allowed to have variable number of children, typically stored as linked lists, and `mcf` trees [16] are rose-tree variants where children are stored in doubly-linked lists with sibling and parent pointers. In order to

<sup>3</sup> Our translation preserves the semantics of source programs, subject to CIL’s limitations.

evaluate the performance of our shape synthesis, we re-verified the source programs against the inferred specifications and listed the verification time (in seconds) in the `Veri.` column and the synthesis times in column `Syn.`. In total, the specification inference took 8.37s while the re-verification<sup>4</sup> took 8.25s.

The experiments showed that our tool can handle fairly complex recursive methods, like trees with linked leaves. It can synthesize shape abstractions for a large variety of data structures; from list and tree variants to combinations. Furthermore, the tool can infer shapes with mutual-recursive definitions, like the `rose-trees` and `mcf` trees.

The normalization phase aims to simplify inferred shape predicates. To evaluate its effectiveness, we performed the synthesis on two scenarios: without (w/o) and with (w/) normalization. The number of conjuncts in the synthesized shapes is captured with `size` column. The results show that normalization is helpful; it reduces by 68% (169/533) the size of synthesized predicates with a time overhead of 27% (8.37s/10.62s).

**Larger Experiments.** We evaluated S2 on real source code from the Glib open source library [1]. Glib is a cross-platform C library including non-GUI code from the GTK+ toolkit and the GNOME desktop environment. We focused our experiments on

	LOC	#Proc	#Loop	#√	Syn. (sec)
<code>gslis.c</code>	698	33	18	47	11.73
<code>glist.c</code>	784	35	19	49	7.43
<code>gtree.c</code>	1204	36	14	44	3.69
<code>gnode.c</code>	1128	37	27	52	16.34

**Fig. 8.** Experiments on Glib Programs

the files which implemented heap data structures, i.e. SLL (`gslis.c`), DLL (`glist.c`), balanced binary trees (`gtree.c`) and N-ary trees (`gnode.c`). In Fig.8 we list for each file number of lines of code (excluding comments) LOC, number of procedures (while/for loops) #Proc (#Loop). #√ describes the number of procedures and loops for which S2 inferred specifications that guarantee memory safety. S2 can infer specifications that guarantee memory safety for 89% of procedures and loops (192/216).<sup>5</sup>

**Limitations.** Our present proposal cannot handle graphs and overlaid data structures since our instantiation mechanism always expands into tree-like data structures with back pointers. This is a key limitation of our approach. For an example, of such a limitation, see appendix F. For future work, we also intend to combine shape analysis with other analyses domains, in order to capture more expressive specifications, beyond memory safety.

## 8 Related Work and Conclusion

A significant body of research has been devoted to shape analysis. Most proposals are orthogonal to our work as they focus on determining shapes based on a fixed set of shape domains. For instance, the analysis in [25] can infer shape and certain numerical properties but is limited to the linked list domain. The analyses from [31,11,4,15,3,23] are tailored to variants of lists and a fixed family of list interleavings. Likewise, Calcagno

<sup>4</sup> Due to our use of sound inference mechanisms, re-verification is not strictly required. We perform it here to illustrate the benefit of integrating inference within a verification framework.

<sup>5</sup> Our current implementation does not support array data structures. Hence, some procedures like `g_tree_insert_internal` cannot be verified.



et al. [7] describes an analysis for determining lock invariants with only linked lists. Lee et al. [21] presents a shape analysis specifically tailored to overlaid data structures. In the matching logic framework, a set of predicates is typically assumed for program verification [30]. The work [2] extends this with specification inference. However, it currently does not deal with the inference of inductive data structure abstractions.

The proposal by Magill et al. [25] is able to infer numerical properties, but it is still parametric in the shape domain. Similarly, the separation logic bi-abduction described in [6,17] assumes a set of built-in or user-defined predicates. Xisa, a tool presented by Rival et. al. [8], works on programs with more varied shapes as long as structural invariant checkers, which play the role of shape definitions, are provided. A later extension [29] also considers shape summaries for procedures with the additional help of global analysis. Other similarly parameterized analysis includes [13]. In comparison, our approach is built upon the foundation of second-order bi-abductive entailment, and is able to infer unknown predicates from scratch or guided by user-supplied assertions. This set-up is therefore highly flexible, as we could support a mix of inference and verification, due to our integration into an existing verification system.

With respect to fully automatic analyses, there are [5], [16] and the Forester system [18]. Although very expressive in terms of the inferred shape classes, the analysis proposed by Guo et al. [16] relies on a heavy formalism and depends wholly on the shape construction patterns being present in the code. They describe a global analysis that requires program slicing techniques to shrink the analyzed code and to avoid noise on the analysis. Furthermore, the soundness of their inference could not be guaranteed; therefore a re-verification of the inferred invariants is required. Brotherston and Goriannis [5] propose a novel way to synthesize inductive predicates by ensuring both memory safety and termination. However, their proposal is currently limited to a simple imperative language without methods. A completely different approach is presented in the Forrester system [18] where a fully automated shape synthesis is described in terms of graph transformations over forest automata. Their approach is based on learning techniques that can discover suitable forest automata by incrementally constructing shape abstractions called boxes. However, their proposal is currently restricted both in terms of the analysed programs, e.g. recursion is not yet supported, and in terms of the inferred shapes, as recursive nested boxes (needed by `τ11`) are not supported.

In the TVLA tradition, [28] describes an interprocedural shape analysis for cut-free programs. The approach explores the interaction between framing and the reachability-based representation. Other approaches to shape analysis include grammar-based inference, e.g. [22] which relies on inferred grammars to define the recursive backbone of the shape predicates. Although [22] is able to handle various types of structures, e.g. trees and dlls, it is limited to structures with only one argument for back pointers. [24] employs inductive logic programming (ILP) to infer recursive pure predicates. While, it might be possible to apply a similar approach to shape inference, there has not yet been any such effort. Furthermore, we believe a targeted approach would be able to easily cater for the more intricate shapes. Since ILP has been shown to effectively synthesize recursive predicates, it would be interesting to explore an integration of ILP with our proposal for inferring recursive predicates of both shape and pure properties. A recent work [14] that aims to automatically construct verification tools has implemented vari-

ous proof rules for reachability and termination properties however it does not focus on the synthesis of shape abstractions. In an orthogonal direction, [10] presents an analysis for constructing precise and compact method summaries. Unfortunately, both these works lack the ability to handle recursive data structures.

**Conclusion** We have presented a novel approach to *modular* shape analysis that can automatically synthesize, from scratch, a set of shape abstractions that are needed for ensuring memory safety. This capability is premised on our decision to build *shape predicate inference* capability directly into a new second-order bi-abductive entailment procedure. Second-order variables are placeholders for unknown predicates that can be synthesized from proof obligations gathered by Hoare-style verification. Thus, the soundness of our inference is based on the soundness of the entailment procedure itself, and is not subjected to a re-verification process. Our proposal for shape analysis has been structured into three key stages: (i) gathering of relational assumptions on unknown shape predicates; (ii) synthesis of predicate definitions via derivation; and (iii) normalization steps to provide concise shape definitions. We have also implemented a prototype of our inference system into an existing verification infrastructure, and have evaluated on a range of examples with complex heap usage patterns.

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## A Second-Order Bi-Abduction: Auxiliary functions

$\text{reach}(S, \kappa \wedge \pi, \mathcal{I})$  “carves” from  $\kappa$  the abstractions of the memory locations reachable from pointer variables in the set  $S$  without traversing variables in  $\mathcal{I}$ . We also locate a set of heap guards to help with the instantiation of parameters  $\bar{w}_{\#}$  that are not included in  $\bar{x}$  but are connected via heap locations. Guard formula  $\kappa_{\bar{g}} \wedge \pi_{\bar{g}}$  is the formula that related to the mismatched RHS but has not been consumed by the assumption. Finally, we have:

$$\text{reach}(S, v \mapsto c(\bar{w}) * \kappa \wedge \pi, \mathcal{I}) = \begin{cases} v \mapsto c(\bar{w}) * \text{reach}(S, \kappa \wedge \pi, \mathcal{I}) & \forall v_i \in \{v\} \cup \bar{w} \cdot \text{ptr}(v_i) \Rightarrow \text{bounded}(v_i, v \mapsto c(\bar{w}) * \kappa \wedge \pi, \mathcal{I}) \\ P(\bar{w}) * \text{reach}(S, \kappa \wedge \pi, \mathcal{I}) & \forall v_i \in \bar{w} \cdot \text{ptr}(v_i) \Rightarrow \text{bounded}(v_i, P(\bar{w}) * \kappa \wedge \pi, \mathcal{I}) \\ \text{emp} & \text{otherwise} \end{cases}$$

Similarly we define  $\text{bounded}(v, \kappa \wedge \pi, \mathcal{I})$  to hold iff the shape pointed to by  $v$  is precisely defined by the frontier  $\mathcal{I}$ . That is, either of the conditions below holds:

1.  $v$  is on the frontier:  $v \in \mathcal{I}$
2.  $v$  is null:  $\pi \Rightarrow v = \text{NULL}$
3.  $v$  is a dangling variable
4.  $v \mapsto c(\bar{w}) \in \kappa$  and  $\forall w_i \in \bar{w} \cdot \text{ptr}(w_i) \wedge \text{bounded}(w_i, \kappa \wedge \pi, \mathcal{I} \cup \{v\})$
5.  $U(\bar{w}) \in \kappa$ ,  $v \in \bar{w}$  and a definition for predicate  $U$  has already been inferred.

## B Further Refinement/Normalization Steps

### B.1 Assumption Sorting and Partitioning

In order to allow for a convenient instantiation order, we sort and group together each set of relational assumptions pertaining to the same predicate, through  $\text{sort-group}(\mathcal{R})$ . Intuitively, the call to  $\text{sort-group}(\mathcal{R})$  will sort assumptions relevant to each predicate according to the following pattern:

1.  $H(\dots) \wedge \pi @ \Delta \Rightarrow H_2(\dots)$
2.  $H(\dots) \Rightarrow H_2(\dots)$
3.  $H(\dots) * \Delta \Rightarrow H_2(\dots)$
4.  $H(\dots) \Rightarrow \Delta$
5.  $H(\dots) \wedge \pi \Rightarrow \Delta$
6.  $\Delta \Rightarrow G(\dots)$

The first four forms could be used directly for in-lining if they are not self-recursive. Guarded assumptions (even if disjunctive but not self-recursive) are given high priority to facilitate their early removal by inlining. The fifth form leads to disjunctive recursive formula, and is not inlined for conciseness reason. As mentioned, post-predicates are only processed after all the pre-predicates have been synthesized. In order to decrease the number of assumptions that need to be considered during the derivation, we will try to partition the assumptions relevant to a predicate into assumptions that will be used in the synthesise process and assumptions that will be treated as proof obligations to be discharged after the predicate synthesis. Note that we order pre-predicates before post-predicates, and would synthesise the simpler predicates, before the more complex ones. Also, the processing of post-obligation for a post-predicate is done after the synthesis of the respective post-predicate. This could generate extra assumptions for other un-synthesised predicates.

### B.2 Obligations for Pre-Predicates

On a similar note, newly created memory locations that were neither abstracted by a pre-predicate nor a post-predicate, could be passed as arguments to a recursive call. One example of an assumption resulting from such a scenario is:

$$x \mapsto \text{snode}(n) * H_n(n) \Rightarrow H(x)$$

Catering for such assumptions follows on a similar scenario as the one for post-predicate obligations: handling will be delayed until a definition for the  $H$  predicate has been synthesized which in turn can be used by our second order bi-abductive entailment to generate constraints for the remaining pre-predicates, e.g.  $H_n$ . That is:

$$\frac{\Delta \Rightarrow U^{\text{pre}}(\bar{v}) \quad \begin{array}{c} \text{[syn-Pre-Obl]} \\ (U^{\text{pre}}(\bar{v}) \equiv \Delta_{U^{\text{pre}}}) \in \Gamma \end{array} \quad \Delta \vdash \Delta_{U^{\text{pre}}} \rightsquigarrow (\mathcal{R}, \Delta_f)}{\mathcal{R}}$$

### B.3 Obligation for Post-Predicates

Memory locations abstracted by post-predicates may be further accessed after a recursive call. This may lead to relational assumptions of the following form.

$$U^{\text{post}}(\dots) \wedge \pi \Rightarrow \Delta$$

We regard this as an obligation that has to be proven, and at the same time it could also be used to infer the definition of unknown post-predicates that were generated. As an example, consider the following post-predicate relational obligation:

$$G(r, x) \wedge x \neq \text{NULL} \Rightarrow r \mapsto \text{snode}(x_n) * G_r(r_n, x\#) * G_x(x, r\#)$$

This obligation introduces two extra unknown post-predicates  $G_r$  and  $G_x$ . In order to synthesize definitions for them, it is possible to leverage on earlier synthesized definitions for  $G$ . Thus, if such a definition exists, say:

$$G(r, x) \equiv x \mapsto \text{snode}(\text{NULL}) \wedge r = x \vee x \mapsto \text{snode}(x_n) * G_x(x_n, r) * r \mapsto \text{snode}(\text{NULL})$$

We can re-use our second-order entailment to prove the earlier post-predicate obligation, which generates the following set of relational assumptions on  $G_r$  and  $G_x$ .

$$\begin{aligned} r = \text{NULL} & \Rightarrow G_r(r, -) \\ x = r \wedge x \neq \text{NULL} & \Rightarrow G_x(x, r) \\ x \mapsto \text{snode}(x_n) * G_x(x_n, r) & \Rightarrow G_x(x, r) \end{aligned}$$

Subjecting them to synthesis for post-predicate yields:

$$\begin{aligned} G_r(r, -) & \equiv r = \text{NULL} \\ G_x(x, r) & \equiv x = r \wedge x \neq \text{NULL} \vee x \mapsto \text{snode}(x_n) * G_x(x_n, r) \end{aligned}$$

More formally,

$$\frac{U^{\text{post}}(\bar{v}) \wedge \pi \Rightarrow \Delta \quad (U^{\text{post}}(\bar{v}) \equiv \Delta_{\text{Upost}}) \in \Gamma \quad \Delta_{\text{Upost}} \wedge \pi \vdash \Delta \rightsquigarrow (\mathcal{R}, \Delta_f)}{\mathcal{R}} \quad [\text{syn-Post-Obl}]$$

### B.4 Predicate Inlining

As derived predicates may contain heap guards, we may remove them by inlining predicate occurrences with the relevant heap context.

$$\frac{\begin{aligned} & U_d^{\text{pre}}(\bar{x}) \equiv \Delta_1 \ @ \ (\kappa_1 \wedge \pi_1) \vee \dots \vee \Delta_n \ @ \ (\kappa_n \wedge \pi_n) \\ & U_d^{\text{pre}}(\bar{x}) \wedge \pi_a \Rightarrow (U_d^{\text{pre}}(\bar{x}) * \kappa \wedge \pi) \ @ \ (\kappa_g \wedge \pi_g) \\ & \mathcal{S}_1 = \{ \Delta_i * \Delta_r * \kappa_i \mid \kappa \wedge \pi \vdash \kappa_i \wedge \pi_i \rightsquigarrow (\emptyset, \Delta_r) \} \\ & \mathcal{S}_2 = \{ \kappa * \Delta_i \wedge \pi \ @ \ (\kappa_i \wedge \pi_i) \mid \text{SAT}(\kappa \wedge \kappa_i \wedge \pi \wedge \pi_i), \kappa \wedge \pi \not\vdash \kappa_i \wedge \pi_i \} \end{aligned}}{U_d^{\text{pre}}(\bar{x}) \wedge \pi_a \Rightarrow \bigvee_{\Delta \in \mathcal{S}_1 \cup \mathcal{S}_2} (\Delta \ @ \ (\kappa_g \wedge \pi_g))} \quad [\text{syn-inline}]$$

Formally, such predicate inlining can be carried out by the  $[\text{syn-inline}]$  rule. Inlining serves two purposes: (i) allow instantiation of back pointers with the use of heap guards; (ii) minimize the number of predicates a definition relies on. As an eager optimization, the inlining discards infeasible disjuncts in which the context contradicts the guard and also drops the guard where it is already satisfied.

## B.5 Conjunctive Unification

When describing the pre-predicate derivation, we observed that there is a need for a normalization operation for formulas  $\Delta_1 \wedge \Delta_2$  to ensure the result is within the logic fragment described in Sec. 2. We obtain this normalization through a *conjunctive unification* step,  $\Rightarrow_{\bar{v}}^{\wedge}$ . Informally, in order for  $\Delta_1 \wedge \Delta_2$  to be satisfiable, to describe at least one feasible heap, it is necessary that the shapes described by  $\Delta_1$  and  $\Delta_2$  agree when describing the same memory locations. Based on this observation, it is possible to construct a possibly stronger approximation for  $\Delta_1 \wedge \Delta_2$  expressed in our logic fragment by unifying the common heap locations as follows:

$$\begin{array}{c}
\pi \wedge \Delta \Rightarrow_{\bar{v}}^{\wedge} (\Delta \wedge \pi, []) \quad \Delta \wedge \pi \Rightarrow_{\bar{v}}^{\wedge} (\Delta \wedge \pi, []) \\
\frac{\Delta_1 \wedge \Delta_3 \Rightarrow_{\bar{v}}^{\wedge} (\Delta_5, \mathcal{S}_1) \quad \Delta_2 \wedge \Delta_4 \Rightarrow_{\bar{v}}^{\wedge} (\Delta_6, \mathcal{S}_2)}{(\Delta_1 \vee \Delta_2) \wedge (\Delta_3 \vee \Delta_4) \Rightarrow_{\bar{v}}^{\wedge} (\Delta_5 \vee \Delta_6, \mathcal{S}_1 \cup \mathcal{S}_2)} \\
\text{P is known} \quad x \in \bar{v} \quad \bar{y} \cap \bar{v} = \emptyset \quad \rho = [\bar{y} \mapsto \bar{z}] \\
\frac{\Delta_1 \wedge \rho \Delta_2 \Rightarrow_{\bar{v}}^{\wedge} (\Delta_3, \mathcal{S})}{P(x, \bar{z}) * \Delta_1 \wedge P(x, \bar{y}) * \Delta_2 \Rightarrow_{\bar{v}}^{\wedge} (P(x, \bar{z}) * \Delta_3, \mathcal{S})} \\
\text{U}_1, \text{U}_2 \text{ are dangling} \quad x \in \bar{v} \quad \rho = [\bar{y} \mapsto \bar{z}] \quad \bar{y} \cap \bar{v} = \emptyset \\
\frac{\Delta_1 \wedge \rho \Delta_2 \Rightarrow_{\bar{v}}^{\wedge} (\Delta_3, \mathcal{S}) \quad \mathcal{S}_1 = \mathcal{S} \cup \{\text{U}_1 \equiv \text{U}_2\}}{\text{U}_1(x, \bar{z}) * \Delta_1 \wedge \text{U}_2(x, \bar{y}) * \Delta_2 \Rightarrow_{\bar{v}}^{\wedge} (\text{U}_1(x, \bar{z}) * \Delta_3, \mathcal{S}_1)} \\
\frac{x \in \bar{v} \quad \rho = [\bar{y} \mapsto \bar{z}] \quad \bar{y} \cap \bar{v} = \emptyset \quad \Delta_1 \wedge \rho \Delta_2 \Rightarrow_{\bar{v}}^{\wedge} (\Delta_3, \mathcal{S})}{x \mapsto c(\bar{z}) * \Delta_1 \wedge x \mapsto c(\bar{y}) * \Delta_2 \Rightarrow_{\bar{v}}^{\wedge} (x \mapsto c(\bar{z}) * \Delta_3, \mathcal{S})}
\end{array}$$

To streamline the unification process, the  $\Rightarrow_{\bar{v}}^{\wedge}$  operation is parameterized with a set of variables  $\bar{v}$  which denotes the set of possibly common memory locations. We seed this parameter initially with the set of arguments of the predicate under construction.

In the process of unifying predicate instances or heap nodes, the transformation on one hand modifies the formulas by applying a substitution of the arguments, and on the other constructs extra constraints on dangling predicates, thus strengthening the result. We observe however that this is not affecting the soundness of the result as this strengthening is restricted to pre-condition predicate definitions. By imposing an equality constraint on dangling predicates the resulting definitions become more concise, with fewer extra predicates being synthesized.

Naturally, this strengthening *may* lead to a contradiction which we will consider as a failure of the shape analysis, due to contradictory scenarios. For example:

$$x = \text{NULL} \wedge x \mapsto \text{node}(p, n) \Rightarrow_{\bar{v}}^{\wedge} (\text{FALSE}, [])$$

## B.6 Disjunctive Unification

We propose to apply *disjunctive unification* to derive more concise definitions for post-predicate. The aim here is to factor out common constraints on disjunctive branches of a given post-predicate, so that common heap terms in disjunct can be abstracted:

$$x \mapsto c(\bar{y}) * \Delta_1 \vee x \mapsto c(\bar{y}) * \Delta_2 \Rightarrow \text{U}(x, \bar{a}) \Rightarrow_{\vee} \begin{cases} x \mapsto c(\bar{y}) * \text{R}(x, \bar{a}, \bar{y}) \Rightarrow \text{U}(x, \bar{a}) \\ \Delta_1 \vee \Delta_2 \Rightarrow \text{R}(x, \bar{a}, \bar{y}) \end{cases}$$

## B.7 Normalization of Shape Predicates

After we have derived suitable predicate definitions, we proceed with normalization to convert each predicate definition to its most concise form. Our current method uses four key steps:

```

function PRED_NORM( $\Gamma$ )
   $\Gamma_1 \leftarrow$  process-dangling-and-unused-preds  $\Gamma$ 
   $\Gamma_2 \leftarrow$  eliminate-useless-parameters  $\Gamma_1$ 
   $\Gamma_3 \leftarrow$  perform-predicate-splitting on  $\Gamma_2$ 
  return reuse-predicates  $\Gamma_3$ 
end function

```

**Detecting and Eliminating Dangling Predicates** We have seen how relational assumptions are soundly transformed into predicate definitions. However, it is still possible for some pre-predicates not to have any definition. As mentioned in Sec. 3, these *dangling* predicates denote fields that were not accessed. Though it is safe to drop such predicates (by frame rule), we keep them to capture linking information between pre- and post-conditions.

In this predicate normalization step, we associate each dangling predicate  $U(x, \dots)$  encapsulating a pointer that is instantiated and not an argument of the current method, with a logical variable  $\mathcal{D}_U$  denoting such a predicate instance. With this extra notation, in effect, we are making explicit that the addresses pointed to by such fields have neither been read nor written to during the execution of its method. Thus, these logical variables appear in the precondition, and after the execution, in the postcondition. Notice that the marking of pointers as dangling is guided by the context and thus reflects the access patterns. We can formalize these steps as follows:

$$\frac{U(\bar{y}) \equiv (U_d(x, \bar{v}) * \kappa \wedge \pi) \ @ \ (\kappa_g \wedge \pi_g) \ \vee \ \Phi^g \quad x \notin \bar{y} \quad U_d(x, \bar{v}) \equiv \top}{U(\bar{y}) \equiv ([x \mapsto \mathcal{D}_{U_d}] (\kappa \wedge \pi)) \ @ \ (\kappa_g \wedge \pi_g) \ \vee \ \Phi^g}$$

**Eliminating Useless Parameters** We observe that there are cases in which predicate arguments are not used in the synthesized definitions. For our `s112d11` example (Sec. 3), the second parameter `q` in the derived pre-predicate is redundant. For the `t11` example outlined in Sec.1, the last two parameters of the derived pre-predicate are redundant.

In order to simplify the definitions and improve predicate reuse, we propose to detect and eliminate such arguments. For a given predicate definition,  $P(\bar{x}) \equiv \Delta$ , we can discover if any constraint in the predicate body involves a parameter through a standard, sound flow analysis. Once a set of candidate arguments,  $\bar{z}$ , has been identified, we construct a new unknown predicate  $U_z(\bar{x}')$  where  $\bar{x}' = \bar{x} \setminus \bar{z}$  which can then be instantiated by running the bi-abductive entailment check on the entailment  $P'(\bar{x}) \vdash U_z(\bar{x}')$ , where  $P'(\bar{x}) \equiv \exists \bar{z}. \Delta$ . This would gather the necessary set of assumptions on  $U_z$  that can provide a definition for the new predicate without the useless argument(s). Followed by a check that with the inferred definition  $U_z(\bar{x})' \vdash P'(\bar{x})$ . Thus, ideally, the resulting definitions would follow:  $P(\bar{x}) \equiv U_z(\bar{x}')$ ,  $U_z(\bar{x}') \equiv \Delta'$ . Using this step, we can obtain a simpler pre-predicate for `t11` (Sec 1):

$$\begin{aligned} H(x, p, t) &\equiv H_f(x) \\ H_f(x) &\equiv x \mapsto \text{tree}(\mathcal{D}_p, \mathcal{D}_1, r, \mathcal{D}_n) \wedge r = \text{NULL} \vee x \mapsto \text{tree}(\mathcal{D}_p, 1, r, \mathcal{D}_n) * H_f(1) * H_f(r) \wedge r \neq \text{NULL} \end{aligned}$$

**Reusing Predicates** In order to derive more concise predicate definitions, we propose an equivalence detection step that would try and match a newly inferred definition with a definition previously provided or inferred. We leverage on our second-order entailment prover to perform this task, but limit its folding steps to identity matching of predicates that may possibly be equivalent. We also use an analysis to pre-determine those predicates that are unlikely to be equivalent, or have already been processed as such. For any two synthesized predicates  $U1(\bar{v})$  and  $U2(\bar{w})$ , we first align their parameters, and then prove two entailments  $unroll[U1(\bar{v})] \vdash U2(\bar{w})$  and  $unroll[U2(\bar{w})] \vdash U1(\bar{v})$ . (Each *unroll* replaces a predicate instance by its definition. It ensures that our inductive proof is well-founded.) If both entailments fail, we assert the pair of predicates to be disequal. If only one of the entailments succeeds, we assert a *predicate subsumption* has been detected. If both succeed and return a further set of possibly equivalence pairs, we proceed to prove the equivalence of pairs from the new set. When no more pairs of possibly equivalent predicates are found, we assert  $U1(\bar{v}) \leftrightarrow U2(\bar{v})$  to indicate the equivalence of the sets of pairs of predicates that we have just proven.

**Predicate Splitting** Here we illustrate a *split-predicate* tactic to derive, where possible, lemmas of the following form:  $U(x, y) \rightarrow U_1(x) * U_2(y)$  which denotes known facts about valid implication over heap formula that can be used by the entailment checker. For example, consider a predicate that captures  $x$  and  $y$ , two linked lists of same length:

$$twosame(x, y) \equiv x=NULL \wedge y=NULL \vee x \mapsto snode(x_n) * y \mapsto snode(y_n) * twosame(x_n, y_n)$$

To explore such splitting, we can follow the example of useless parameter elimination where our second-order bi-abductive entailment is again used to infer definitions for unknown predicates,  $U_1$  and  $U_2$ . Once these definitions are derived, we can even use the same entailment check to determine if the converse implication  $twosame(x, y) \leftarrow U_1(x) * U_2(y)$  holds. For this example, we can only derive <sup>6</sup>:

$$\begin{aligned} twosame(x, y) &\rightarrow U_1(x) * U_2(y) \\ U_1(x) &\equiv x=NULL \vee x \mapsto snode(n) * U_1(n) \\ U_2(x) &\equiv x=NULL \vee x \mapsto snode(n) * U_2(n) \end{aligned}$$

Now, two resulting predicates  $U_1$  and  $U_2$  have more chances to be matched/reused with given library predicates (e.g. 11 in Sec. 4).

## C S2 Virtual Machine (S2-VM)

There are two options to reproduce the results in Sec. 7:

1. Testing programs through the tool's website (by clicking the URL given in Sec 1).
2. Setting up and conducting the benchmark on a virtual machine S2-VM as suggested in what follows.

<sup>6</sup> We only derive a weakening lemma that can be applied to post-condition, but not to pre-condition. For safely splitting pre-condition of *twosame* which captures two lists of the same length, we will need to extend our inference to capture size properties on lists.



## C.1 Set up The Virtual Machine

- Download it by clicking the URL below. Note to copy-and-paste the link may not work correctly (in which case, please ensure the character `~` before `project` is shown properly in the browser).
  - <http://loris-7.ddns.comp.nus.edu.sg/~project/s2/beta/s2.ova>
  - checksum of **s2.ova** file: 5874796047da26ce53569930d3ae183e
  - virtual machine player: Oracle VM VirtualBox 4.3.6 (or VMware Player 6.0.1)
- Open Terminal in Desktop of S2-VM. Set the working directory to `Desktop/s2`:
  - > `cd Desktop/s2`
- Build S2 from the source code:
  - > `make hip`
- Copy s2 binary into `cav14/bin` folder:
  - > `cp hip cav14/bin`

## C.2 Reproduce the Experimental Results in S2-VM

Assume the current directory of the terminal is **Desktop/s2**

- Set the directory to `cav14` (which contains all examples in the Experiments):
  - > `cd cav14`
- Please refer to README file for the structure of the **cav14** folder.
- Synthesize small benchmark in Table 1 (Sec 7) without normalization:
  - > `cd small`
  - > `./run-wo.sh`
  - > `cd ..`
- Synthesize small benchmark in Table 1 (Sec 7) with normalization:
  - > `cd small`
  - > `./run-norm.sh`
  - > `cd ..`
- Verify small benchmark in Table 1(Sec 7):
  - > `cd small/veri`
  - > `./veri.sh`
  - > `cd ../..`
- Analyze Glib Programs:
  - > `cd glib`
  - > `./run-glib.sh`
  - > `cd ..`

## D Two More Examples

We provide further illustrations of our proposal through two examples to highlight key features of our shape inference mechanism.

```
void append(struct node * x, struct node * y)
requires H(x, y#) ensures G(x, y#)
{ if (x->next) append(x->next, y);
  else { x->next = y; y->prev = x; } }
```

The `append` method joins two doubly-linked lists. To guide the shape synthesis, the initial stub specification is pre-analysed with `#` annotations. Thus, by the same process described in previous sections the following relational assumptions are inferred:

- 1  $H(x, y\#) \Rightarrow x \mapsto \text{node}(x_p, x_n) * H_p(x_p, y\#) * H_n(x_n, y\#) * H_y(y, x\#)$
- 2  $H_n(x_n, y\#) * H_y(y, x\#) \wedge x_n \neq \text{NULL} \Rightarrow H(x_n, y\#)$
- 3  $H_y(y, x\#) \Rightarrow y \mapsto \text{node}(y_p, y_n) * H_{yp}(y_p, x\#) * H_{yn}(y_n, x\#)$
- 4  $H_n(x_n, y\#) \wedge x_n = \text{NULL} \Rightarrow \text{emp}$
- 5  $H_p(x_p, y\#) * x \mapsto \text{node}(x_p, x_n) * G(x_n, y\#) \wedge x_n \neq \text{NULL} \Rightarrow G(x, y\#)$
- 6  $x \mapsto \text{node}(x_p, y) * y \mapsto \text{node}(x, y_n) * H_{yn}(y_n, x\#) * H_p(x_p, y\#) \Rightarrow G(x, y\#)$

We can then synthesize the following predicate definitions:

$$\begin{aligned}
H(x, y) &\equiv x \mapsto \text{node}(\mathcal{D}_p, x_n) * H_n(x_n) * y \mapsto \text{node}(\mathcal{D}_{yp}, \mathcal{D}_{yn}) \\
H_n(x_n) &\equiv \text{emp} \wedge x_n = \text{NULL} \vee x_n \mapsto \text{node}(\mathcal{D}_p, x_{nn}) * H_n(x_{nn}) \\
G(x, y) &\equiv x \mapsto \text{node}(\mathcal{D}_p, y) * y \mapsto \text{node}(x, \mathcal{D}_{yn}) \\
&\quad \vee x \mapsto \text{node}(\mathcal{D}_p, x_n) * G(x_n, y) \wedge x_n \neq \text{NULL}
\end{aligned}$$

Our shape inference mechanism manages to infer a precise (weak) pre-condition which only requires a singly-linked list for the first parameter, and a single node for the second parameter without enforcing unnecessary constraints on the rest of the locations reachable from the second parameter. Furthermore, the derived post-predicate describes a non-empty recursive list segment joined with the structure described by the second parameter. Through the use of dangling references, the derived specification permits cyclic data structures for the second parameter, and moreover guarantees that only its first node is being changed. This more precise pre/post specification subsumes the specification which uses two doubly-linked lists for the two parameters.

To illustrate a more complex data structure, consider the following mutual-recursive methods to validate a rose tree, whose children are linked via a doubly-linked list with

parent pointers,

```

struct mtree {int val; struct mnode* children; }
struct mnode { struct mtree* child; struct mnode* prev;
              struct mnode* next; struct mtree* parent; }
bool c_tree (struct mtree* t)
  requires H1(t) ensures G1(t) ∧ res;
{ if (t->children == null) return true;
  else return c_child(t->children, NULL, t); }

bool c_child(struct mnode* l, struct mnode* prv,
            struct mtree* par)
requires H2(l, prv, par) ensures G2(l, prv, par) ∧ res;
{ if (l == null) return true;
  else if (l->parent == par && l->prev == prv)
    return c_child(l->next, l, par) && c_tree(l->child);
  else return false; }

```

This checker code is special in that we are using it to validate some expected data structure. We use it here primarily for evaluating the precision of our synthesis method. Notice the use of mixed constraints<sup>7</sup> e.g.  $G_1(t) \wedge \text{res}$ , which requires both inference and verification to work together. This comes naturally from our integration of *second-order bi-abduction* into an existing separation logic verifier with proving capability. Essentially, this code is checking that each tree node contains a pointer to a null-terminated doubly-linked list, with pointers to parent node. Our approach is able to derive the following precise and concise predicate definitions. We achieve this feat by using normalization techniques which unify disjuncts and semantically-equivalent predicates, where possible.

$$\begin{aligned}
H_1(t) &\equiv t \mapsto \text{mtree}(v, c) * H_2(c, \text{NULL}, t) \\
H_2(l, b, p) &\equiv \text{emp} \wedge l = \text{NULL} \vee H_2(n_1, l, p) * l \mapsto \text{mnode}(c_1, b, n_1, p) * c_1 \mapsto \text{mtree}(v, c) * H_2(c, \text{NULL}, c_1) \\
G_1(t) &\equiv H_1(t) \\
G_2(l, b, p) &\equiv H_2(l, b, p)
\end{aligned}$$

## E Expanded Soundness

*Proof for Lemma 1* We will show that for all  $\Delta_{\text{ante}}$  and  $\Delta_{\text{conseq}}$  such that

$$\Delta_{\text{ante}} \vdash \Delta_{\text{conseq}} \rightsquigarrow (\mathcal{R}, \Delta_{\text{frame}})$$

and  $\Gamma = \{U_1(\bar{v}_1) \equiv \Delta_1, \dots, U_n(\bar{v}_n) \equiv \Delta_n\}$ , a set of instantiations for unknown predicates such that  $\mathcal{R}(\Gamma)$  then the entailment  $\Gamma: \Delta_{\text{ante}} \vdash \Delta_{\text{conseq}} * \Delta_{\text{frame}}$  holds. We will show by structural induction on  $\Delta_{\text{conseq}}$ .

<sup>7</sup> C languages uses integer for boolean values, which gets translated to boolean type in our core language.

Due to the construction of the  $\Delta_{\text{ante}} \vdash \Delta_{\text{conseq}} \rightsquigarrow (\mathcal{R}, \Delta_{\text{frame}})$  procedure as an extension of an existing entailment procedure with frame inference, for all  $\Delta_{\text{ante}}$  and  $\Delta_{\text{conseq}}$  not involving unknown predicates  $\mathcal{R} = \text{true}$  and  $\Gamma: \Delta_{\text{ante}} \vdash \Delta_{\text{conseq}} * \Delta_{\text{frame}}$ .

Bellow we consider the cases that actually involve unknown predicates. These cases fall under two categories:

- $\Delta_{\text{ante}} = U(\mathbf{r}, \bar{v}_i, \bar{v}_{n\#}) * \kappa_1 \wedge \pi_1$  and  $\Delta_{\text{conseq}} = \kappa_s * \kappa_2 \wedge \pi_2$  where  $\kappa_s \equiv \mathbf{r} \mapsto \mathbf{c}(\bar{d}, \bar{p})$  or  $\kappa_s \equiv \mathbf{P}(\mathbf{r}, \bar{d}, \bar{p})$ . By hypothesis  $\Delta_{\text{ante}} \vdash \Delta_{\text{conseq}} \rightsquigarrow (\mathcal{R}, \Delta_{\text{frame}})$ . Then, as described in Sec.6 the `[SO-ENT-UNFOLD]` step must hold ensuring the following assertion holds:

$$\kappa_1 * \Delta_{\text{dangl}} * \Delta_{\text{rem}} \wedge \pi_1 \vdash \kappa_2 \wedge \pi_2 \rightsquigarrow (\mathcal{R}', \Delta_{\text{frame}})$$

where  $\mathcal{R} = \mathcal{R}' \wedge (U(\mathbf{r}, \bar{v}_i, \bar{v}_{n\#}) \wedge \pi_a \Rightarrow \kappa_s * \Delta_{\text{dangl}} * \Delta_{\text{rem}} \wedge \pi_c)$  It follows from the structural induction hypothesis that:

$$\Gamma: \kappa_1 * \Delta_{\text{dangl}} * \Delta_{\text{rem}} \wedge \pi_1 \vdash (\kappa_2 \wedge \pi_2) * \Delta_{\text{frame}} \quad (1)$$

From  $\mathcal{R}(\Gamma)$  it follows that

$$\Gamma: U(\mathbf{r}, \bar{v}_i, \bar{v}_{n\#}) \wedge \pi_a \vdash \kappa_s * \Delta_{\text{dangl}} * \Delta_{\text{rem}} \wedge \pi_c \quad (2)$$

From equations 1 and 2 it follows that  $\Gamma: \Delta_{\text{ante}} \vdash \Delta_{\text{conseq}} * \Delta_{\text{frame}}$ .

- $\Delta_{\text{ante}} = \kappa_1 \wedge \pi_1$  and  $\Delta_{\text{conseq}} = U_c(\bar{w}, \bar{z}\#) * \kappa_2 \wedge \pi_2$ .  
Let  $\pi_r = \text{slice}(\bar{r}, \pi_1)$  and  $\pi_w = \text{slice}(\bar{w}, \pi_1)$ .  
By hypothesis  $\Delta_{\text{ante}} \vdash \Delta_{\text{conseq}} \rightsquigarrow (\mathcal{R}, \Delta_{\text{frame}})$ .  
Then, as described in Sec.6 the `[SO-ENT-FOLD]` step must hold ensuring the following assertions hold:
    - $\kappa_1 = \kappa_{11} * \kappa_{12}$
    - $\kappa_{12} \wedge \pi_1 \vdash \kappa_2 \wedge \pi_2 \rightsquigarrow (\mathcal{R}', \Delta_{\text{frame}})$  which by structural induction leads to  $\Gamma: \kappa_{12} \wedge \pi_1 \vdash (\kappa_2 \wedge \pi_2) * \Delta_{\text{frame}}$
    - $\mathcal{R} = (\kappa_{11} \wedge \pi_w \Rightarrow U_c(\bar{w}, \bar{z}\#) \ @ \ \kappa_g \wedge \pi_r) \wedge \mathcal{R}'$  which by  $\mathcal{R}(\Gamma)$  leads to:  $\Gamma: \kappa_{11} \wedge \pi_w \vdash U_c(\bar{w}, \bar{z}\#) \ @ \ \kappa_g \wedge \pi_r$  Note that by the definition in Sec.2 for guarded assumptions, it follows that  $\Delta \ @ \ (\kappa_g \wedge \pi_r) * \kappa_g \wedge \pi_r$  is equivalent with  $\Delta * (\kappa_g \wedge \pi_r)$ .
- From the above three assertions it follows that  $\Gamma: \Delta_{\text{ante}} \vdash \Delta_{\text{conseq}} * \Delta_{\text{frame}}$

□

*Proof for Lemma 2* We will show that given a set of relational assumptions  $\mathcal{R}$  and one of the synthesis rules is applied to obtain  $\mathcal{R}'$  then if exists  $\Gamma$  such that  $\mathcal{R}'(\Gamma)$  then  $\mathcal{R}(\Gamma)$ .

- If `[syn-base]` was applied to  $\mathcal{R} \wedge (U^{\text{pre}}(\bar{x}) * \kappa \wedge \pi \Rightarrow U^{\text{post}}(\bar{y}))$  then the resulting assumptions are either:
  1.  $\mathcal{R}' = \mathcal{R} \wedge (U^{\text{pre}}(\bar{x}) \wedge \pi' \Rightarrow \text{emp}) \wedge (\kappa \wedge \pi \Rightarrow U^{\text{post}}(\bar{y}))$   
OR
  2.  $\mathcal{R}' = \mathcal{R} \wedge (U^{\text{fr}}(\bar{x}) \Rightarrow \top) \wedge (U^{\text{pre}}(\bar{x}) \Rightarrow U^{\text{fr}}(\bar{x}) \ @ \ (\kappa_g \wedge \pi_g)) \wedge (U^{\text{pre}}(\bar{x}) * \kappa \wedge \pi \Rightarrow U^{\text{post}}(\bar{y}))$  where  $U^{\text{fr}}$  is a fresh unknown predicate

If there exists  $\Gamma$  such that  $\mathcal{R}'(\Gamma)$  then by definition, using the  $\Gamma$  interpretation for the unknown predicates then either:

1.  $\Gamma: \mathbb{U}^{\text{pre}}(\bar{x}) \wedge \pi' \vdash \text{emp}$  and  $\Gamma: \kappa \wedge \pi \vdash \mathbb{U}^{\text{post}}(\bar{y})$  and since by construction  $\pi \vdash \pi'$  it follows that  $\Gamma: \mathbb{U}^{\text{pre}}(\bar{x}) * \kappa \wedge \pi \vdash \mathbb{U}^{\text{post}}(\bar{y})$  thus  $\mathcal{R}(\Gamma)$ .
  2.  $\Gamma: \mathbb{U}^{\text{pre}}(\bar{x}) * \kappa \wedge \pi \vdash \mathbb{U}^{\text{post}}(\bar{y})$  and  $\Gamma: \mathbb{U}^{\text{pre}}(\bar{x}) \vdash \mathbb{U}^{\text{fr}}(\bar{x}) \in (\kappa_g \wedge \pi_g)$  and  $\Gamma: \mathbb{U}^{\text{fr}}(\bar{x}) \vdash \top$  leading to  $\Gamma: \mathbb{U}^{\text{pre}}(\bar{x}) * \kappa \wedge \pi \vdash \mathbb{U}^{\text{post}}(\bar{y}) * \top \in (\kappa_g \wedge \pi_g)$  which by construction of  $\kappa_g \wedge \pi_g$  and  $\sigma_2$  leads to  $\Gamma: \mathbb{U}^{\text{pre}}(\bar{x}) * \kappa \wedge \pi \vdash \mathbb{U}^{\text{post}}(\bar{y})$  and thus  $\mathcal{R}(\Gamma)$ .
- If **[syn-case]** was applied to:  $\mathcal{R} \wedge (\mathbb{U}(\bar{x}) \wedge \pi_1 \Rightarrow \Delta_1 \in \Delta_{1g}) \wedge (\mathbb{U}(\bar{x}) \wedge \pi_2 \Rightarrow \Delta_2 \in \Delta_{2g})$ . To generate:

$$\mathcal{R}' = \mathcal{R} \wedge (\mathbb{U}(\bar{x}) \wedge \pi_1 \wedge \neg \pi_2 \Rightarrow \Delta_1 \in \Delta_{3g}) \wedge (\mathbb{U}(\bar{x}) \wedge \pi_2 \wedge \neg \pi_1 \Rightarrow \Delta_2 \in \Delta_{3g}) \wedge (\mathbb{U}(\bar{x}) \wedge \pi_1 \wedge \pi_2 \Rightarrow \Delta_3 \in \Delta_{3g})$$

with  $\pi_1 \wedge \pi_2 \not\Rightarrow \text{FALSE}$  and  $\Delta_1 \wedge \Delta_2 \Rightarrow \bar{x} \Delta_3$  and  $\Delta_{1g} \wedge \Delta_{2g} \Rightarrow \bar{x} \Delta_{3g}$  and exists a  $\Gamma$  such that  $\mathcal{R}'(\Gamma)$ . From  $\mathcal{R}'(\Gamma)$  it follows that:

$$\Gamma: \mathbb{U}(\bar{x}) \wedge \pi_1 \wedge \neg \pi_2 \vdash \Delta_1 \in \Delta_{3g} \quad \Gamma: \mathbb{U}(\bar{x}) \wedge \pi_2 \wedge \neg \pi_1 \vdash \Delta_2 \in \Delta_{3g} \\ \Gamma: \mathbb{U}(\bar{x}) \wedge \pi_1 \wedge \pi_2 \vdash \Delta_3 \in \Delta_{3g}$$

We need to show that:  $\Gamma: \mathbb{U}(\bar{x}) \wedge \pi_1 \vdash \Delta_1 \in \Delta_{1g}$  and  $\Gamma: \mathbb{U}(\bar{x}) \wedge \pi_2 \vdash \Delta_2 \in \Delta_{2g}$ .

Note that by the definition of the conjunctive unification, it follows that if  $\Delta_1 \wedge \Delta_2 \Rightarrow \bar{x} \Delta_3$  then  $\Gamma: \Delta_3 \vdash \Delta_1$  and  $\Gamma: \Delta_3 \vdash \Delta_2$ . Thus from  $\Gamma: \mathbb{U}(\bar{x}) \wedge \pi_1 \wedge \pi_2 \vdash \Delta_3 \in \Delta_{3g}$  and follows that:  $\Gamma: \mathbb{U}(\bar{x}) \wedge \pi_1 \wedge \pi_2 \vdash \Delta_1 \in \Delta_{3g}$  and  $\Gamma: \mathbb{U}(\bar{x}) \wedge \pi_1 \wedge \pi_2 \vdash \Delta_2 \in \Delta_{3g}$

Thus it follows:  $\Gamma: \mathbb{U}(\bar{x}) \wedge \pi_1 \wedge (\neg \pi_2 \vee \pi_2) \vdash \Delta_1 \in \Delta_{3g}$

which simplifies to  $\Gamma: \mathbb{U}(\bar{x}) \wedge \pi_1 \vdash \Delta_1 \in \Delta_{3g}$  which by the construction of  $\Delta_{3g}$  leads to  $\Gamma: \mathbb{U}(\bar{x}) \wedge \pi_1 \vdash \Delta_1 \in \Delta_{1g}$ . Similarly we obtain:  $\Gamma: \mathbb{U}(\bar{x}) \wedge \pi_2 \vdash \Delta_2 \in \Delta_{2g}$ .

- **[syn-group-pre]** The proof obligation reduces to: if there exists  $\Gamma$  such that  $\Gamma: \mathbb{U}(\bar{v}) \wedge (\pi_1 \vee \pi_2) \vdash \Phi^{g_1} \wedge \pi_1 \vee \Phi^{g_2} \wedge \pi_2$  and  $\pi_1 \wedge \pi_2 \vdash \text{FALSE}$  then  $\Gamma: \mathbb{U}(\bar{v}) \wedge \pi_1 \vdash \Phi^{g_1}$  and  $\Gamma: \mathbb{U}(\bar{v}) \wedge \pi_2 \vdash \Phi^{g_2}$ . It follows that  $\Gamma: \mathbb{U}(\bar{v}) \wedge \pi_1 \vdash \Phi^{g_1} \wedge \pi_1 \vee \Phi^{g_2} \wedge \pi_2$  and  $\Gamma: \mathbb{U}(\bar{v}) \wedge \pi_2 \vdash \Phi^{g_1} \wedge \pi_1 \vee \Phi^{g_2} \wedge \pi_2$ . And since  $\pi_1 \wedge \pi_2 \vdash \text{FALSE}$  it follows that  $\Gamma: \mathbb{U}(\bar{v}) \wedge \pi_1 \vdash \Phi^{g_1} \wedge \pi_1$  and  $\Gamma: \mathbb{U}(\bar{v}) \wedge \pi_2 \vdash \Phi^{g_2} \wedge \pi_2$ .
- **[syn-group-post]** It follows trivially that  $\Gamma: \Delta_a \vdash \mathbb{U}^{\text{post}}(\bar{v})$  and  $\Gamma: \Delta_b \vdash \mathbb{U}^{\text{post}}(\bar{v})$  from  $\Gamma: \Delta_a \vee \Delta_b \vdash \mathbb{U}^{\text{post}}(\bar{v})$ .
- if **[syn-inline]** was applied to:  $\mathcal{R} \wedge (\mathbb{U}^{\text{pre}}(\bar{x}) \wedge \pi_a \Rightarrow (\mathbb{U}_a^{\text{pre}}(\bar{x}) * \kappa \wedge \pi) \in (\kappa_g \wedge \pi_g))$  resulting in the assumption set:  $\mathcal{R}' = \mathcal{R} \wedge (\mathbb{U}^{\text{pre}}(\bar{x}) \wedge \pi_a \Rightarrow \bigvee_{\Delta_i \in \mathcal{S}_1 \cup \mathcal{S}_2} (\Delta_i \in (\kappa_g \wedge \pi_g)))$  when

$$\mathbb{U}_a^{\text{pre}}(\bar{x}) \equiv \Delta_1 \in (\kappa_1 \wedge \pi_1) \vee \dots \vee \Delta_n \in (\kappa_n \wedge \pi_n) \\ \mathcal{S}_1 = \{ \Delta_i * \Delta_r * \kappa_i \mid \kappa \wedge \pi \vdash \kappa_i \wedge \pi_i \rightsquigarrow (\emptyset, \Delta_r) \} \\ \mathcal{S}_2 = \{ \kappa * \Delta_i \wedge \pi \in (\kappa_i \wedge \pi_i) \mid \text{SAT}(\kappa \wedge \kappa_i \wedge \pi \wedge \pi_i), \kappa \wedge \pi \not\vdash \kappa_i \wedge \pi_i \}$$

We need to prove that if exists  $\Gamma$  such that  $\mathcal{R}'(\Gamma)$  then,  $\mathcal{R}(\Gamma)$ . That is:

$\Gamma: \mathbb{U}^{\text{pre}}(\bar{x}) \wedge \pi_a \vdash (\mathbb{U}_a^{\text{pre}}(\bar{x}) * \kappa \wedge \pi) \in (\kappa_g \wedge \pi_g)$ . Which by using the  $\mathbb{U}_a^{\text{pre}}$  definition translates in having to prove:  $\Gamma: \mathbb{U}^{\text{pre}}(\bar{x}) \wedge \pi_a \vdash \bigvee_{i \in 1 \dots n} ((\Delta_i \in (\kappa_i \wedge \pi_i) * \kappa \wedge \pi) \in (\kappa_g \wedge \pi_g))$ . From  $\mathcal{R}'(\Gamma)$  it follows that:  $\Gamma: \mathbb{U}^{\text{pre}}(\bar{x}) \wedge \pi_a \vdash \bigvee_{\Delta_i \in \mathcal{S}_1 \cup \mathcal{S}_2} ((\kappa \wedge \pi) * \Delta_i) \in (\kappa_g \wedge \pi_g)$ .

We will show that:

$$\bigvee_{\Delta_i \in \mathcal{S}_1 \cup \mathcal{S}_2} (((\kappa \wedge \pi) * \Delta_i) \in (\kappa_g \wedge \pi_g)) \equiv \bigvee_{i \in 1 \dots n} (\Delta_i \in (\kappa_i \wedge \pi_i) * \kappa \wedge \pi) \in (\kappa_g \wedge \pi_g)$$

Observe that by the definition of the guard assertion,  $(\Delta_i \textcircled{=} (\kappa_i \wedge \pi_i) * \kappa \wedge \pi)$  a RHS disjunction where the guard  $\kappa_i \wedge \pi_i$  contradicts the context  $\kappa \wedge \pi$  is equivalent to FALSE and thus can be discarded, leaving only disjuncts that do not contradict the context. Note that by construction,  $\mathcal{S}_1 \cup \mathcal{S}_2$  denotes exactly that set. Furthermore, by the definition of the guarded assumption, assertions  $(\Delta_i \textcircled{=} (\kappa_i \wedge \pi_i) * \kappa \wedge \pi)$  in which  $\kappa \wedge \pi \vdash \kappa_i \wedge \pi_i$  can be reduced to  $\Delta_i * \kappa \wedge \pi$ . Observe that the result of the application of the above two equivalence preserving simplification steps on the RHS is identical to the LHS. Thus the required disjunction equivalence holds.

*Proof for Lemma 3* Follows from the observation that there are only two rules generating predicate definitions: `[syn-pre-def]` and `[syn-post-def]`. Each applicable only if there exists only one assumption corresponding to the predicate that is currently being derived. Each rule generates exactly the predicate definition that would satisfy the unique assumption.

*Proof for Lemma 4* First, we observe that the algorithm in Fig.7 finishes only when all assumptions have been catered for: assumptions used for synthesis have been reduced to a unique assumption which becomes the predicate definition; assumptions not included in the synthesis are discharged by an entailment step. Thus by the previous lemmas and the soundness of the underlying entailment checker the resulting definitions satisfy all the initial assumptions.

Second, we will show that all normalization steps are meaning preserving:

- Dangling elimination: We need to show that if  $U_d(x, \bar{v}) \equiv \top$  and  $x \notin \bar{v}$  then:

$$U(\bar{y}) \equiv ([x \mapsto \mathcal{D}_{U_d}](\kappa \wedge \pi)) \textcircled{=} (\kappa_g \wedge \pi_g) \vee \Phi^g$$

is equivalent to:

$$U(\bar{y}) \equiv (U_d(x, \bar{v}) * \kappa \wedge \pi) \textcircled{=} (\kappa_g \wedge \pi_g) \vee \Phi^g$$

We first observe that variables local to the predicate definition, not part of the predicate arguments, are implicitly existentially quantified. As mentioned, we use the  $\mathcal{D}_{U_d}$  notation as a visual aid, to identify an instance of the predicate  $U_d$  whose root pointer  $x$  is reachable but has been neither read nor written to. By expanding the notation, the equivalence to be proven becomes:

$$\exists x. (U_d(x, \bar{v}) * \kappa \wedge \pi) \equiv \exists \mathbf{f}_v. [\mathcal{D}_{U_d} \mapsto \mathbf{f}_v] ([x \mapsto \mathcal{D}_{U_d}](\kappa \wedge \pi)) * U_d(\mathbf{f}_{v2}, \dots)$$

By applying the predicate definitions:

$$\exists x. (\top * \kappa \wedge \pi) \equiv \exists \mathbf{f}_v. [\mathcal{D}_{U_d} \mapsto \mathbf{f}_v] ([x \mapsto \mathcal{D}_{U_d}](\kappa \wedge \pi)) * \top$$

Which holds trivially.

- Eliminating useless parameters: We need to show that if at this step a predicate  $P(\bar{x}) \equiv \Delta_1$  is distilled into  $Q(\bar{x}') \equiv \Delta_2$  then  $P'(\bar{x}) \equiv Q(\bar{x}')$  that is,  $\Delta_1$  holds iff  $\Delta_2$  holds. By construction  $\bar{x}' = \bar{x} \setminus \bar{z}$  and  $\exists \bar{z}. \Delta_1 \vdash \Delta_2$  and also  $\Delta_2 \vdash \exists \bar{z}. \Delta_1$  which leads to  $\Delta_2 \equiv \exists \bar{z}. \Delta_1$  and by the soundness of the flow analysis used to detect that variables  $\bar{z}$  are not used in  $\Delta_1$  it follows that  $\Delta_1 \equiv \exists \bar{z}. \Delta_1$ .

- Re-using predicates: We need to show that if at this step a predicate  $P(\bar{x}) \equiv \Delta_1$  is found to be equivalent with  $Q(\bar{x}') \equiv \Delta_2$  then  $\Delta_1$  holds iff  $\Delta_2$  holds. By the premise of this normalization step,  $\Delta_1 \vdash Q(\bar{x}')$  and also  $\Delta_2 \vdash P(\bar{x})$  which leads to  $\Delta_2 \equiv \Delta_1$ .
- Predicate splitting: Soundness follows from the construction. Given a predicate  $P(\bar{x})$  we need to show that if the bi-abduction succeeds in discovering definitions for  $U_1(\bar{x})$  and  $U_2(\bar{x})$  such that  $P(\bar{x}) \vdash U_1(\bar{x}) * U_2(\bar{x})$  and if the derived predicate definitions can be used to prove  $P(\bar{x}) \equiv U_1(\bar{x}) * U_2(\bar{x})$  which follows from the soundness of the bi-abduction and of the entailment methods.

## F Limitation Example

To show limitation of our current proposal, we highlight an example in Fig. 9 where we are currently unable to prove memory safety. The lists  $l_1$  and  $l_2$  are actually from overlapping heap memory since they are computed from the same list, `list`.

```

struct GSList*
g_slist_sort_real(struct GSList* list, ...)
{ struct GSList *l1, *l2;
  if (!list) return NULL; if (!list->next)
    return list;
  l1 = list; l2=list->next;
  while ((l2=l2->next) != NULL){
    if ((l2=l2->next) == NULL) break;
    l1=l1->next; } //failed precondition
  l2=l1->next; ...}

```

**Fig. 9.** `g_slist_sort_real` from `gslis.c`

Our current modular procedure infers two disjoint lists as pre-condition of the loop, with  $l_1$  being longer than  $l_2$  through a composite predicate (similar to the `zip` example). Precondition of this loop cannot be currently proven inside `g_slist_sort_real`. To analyse this example successfully, our current tool would have to be extended to infer immutability and size property of heap data structure, so that certain heap overlaps can be handled. In particular, we would need to infer the following more precise

specification:

$$\begin{aligned} &\text{requires } (l\text{seg}(l_2, \text{NULL}, n)@L \wedge l\text{seg}(l_1, q, n-1)@L) \wedge n > 0 \\ &\text{ensures } l'_2 = \text{NULL} \wedge l'_1 = q \end{aligned}$$

Here, list segments of  $l_1$  and  $l_2$  of lengths  $n$  and  $n-1$  are overlapping but accessed in read-mode via the `@L` annotation.  $l'_1$  and  $l'_2$  captures the updated variables at exit of loop. Furthermore, there are other examples which rely on both shape and pure properties (e.g. sortedness or size) for memory safety.