

3. Programming

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Carrot Cake Recipe (adapted from allrecipes.com)

- 1 Preheat the oven to 175 degrees Celsius.
- 2 Grease and flour a 9 inches by 13 inches pan.
- 3 Beat four eggs.
- 4 Mix the eggs, one fourth of a cup of vegetable oil, two cups of sugar, two cups of flour, two tea spoons of backing soda, and three cups of grated carrots in a large bowl.
- 5 Add two tea spoons of vanilla extract if you have some.
- 6 Pour the mixture into the pan.
- 7 Bake until a knife inserted into the center comes out clean.
- 8 Let cool for ten minutes.
- 9 Put in a plate.
- 10 Prepare the frosting.
- 11 Pour the frosting onto the cake.

Von Neumann Architecture

The **Von Neumann architecture** was proposed in 1945 by the mathematician and computer scientist John von Neumann.

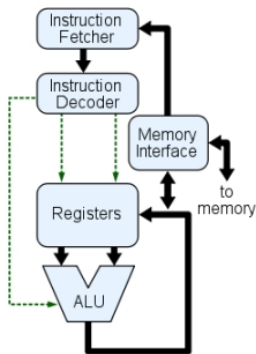


Von Neumann Architecture

The Von Neumann Architecture describes a computer as consisting of a **processing unit** with an **arithmetic logic unit** and **registers**, a **control unit** containing an **instruction register** and **program counter**, a **memory** to store both data and instructions, **external mass storage**, and **input** and **output** mechanisms.

Central Processing Unit

The **central processing unit (CPU)** is composed of the **arithmetic logic unit (ALU)**, **registers**, an **interface to main memory** and an **instruction fetcher and decoder**.



Central Processing Unit

The Central Processing Unit repeatedly fetches, decodes and executes program instructions, and writebacks results.

The program is in memory. It is a series of instructions each represented by one binary word.

- 1 The CPU **fetches** the next instruction at a location in memory determined by the program counter.
- 2 The CPU **decodes** the instruction and determines what it has to do as defined by the CPU's instruction set architecture (ISA).
- 3 The ALU **performs** the arithmetic and logical operation required on the inputs (in the registers or main memory).
- 4 The CPU **writes** the results to memory (register or main memory).

Instruction Set

Every processor or processor family has its own **machine code instruction set**.

Instructions

move, add, subtract, multiply, divide, increment, decrement, exchange, compare, jump on condition, etc.

x86 Instruction Format

x86 instructions are represented as binary numbers and require between 1 and 6 bytes. Most instructions are coded on 2 bytes (16 bits) as follows.

- 6 bits for the code of the operation
- 1 bit for the direction of data movement (1 for movement from second to first operand, 0 otherwise)
- 1 bit for the size of the operands (1 for word - 16 bits or 32 bits machine - and 0 for byte)
- 2 bits for the interpretation of the second operand
- 3 bits for the code of the first operand (a register)
- 3 bits for the code or address of the second operand (a register or memory)

Example

The instruction 100010 1 1 00 000 111 (8B 07 in hexadecimal) moves the value at the address in the register bx into the register ax.

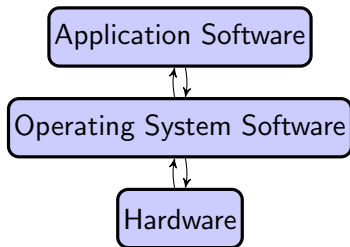
Assembly Language

The machine is programmed in **assembly language**. An **assembler** generates the machine code.

```
1 mov ax , [ bx ]
```

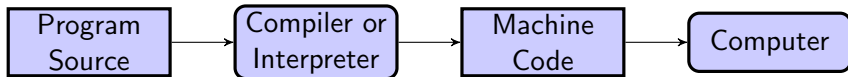


```
1 .data
2     Sum    DW ?    ; non-initialised 2-byte value.
3     Length DW 6    ; initialised 2-byte value.
4     Table  DB 89, 53, 5, 61, 127, 5
5           ; Table is an array of Length 1-byte elements.
6
7 .code
8     lea bx, Table    ; bx receives Table's address.
9     mov ax, 0        ; let ax take the value 0.
10    mov si, 0        ; let si take the value 0.
11 next:
12    cmp si, Length   ; compare si with Length (6).
13    je finish        ; if equal, go to "finish".
14    add ax, byte ptr [bx+si]
15           ; add to ax the 1-byte value pointed by bx+si.
16    inc si           ; add to si the value 1.
17    jmp next        ; go to "next".
18 finish:
19    mov Sum, ax      ; store the result into Sum.
```



The Operating System

The **operating system software** (e.g. Android, iOS, GNU/Linux, Mac OS X, Unix BSD, Microsoft Windows) provides the interface to the **computer hardware** (central processing unit and main memory) and **devices** (e.g. keyboard, screen, printer, hard drive, DVD, network cards etc. by means of **drivers**) and generic services and abstractions (such as memory management, multitasking, multiprocessing and file and directory management) for the **application software**. It often includes a **user interface**.



Programming Languages

The application is written in a higher level **programming language**.

Compilers and Interpreters

- The **program source** written in a programming language is compiled or interpreted by a **compiler** or **interpreter**, respectively, and executed on the hardware with the mediation of the operating system.

Some Programming and other Languages

ABAP ACSL Ada Algol Ant APL Assembler Awk bash Basic C C++ Caml
 Clean Cobol Comal csh Delphi Eiffel Elan erlang Euphoria Fortran GCL
 Gnuplot Haskell HTML IDL inform Java JVMIS ksh Lisp Logo make
 Mathematica Matlab Mercury MetaPost Miranda Mizar ML Modelica
 Modula-2 MuPAD NASTRAN Oberon-2 OCL Octave Oz Pascal Perl PHP
 PL/I Plasm POV Prolog Promela Python R Reduce Rexx RSL Ruby S SAS
 Scheme Scilab sh SHELXL Simula SmallTalk SQL tcl TeX VBScript Verilog
 VHDL VRML XML XSLT

Data

A programming language provides **variables**, **data structures** and other **objects** to record, organize and access values of various **data types**.

- Numbers (integer, floating point real),
- Strings,
- Boolean,
- etc.

Operations

A programming language provides **constructs**, **operations** and **functions** to create and manipulate the data, data structures, objects and to access resources.

- Assignment,
- Arithmetic operations,
- Boolean operations and conditions,
- Input/Output operations,
- etc.

Control Structures

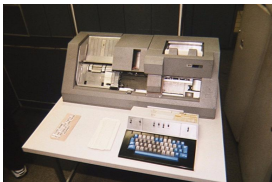
A programming language provides **control structures** to define the execution flow at runtime.

- Direct sequencing,
- Jump,
- Conditional branching,
- Bounded iteration,
- Conditional iteration,
- Subroutines,
- Recursion.

FORTRAN

Fortran is an imperative programming language designed for scientific computing by IBM in the 1950s.

```
1 PROGRAM MAIN
2 INTEGER N, I
3 N=0
4 DO I = 0, 100, 1
5   N=N+I
6 END DO
7 PRINT *, N
8 END
```

Cobol

Fortran is an imperative programming language designed for business computing by a committee of computer scientists from academia and industry in the 1960s. It was inspired by earlier languages designed by Grace Hopper.

```
1  identification  division .
2  program-id.    Gauss .
3  data  division .
4  working-storage section .
5  01  n  pic 9999  value zeros .
6  01  i  pic 9999  value zeros .
7  procedure  division .
8  perform  varying  i  from 0 by 1 until i > 100
9      add  i  to  n
10 end-perform
11 display  n .
12 stop  run .
```

Pascal

Pascal is an imperative programming language designed for structured programming by Niklaus Wirth in the 1970s. It was widely used for teaching computing in the 1980s.

```
1 program gauss (output);  
2 var  
3   n : integer;  
4   i : integer;  
5 begin  
6   n:=0;  
7   for i:= 0 to 100 do  
8     n:= n + i;  
9     write(n);  
10  end.
```

Ada

Ada is an imperative and object oriented programming language designed for object oriented and structured programming by CII Honeywell Bull in the 1970s. It was named after Ada Lovelace.

```
1 with Text_IO;
2 procedure Sum100 is
3     N : Natural := 0;
4 begin
5     for I in 1 .. 100 loop
6         N := N + I;
7     end loop;
8     Text_IO.Put_line(Natural'Image(N));
9 end Sum100;
```

Perl

Perl is a scripting language designed for shell programming by Unisys in the 1980s.

```
1 my $n = 0;
2 for (my $i = 1; $i <= 100; $i++) { $n = $n + $i; }
3 print $n;
```

SmallTalk

SmallTalk is an object oriented programming language designed for education by Xerox PARC in the 1970s.

```
1 m := 0.  
2 0 to: 100 do: [:i | m := m + i. ]  
3 m printN1.
```

Prolog

Prolog is logic programming language designed for computational linguistic by Alain Colmerauer and Philippe Roussel in the 1970s. It was widely used for artificial intelligence in the 1980s and 1990s.


```
1 :- sum(100, R), writeln(R)
2 sum(0, 0).
3 sum(I, J) :- I is I - 1, sum(I, JJ), J is I + JJ.
```

Computers Err

- **Compile-time error**: Some errors are caught by the compiler.
- **Runtime error**: Some errors are caught by the operating system or interpreter or cause the application or system to crash at runtime.
- Some errors simply result in unwanted (and sometimes undetected) behaviours.

9/9

0800 Action started
 1000 sheet - action ✓ { 1.200 2.020 100 015
 1500 1500 MP-AC 1.12000000 2.05 016 015 015
 2000 PRO - 2.13002015
 2500 2.13002015
 3000 Relay 6-2 - 022 fault speed speed test
 to relay
 3500 Relays changed
 Started Relay Test (Sine check)
 1525 Started Relay Test
 1545 Relay #70 Panel F
 (Fault) in relay.



1650 First actual case of bug being found.
 1700 closed down.

Relay #70
 1007 1177



*Harvard University Mark II Computer
 group's 1947 log book, entry
 attributed to
 Grace Hopper (December 9, 1906 -
 January 1, 1992).*

Where could the Bug Be?

Errors in hardware, operating systems, compilers, interpreters and commercial application software occur. They are well publicized when discovered and fixed in the following versions (in particular when they can be security threats: see “Top 25 Most Dangerous Software Errors” <http://cwe.mitre.org/top25>).

Hardware errors are the rarest but occur: The Pentium FDIV bug discovered by Professor Thomas R. Nicely in October 1994 - “An error in a lookup table created the infamous bug in Intel’s latest processor”, by Tom R. Halfhill, BYTE (March 1995).

Errors can be due to interactions between components, for instance in operating systems, the interaction between drivers and applications.

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