

Integrated Formal Modeling Techniques and UML

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The 23rd IEEE International Conference on Software Engineering (ICSE 2001, Toronto, Canada) Tutorial Notes (Thanks Drs. Roger Duke and Brendan Mahony for the joint research work and some teaching slides)

Overview

- State-based Formalism: Z/Object-Z
- Event-based Formalism: CSP/Timed-CSP
- Timed Communicating Object Z – TCOZ
- Active Objects and Network Topology
- Case Study: Lift System
- Sensor, Actuator and Control Systems
- Unified Modeling Language (UML)
- Linking TCOZ with UML
- Z family on the Web with their UML pictures

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Why Formal Specification?

A formal specification should

- add clarity and understanding by giving a description of the system which is
 - complete
 - unambiguous
 - easily analysed;
- lead to better code that is
 - reliable
 - accurate
 - maintainable
 - reusable
 - verified.

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Formal Specification and Software Engineering

(Towards an integrated methodology for software engineering.)

Formal specification

- is not a replacement, but rather an enhancement of existing methodologies;
- can only be effective if integrated within an overall methodology for software engineering.

Implications of using formal specification

- training in the use of notation
- integration with informal methodologies
- translation for client consumption
- emphasis upon abstraction

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Some Specification Languages

Process Algebra

Systems modelled as processes partaking in communication:

CSP, CCS, LOTOS

State Oriented

Systems modelled by an underlying state which can undergo change:

VDM, Z, Object-Z

Algebraic

Systems modelled by equations related by axioms (re-writing rules):

ACT 1, CLEAR, OBJ, Larch

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The Z Specification Language

- developed originally at Programming Research Group, Oxford University
- based on set theory and predicate logic
- system described by introducing fixed sets and variables and specifying the relationships between them using predicates
- declarative, not procedural
- system state determined by values taken by variables subject to restrictions imposed by state invariant
- operations expressed by relationship between values of variables before, and values after, the operation
- variable declarations and related predicates encapsulated into schemas
- schema calculus facilitates the composition of complex specifications
- J. Woodcock and J. Davies , Using Z: Specification, Refinement, and Proof. Prentice-Hall, 1996

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Types

Z is strongly typed: every expression is given a type.

Any set can be used as a type.

The following are equivalent within set comprehension

$$\begin{aligned} &(x, y) : A \times B \\ &x : A; y : B \\ &x, y : A \quad (\text{when } B = A) \end{aligned}$$

Notice that

$$\forall S : \mathbb{P}A \bullet \dots \quad \text{not} \quad \forall S \subseteq A \bullet \dots$$

In order to support the use of standard units of measurement, Hayes and Mahony has extended the Z typing system with standard units of measurement, e.g. time quantities are represented by the type

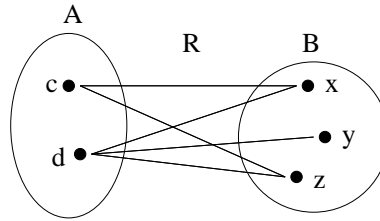
$$\mathbb{T} == \mathbb{R}s,$$

which represents real-valued time measured in seconds.

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Relations

A relation R from A to B , denoted by
 $R : A \leftrightarrow B$,
 is a subset of $A \times B$.



R is the set $\{(c, x), (c, z), (d, x), (d, y), (d, z)\}$

Notation: the predicates

$(c, z) \in R$ and $c \mapsto z \in R$ and $c \underline{R} z$

are equivalent.

$\text{dom } R$ is the set $\{a : A \mid \exists b : B \bullet a \underline{R} b\}$
 $\text{ran } R$ is the set $\{b : B \mid \exists a : A \bullet a \underline{R} b\}$

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Examples

$$\left| \begin{array}{l} - \leq - : \mathbb{N} \leftrightarrow \mathbb{N} \\ \hline \forall x, y : \mathbb{N} \bullet \\ x \leq y \Leftrightarrow \exists k : \mathbb{N} \bullet x + k = y \end{array} \right.$$

i.e. the relation \leq is the infinite subset

$\{(0, 0), (0, 1), (1, 1), (0, 2), (1, 2), (2, 2), \dots\}$

of ordered pairs in $\mathbb{N} \times \mathbb{N}$.

$$\left| \begin{array}{l} \text{divides} : \mathbb{N}_1 \leftrightarrow \mathbb{N} \\ \hline \forall x : \mathbb{N}_1; y : \mathbb{N} \bullet \\ x \underline{\text{divides}} y \Leftrightarrow \exists k : \mathbb{N} \bullet x k = y \end{array} \right.$$

$3 \underline{\text{divides}} 6$ but $\neg (3 \underline{\text{divides}} 7)$

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Domain and Range Restriction/Subtraction

Suppose $R : A \leftrightarrow B$ and $S \subseteq A$ and $T \subseteq B$; then

$S \triangleleft R$ is the set $\{(a, b) : R \mid a \in S\}$
 $R \triangleright T$ is the set $\{(a, b) : R \mid b \in T\}$

$S \triangleleft R$ is the set $\{(a, b) : R \mid a \notin S\}$
 $R \triangleright T$ is the set $\{(a, b) : R \mid b \notin T\}$

e.g. if

$has_sibling : People \leftrightarrow People$ then

$female \triangleleft has_sibling$ is the relation is_sister_of
 $has_sibling \triangleright female$ is the relation has_sister

$female \triangleleft has_sibling$ is the relation $is_brother_of$
 $has_sibling \triangleright female$ is the relation $has_brother$

Functions

A (partial) function f from a set A to a set B , denoted by

$$f : A \rightarrow B,$$

is a subset f of $A \times B$ with the property that for each $a \in A$ there is at most one $b \in B$ with $(a, b) \in f$. The function f is a *total* function, denoted

$$f : A \rightarrow B,$$

if and only if $\text{dom } f$ is the set A .

The predicates

$$(a, b) \in f \quad \text{and} \quad f(a) = b$$

are equivalent.

Examples:

$$\frac{\text{root} : \mathbb{N} \rightarrow \mathbb{N}}{\text{dom root} = \{n : \mathbb{N} \mid \exists m : \mathbb{N} \bullet m^2 = n\}} \\ \forall n : \text{dom root} \bullet (\text{root}(n))^2 = n$$

$$\frac{+ : \mathbb{N} \times \mathbb{N} \rightarrow \mathbb{N}}{\forall (n, m) : \mathbb{N} \times \mathbb{N} \bullet +(n, m) = n + m}$$

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Generic Definitions

$$\frac{[X, Y]}{\frac{\text{first} : X \times Y \rightarrow X}{\forall x : X; y : Y \bullet \text{first}(x, y) = x}}}$$

Exercise: Generic Purge Function

A generic purge function takes a time period (a timeout of type \mathbb{T}) and a set of time stamped elements of generic type X , and returns a set of updated time stamped elements. For example

$$ps(2s, \{(1s, a), (3s, b), (7s, c)\}) = \{(1s, b), (5s, c)\}$$

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Solution

$$\begin{array}{l} \overline{\overline{[X]}} \\ ps : (\mathbb{T} \times \mathbb{F}(\mathbb{T} \times X)) \rightarrow \mathbb{F}(\mathbb{T} \times X) \\ \overline{\forall t : \mathbb{T}; s : \mathbb{F}(\mathbb{T} \times X) \bullet} \\ ps(t, s) = \{(t_1, e) : \mathbb{T} \times X \mid \exists (t_o, e) : s \bullet t_o - t = t_1\} \end{array}$$

or

$$\begin{array}{l} \overline{\overline{[X]}} \\ \dots \\ ps(t, s) = \{(t_o, e) : s \mid t_o > t \bullet (t_o - t, e)\} \end{array}$$

Any difference between the two?

Sequences

A sequence s of elements from a set A , denoted

$$s : \text{seq } A,$$

is a function $s : \mathbb{N} \rightarrow A$ where $\text{dom } s = 1 \dots n$ for some natural number n . For example,

$$\langle b, a, c, b \rangle \text{ denotes the sequence (function) } \{1 \mapsto b, 2 \mapsto a, 3 \mapsto c, 4 \mapsto b\}$$

The empty sequence is denoted by $\langle \rangle$.

The set of all sequences of elements from A is denoted $\text{seq } A$ and is defined to be

$$\text{seq } A == \{s : \mathbb{N} \rightarrow A \mid \exists n : \mathbb{N} \bullet \text{dom } s = 1 \dots n\}$$

We define $\text{seq}_1 A$ to be the set of all non-empty sequences, i.e.

$$\text{seq}_1 A == \text{seq } A - \{\langle \rangle\}$$

Notice that: $\langle a, b, a \rangle \neq \langle a, a, b \rangle \neq \langle a, b \rangle$

Special Functions for Sequences

Concatenation

$$\langle a, b \rangle \hat{\ } \langle b, a, c \rangle = \langle a, b, b, a, c \rangle$$

Head, Last

$$\left| \begin{array}{l} \text{head, last} : \text{seq}_1 A \rightarrow A \\ \hline \forall s : \text{seq}_1 A \bullet \text{head}(s) = s(1) \wedge \text{last}(s) = s(\#s) \end{array} \right.$$

$$\text{head}\langle c, b, b \rangle = c \quad \text{last}\langle c, b, b \rangle = b$$

Tail

$$\left| \begin{array}{l} \text{tail} : \text{seq}_1 A \rightarrow \text{seq } A \\ \hline \forall s : \text{seq}_1 A \bullet \langle \text{head}(s) \rangle \hat{\ } \text{tail}(s) = s \end{array} \right.$$

$$\text{tail}\langle c, b, b \rangle = \langle b, b \rangle$$

Combining Formal Specification with Object-Oriented Design

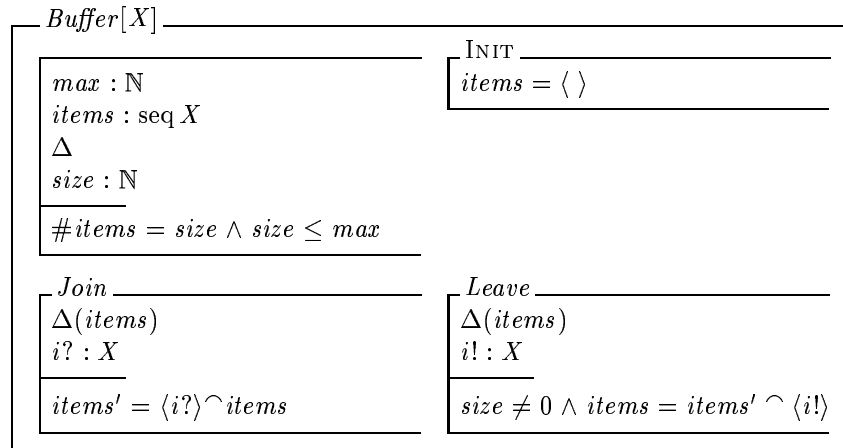
goes against the conventional view of separating the concerns of functionality and design, but

- adds clarity and leads to simplification of large systems;
- helps with system abstraction and suggests a refinement into object-oriented code.

Object-Z is an extension to Z developed at the University of Queensland. It supports the design of object-oriented design.

- R. Duke and G. Rose, Formal Object Oriented Specification Using Object-Z. Cornerstones of Computing Series (editors: R. Bird, C.A.R. Hoare), Macmillan Press, March 2000.
- G. Smith, The Object-Z Specification Language, Kluwer Academic Publishers, 2000.

Object-Z Basics: Buffer Example



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Exercise: For this *Buffer* class specify:

- (a) an operation *count* which, given a message, outputs the number of times that message occurs in the buffer;
- (b) an operation *duplicate* which appends to the buffer the message currently at the head of the buffer, provided the buffer is not empty or already full;
- (c) an operation *titanic* whereby a sequence of messages is appended to the buffer except those messages for which there is no room are discarded (the buffer is like a life-boat on the Titanic: people queue to get on, but once the boat is full all the remaining people are left behind);
- (d) an operation *penguin* whereby, like the operation *titanic*, a sequence of messages is input to the buffer, but this time the messages on the end of the sequence are accepted while those at the front are discarded if there is no room (the messages are acting like penguins, pushing out the messages already in the buffer once the buffer is full).

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Solution

| |
|--|
| $count$ $m? : X$ $count! : \mathbb{N}$ |
| $count! = \#(items \triangleright \{m?\})$ |

| |
|---|
| $duplicate$ $\Delta(items)$ |
| $\#items \in 1..(max - 1)$ $items' = items \hat{\ } \langle head\ items \rangle$ |

| |
|---|
| $titanic$ $\Delta(items)$ $s? : seq\ X$ |
| $items' = (1..max) \triangleleft (items \hat{\ } s?)$ |

| |
|--|
| $penguin$ $\Delta(items)$ $s? : seq\ X$ |
| $\exists s : seq\ X \bullet$ $s \hat{\ } items' = items \hat{\ } s?$ $s \neq \langle \rangle \Rightarrow \#items' = max$ |

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Two Linked Buffers (single thread)

| | |
|---|--------------------------------------|
| $TwoBuffers[X]$ | $INIT$ $b_1.INIT \wedge b_2.INIT$ |
| $b_1, b_2 : Buffer[X]$ $b_1 \neq b_2$ | |
| $Join \hat{=} b_1.Join$ $Leave \hat{=} b_2.Leave$ $Transfer \hat{=} b_1.Leave \parallel b_2.Join$ | |

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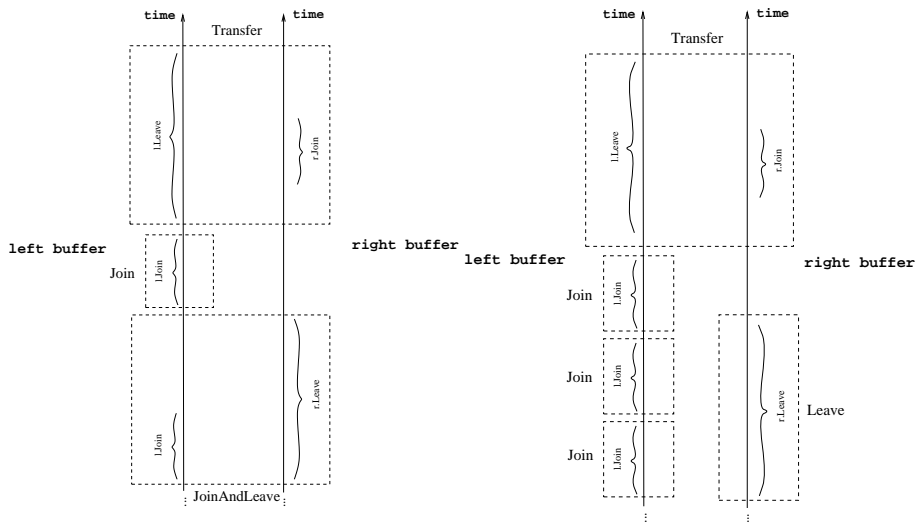


Figure 1: passive events

Figure 2: active events

CSP/Timed CSP

- Hoare's CSP (Communicating Sequential Processes) an *event* based notation primarily aimed at describing the sequencing of behaviour within a process and the synchronisation of behaviour (or *communication*) between processes.
- Timed CSP extends CSP by introducing a capability to quantify temporal aspects of sequencing and synchronisation.
- S. Schneider. Concurrent and Real-time Systems: The CSP Approach, Wiley, 1999.
- A.W. Roscoe. The Theory and Practice of Concurrency. Prentice-Hall, 1997.
- J. Davies, Specification and Proof in Real-Time CSP, Cambridge University Press, 1993.
- C. A. R. Hoare. Communicating Sequential Processes. Prentice-Hall International, 1985.

Events

A process engages in *events*; each event is an atomic action. e.g. the events for a vending machine are

coin—insert a coin

choc—extract a chocolate

The set of events that a process can possibly engage in is the *alphabet* of the process e.g. the alphabet of the vending machine is

$\{coin, choc\}$

Traces

A *trace* is a finite sequence of events.

A (deterministic) process is specified by the set of traces denoting its possible behaviour. e.g. the traces of the vending machine:

$\langle \rangle$
 $\langle coin \rangle$
 $\langle coin, choc \rangle$
 $\langle coin, choc, coin \rangle$
...

Any execution of the process will be one of these sequences. If $s \hat{=} t$ is a trace of a process,

then so also is s ;

i.e. the set of traces is prefix closed.

Examples

- (1) $STOP_A$ is the process with alphabet A that can do nothing.

$$traces(STOP_A) = \{\langle \rangle\}$$

- (2) $CLOCK$ is the process with $\alpha CLOCK = \{tick\}$ which can 'tick' at any time.

$$traces(CLOCK) = tick^*$$

- (3) VM is the process with $\alpha VM = \{coin, choc\}$ which repeatedly supplies a chocolate after a coin is inserted.

$$traces(VM) = \{s : seq\{coin, choc\} \mid \exists n : \mathbb{N} \bullet s \leq \langle coin, choc \rangle^n\}$$

- (4) $WALK$ is a one-dimensional random walk process with $\alpha WALK = \{left, right\}$.

$$traces(WALK) = (left \cup right)^*$$

Prefix

A process which may participate in event a then act according to process description P is written

$$a@t \rightarrow P(t).$$

The event a is initially enabled by the process and occurs as soon as it is requested by its environment, all other events are refused initially. The event a is sometimes referred to as the *guard* of the process. The (optional) timing parameter t records the time, relative to the start of the process, at which the event a occurs and allows the subsequent behaviour P to depend on its value. For examples:

$$VMU = coin \rightarrow STOP$$

$$SHORTLIFE = (beat \rightarrow (beat \rightarrow STOP)) = beat \rightarrow beat \rightarrow STOP$$

$$VMS = coin \rightarrow choc \rightarrow STOP$$

Understanding Timed Prefix

Let P be a process which has two free time variables t_1 and t_2 . A possible execution of the prefix:

$$\begin{aligned} & a@t_1 \rightarrow b@t_2 \rightarrow P \\ & \downarrow 3 \text{ (time passed)} \\ & a@t_1 \rightarrow b@t_2 \rightarrow P[(t_1 + 3)/t_1] \\ & \downarrow a \text{ (event occur)} \\ & b@t_2 \rightarrow P[3/t_1] \\ & \downarrow 4 \text{ (time passed)} \\ & b@t_2 \rightarrow P[3/t_1][(t_2 + 4)/t_2] \\ & \downarrow b \text{ (event occur)} \\ & P[3/t_1][4/t_2] \end{aligned}$$

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Other CSP/Timed-CSP primitives:

- $P; Q$ (sequential composition)
- $P \parallel [X] Q$ (synchronous), $P \parallel\parallel Q$ (asynchronous)
- $a \rightarrow P \square b \rightarrow Q$ (external choice), $a \rightarrow P \sqcap b \rightarrow Q$ (internal choice)
- $P_1 \nabla e \rightarrow P_2$ (interrupt process)
- $\text{WAIT } t; P$ (delay), $a \rightarrow P \triangleright \{t\} Q$ (time-out)

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Sequential Composition

- The second form of sequencing is process sequencing. A distinguished event \checkmark is used to represent and detect process termination.
- The sequential composition of P and Q , written $P; Q$, acts as P until P terminates by communicating \checkmark and then proceeds to act as Q .
- The termination signal is hidden from the process environment and therefore occurs as soon as enabled by P . The process which may only terminate is written SKIP.

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Parallel composition

The parallel composition of processes P and Q , synchronised on event set X , is written

$$P \parallel [X] \parallel Q.$$

No event from X may occur in $P \parallel [X] \parallel Q$ unless enabled jointly by both P and Q . When events from X do occur, they occur in both P and Q simultaneously and are referred to as *synchronisations*. Events not from X may occur in either P or Q separately but not jointly. For example, in the process described by

$$(a \rightarrow P) \parallel [a] \parallel (c \rightarrow a \rightarrow Q)$$

all a events must be synchronisations between the two processes.

In an asynchronous parallel combination

$$P \parallel \parallel Q$$

both components P and Q execute concurrently without any synchronisations.

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Choice

Diversity of behaviour is introduced through two choice operators.

The external choice operator allows a process a choice of behaviour according to what events are requested by its environment. The process

$$(a \rightarrow P) \square (b \rightarrow Q)$$

begins with both a and b enabled. The environment chooses which event actually occurs by requested one or the other first. Subsequent behaviour is determined by the event which actually occurred, P after a and Q after b respectively.

Internal choice represents variation in behaviour determined by the internal state of the process. The process

$$a \rightarrow P \sqcap b \rightarrow Q$$

may initially enable either a , or b , or both, as it wishes, but must act subsequently according to which event actually occurred. The environment cannot affect internal choice.

Channel

A channel is a collection of events of the form $c.n$: the prefix c is called the *channel name* and the collection of suffixes is called the *values* of the channel.

When an event $c.n$ occurs it is said that *the value n is communicated on channel c* . When the value of a communication on a channel is determined by the environment (external choice) it is called an *input* and when it is determined by the internal state of the process (internal choice) it is called an *output*.

It is convenient to write $c?n : N \rightarrow P(n)$ to describe behaviour over a range of allowed inputs instead of the longer $\square n : N \bullet c.n \rightarrow P(n)$. Similarly the notation $c!n : N \rightarrow P(n)$ is used instead of $\sqcap n : N \bullet c.n \rightarrow P(n)$ to represent a range of outputs.

e.g.

$$COPYBIT = in.0 \rightarrow out.0 \rightarrow COPYBIT \square in.1 \rightarrow out.1 \rightarrow COPYBIT$$

$$\alpha COPYBIT = \{in.0, out.0, in.1, out.1\}$$

Interrupt

The interrupt process $P_1 \nabla e \rightarrow P_2$ behaves as P_1 until the first occurrence of interrupt event e , then the control passes to P_2 .

Recursion

Recursion is used to give finite representations of non-terminating processes. The process expression

$$\mu P \bullet a?n : \mathbb{N} \rightarrow b!f(n) \rightarrow P$$

describes a process which repeatedly inputs a natural on channel a , calculates some function f of the input, and then outputs the result on channel b .

Timeout

The timeout construct passes control to an exception handler if no event has occurred in the primary process by some deadline.

The process

$$(a \rightarrow P) \triangleright\{t\} Q$$

will try to perform $a \rightarrow P$, but will pass control to Q if the a event has not occurred by time t , as measured from the invocation of the process. For example,

$$MayPrint1 = (receive \rightarrow print \rightarrow STOP) \triangleright\{60\} shutdown \rightarrow STOP$$

$$MP1(t) = (receive \rightarrow print \rightarrow STOP) \triangleright\{60 - t\} shutdown \rightarrow STOP$$

Exercise: Transmitter

A transmitter which repeatedly send a given message x until it receives and acknowledgement. Assume that the transmitter is in an environment which is always ready to accept a *send* message, then it will send the message every 5 time units until an *ack* message is received. (hint using recursion together with timeout).

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Solution

$$\text{Transmit}(x) = \text{send!}x \rightarrow ((\text{ack} \rightarrow \text{STOP}) \triangleright \{5\} \text{Transmit}(x))$$

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Delay

A process which allows no communications for period t then terminates is written $\text{WAIT } t$. The process

$$\begin{aligned}\text{WAIT } t; P &= \text{STOP} \triangleright \{t\} P \\ a \xrightarrow{t} P &= a \rightarrow \text{WAIT } t; P = a \rightarrow (\text{STOP} \triangleright \{t\} P)\end{aligned}$$

is used to represent P delayed by time t .

State parameters

In general, the behaviour of a process at any point in time may be dependent on its internal state and this may conceivably take an infinite range of values.

It is often not possible to provide a finite representation of a process without introducing some notation for representing this internal process state.

The approach adopted by CSP is to allow a process definition to be parameterised by state variables. Thus a definition of the form

$$P_{n:N} \hat{=} Q(n)$$

represents a (possibly infinite) family of definitions, one for each possible value of n .

There is no inherent notion of process state in CSP, but rather these annotations are a convenient way to provide a finite representation of an infinite family of process descriptions.

Exercise: A generic timed-collection

The generic timed-collection denotes a collection of elements of type X with a time stamp. Operations are allowed to add elements to and delete elements from the collection. When deleting an element from the collection, the oldest element should be removed and output to the environment. The collection has the following timing properties. Firstly, that it updates the internal state during a *add* or *delete* operation. Secondly, each element of the collection becomes *stale* if it is not passed on within t_o time units of being added to the collection. Stale elements should never be passed on, but are instead purged from the collection upon becoming stale.

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Solution

$$\text{TimedCollection} \hat{=} TC_{\emptyset}.$$

$$TC_{\emptyset} \hat{=} \text{left}?e : X \rightarrow TC_{\{(t_o, e)\}}$$

$$TC_{\{(t, a)\} \cup s} \hat{=} \\ (\text{left}?e : X @t_i \rightarrow TC_{ps(t_i, \{(t, a)\} \cup s) \cup \{(t_o, e)\}} \square \\ \text{right}!a @t_i \rightarrow TC_{ps(t_i, s)} \triangleright \{t\} TC_{ps(t, s)})$$

where $(t, a) = \text{find_oldest}(\{(t, a)\} \cup s)$.

| |
|--|
| $\text{find_oldest} : \mathbb{P}_1(\mathbb{T} \times X) \rightarrow (\mathbb{T} \times X)$ |
| $\forall s : \mathbb{P}_1(\mathbb{T} \times X) \bullet \\ \exists (t, e) : s \bullet t = \min(\text{dom } s) \\ \text{find_oldest}(s) = (t, e)$ |

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Object-Z and Timed CSP

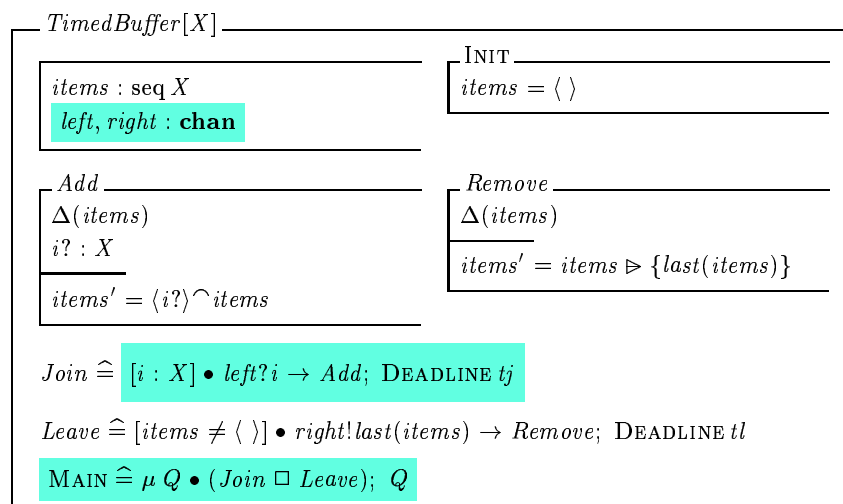
- Object-Z
 - ✓ an excellent tool for modeling data states
 - ✗ but difficult for modelling real-time concurrent systems
- Timed CSP
 - ✓ Good for specifying the timed process and communication
 - ✗ Like CSP, cumbersome to capture the data state of a complex system
- Timed Communicating Object Z: a blending of Object-Z and Timed CSP

Related Work

- * Z/OZ with CSP: Fischer, Smith, Derick, Suhl, Bolton, Davies, Woodcock ...
- * Z with CCS: Galloway, Stoddart, Taguchi, Araki ...

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Timed Communicating Object Z (TCOZ)



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Abstract syntax

| | |
|--|--------------------------------|
| $[ZS, ZE]$ | [Z schemas and expressions] |
| $TZE ::=$ $ref \langle \langle NAME \rangle \rangle \mid STOP \mid SKIP \mid WAIT \langle \langle ZE \rangle \rangle \mid (- \bullet -) \langle \langle ZS \times TZE \rangle \rangle \mid$ $(- \rightarrow -) \langle \langle \Sigma \times TZE \rangle \rangle \mid (- _ \rightarrow -) \langle \langle \Sigma \times ZE \times TZE \rangle \rangle \mid$ $(- \square -) \langle \langle TZE \times TZE \rangle \rangle \mid \dots \mid (\mu - \bullet -) \langle \langle NAME \times TZE \rangle \rangle$ | [TCOZ constructor expressions] |
| $TZB == \{ \mathcal{C} : NAME \mapsto TZE \mid$ $\quad \forall tze : ran \mathcal{C}; N : NAME \bullet$ $\quad N \in sig \ tze \Rightarrow N \in dom \mathcal{C} \}$ | [TCOZ class body] |
| $TZC_p == [init : ZS; \mathcal{C} : TZB]$ | [passive classes] |
| $TZC_a ==$ $[init : ZS; \mathcal{C} : TZB; main : TZE \mid sig \ main \subseteq dom \ TZB]$ | [active classes] |

TCOZ Semantics

The support of timing primitives in TCOZ is made possible through the adoption of Reed's timed-failures semantics for Timed CSP. The timed-failures semantics models CSP processes in terms of timed event-traces and timed event-failures. This semantic model allows CSP to be extended with time related primitives such as delays, timeouts, and clock-interrupts. In order to support objects with encapsulated state this model is extended to include an initial state and state update events. Object-Z operations are modelled as terminating sequences of timed state-update events.

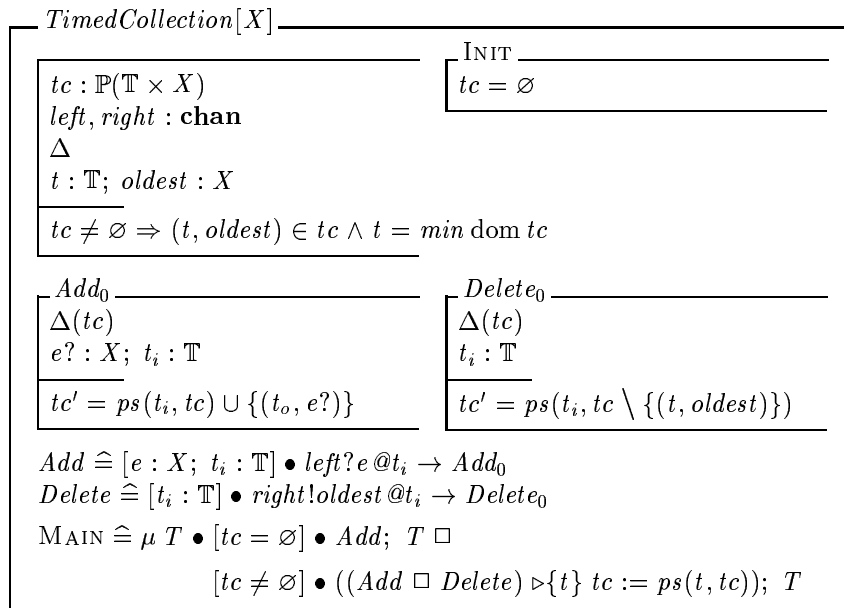
- B. Mahony and J.S. Dong. Overview of the Semantics of TCOZ. *Integrated Formal Methods (IFM'99)*, pages 66-85, Springer-Verlag, York, UK, June 1999.

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Solution

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The Notion of Active Object

- Active objects have their own thread of control.
- Passive objects are controlled by other objects in a system.
- A class for defining active objects is called an *active class*
- A class for defining passive objects is called a *passive class*.
- In TCOZ, MAIN, a non-terminating process definition, distinguishes the active and the passive classes.

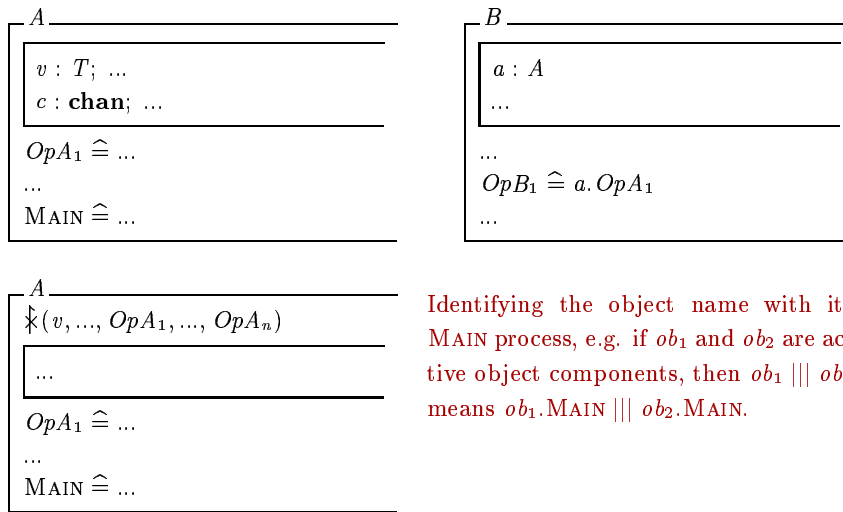
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Inheritance between active/passive classes

- When a new active class is derived from an existing active class, the MAIN process must always be redefined explicitly.
- A new active class can be derived from an existing passive class, in this case, a MAIN process definition needs to be added.
- A new passive class can also be derived from an existing active class, in this case, the MAIN process of the existing class is implicitly removed.
- A new passive class can be derived from an existing passive class following the same rules as the standard Object-Z.

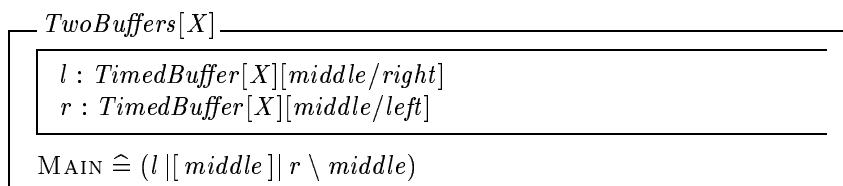
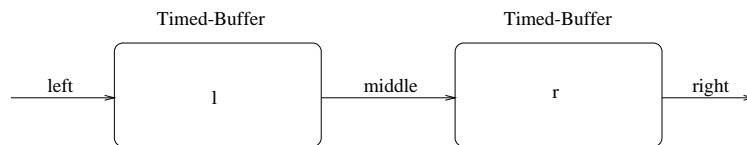
49

Composition and interaction of active objects



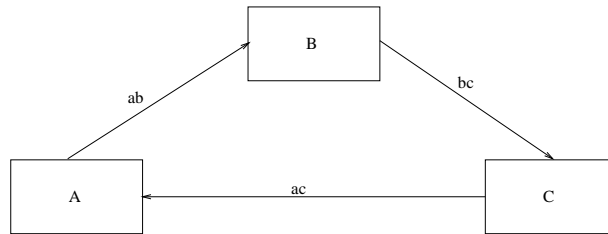
50

Two Communicating Timed Buffers



51

Complex network topologies



$$(A[bc'/bc] \parallel [ab, ac]) \parallel (B[ac'/ac] \parallel [bc]) \parallel C[ab'/ab] \setminus ab, ac, bc [ab, ac, bc/ab', ac', bc']$$

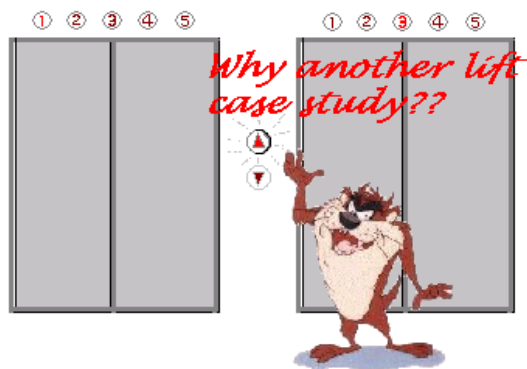
$$\left(\left\| v_1, v_2, v_3 \dots \bullet v_1 \xleftrightarrow{ch_{12}} v_2, v_2 \xleftrightarrow{ch_{23}} v_3, v_3 \xleftrightarrow{ch_{13}} v_1, \dots \right\| (A, B, C) \right)$$

$$\left\| (A \xleftrightarrow{ab} B, B \xleftrightarrow{bc} C, C \xleftrightarrow{ca} A) \right\| \text{ or } \left\| (A \xleftrightarrow{ab} B \xleftrightarrow{bc} C \xleftrightarrow{ca} A) \right\|$$

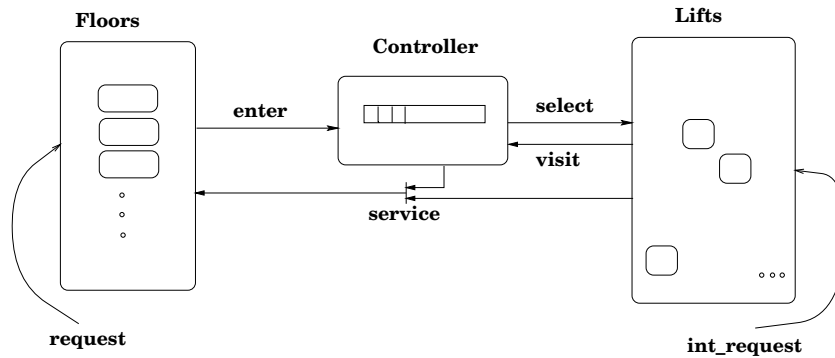
52

The Lift Case Study

- Multi-floors with multi-elevators
- Non-trivial
- Commonly used example
- Both CSP and Object-Z have been applied (but no real-time issues)



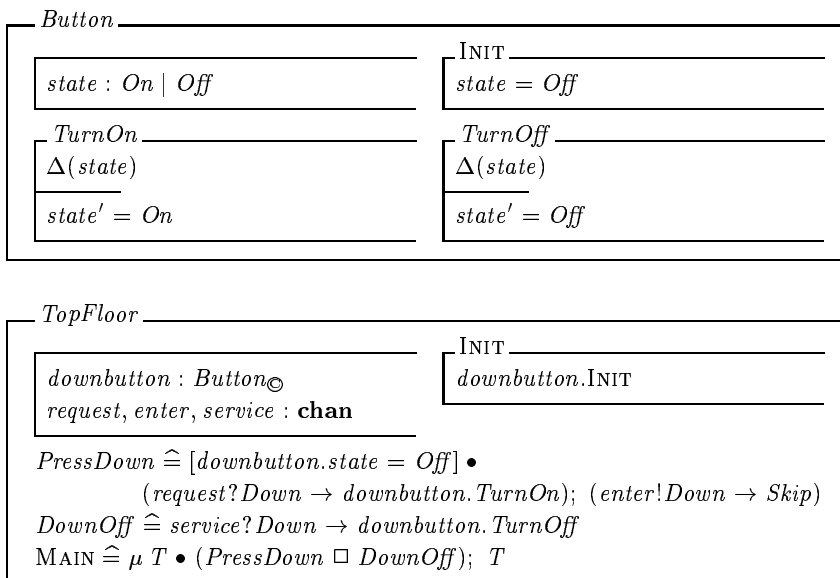
53

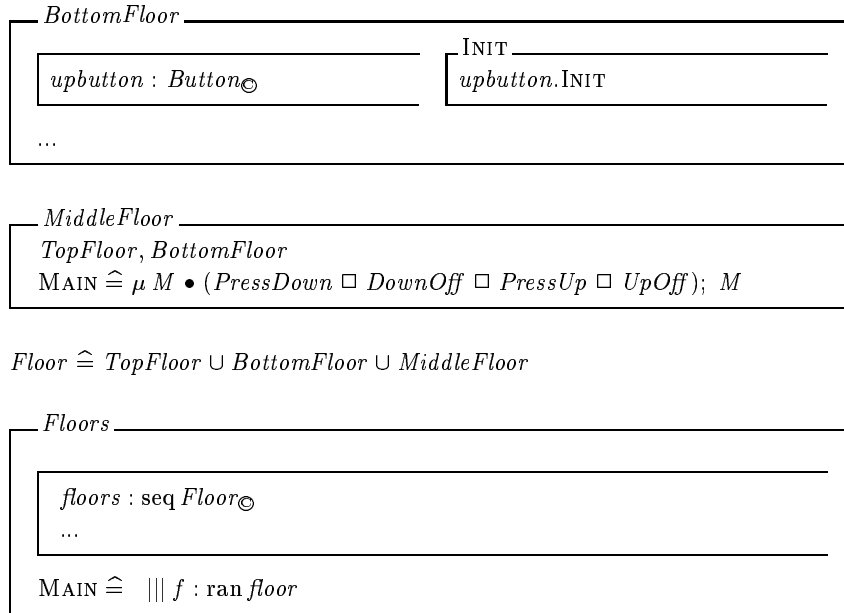


Detailed model can be found at:

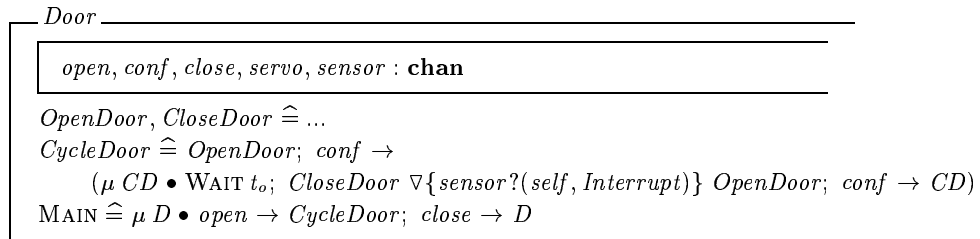
B. Mahony and J.S. Dong. Timed Communicating Object Z. IEEE Transactions on Software Engineering, 26(2):150-177, Feb 2000.

<http://www.comp.nus.edu.sg/~dongjs/papers/tse00.ps>

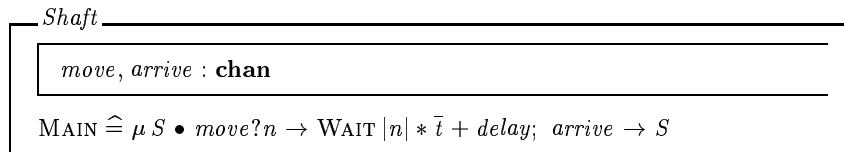




Lift door control



Moving the lift



Internal_Q

panel : seq *Button*_⊙
int_request, int_sched, int_serv : **chan**

NextUp, NextDown, MAIN ≐ ...

LiftControl

fl : **N**
md : *MoveDirection*
move, arrive : **chan** [shaft]
 ...

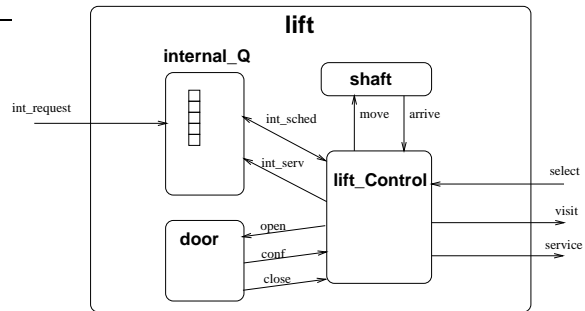
...
MAIN ≐ μ *LC* •
 ... → *Internal*; *LC* □
 ... → *External*; *LC*

58

Lift

iq : *Internal_Q*_⊙
lc : *LiftControl*_⊙
s : *Shaft*_⊙
d : *Door*_⊙

MAIN ≐ || (
lc $\xleftrightarrow{\text{move, arrive}}$ *s*;
lc $\xleftrightarrow{\text{open, close, conf}}$ *d*;
lc $\xleftrightarrow{\text{int_sched, int_serv}}$ *iq*)



Lifts

lifts : \mathbb{P} *Lift*_⊙

MAIN ≐ ||| *l* : *lifts*

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Controller

$requests : seq(\mathbb{N} \times MoveDirection)$
 $enter, select, visit, service : \mathbf{chan}$

Join

...

Remove

...

$Dispatch \hat{=} [...] \bullet select!item \rightarrow Remove$

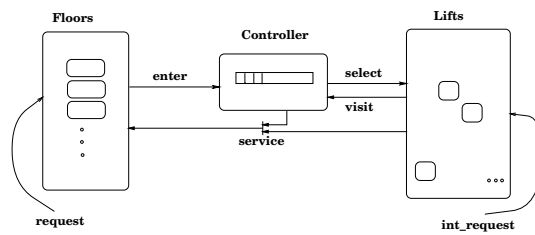
$CheckServ \hat{=}$

$[item : \mathbb{N} \times MoveDirection] \bullet visit?item \rightarrow \dots$

$MAIN \hat{=} \mu C \bullet (Join \square Dispatch \square CheckServ); C$

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The Lift System



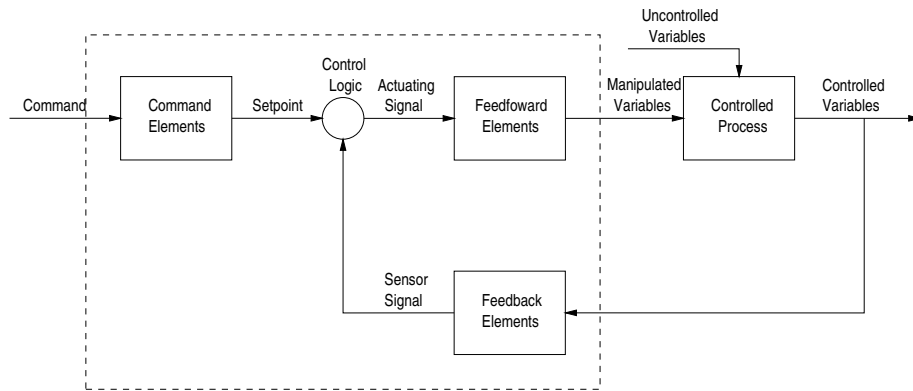
LiftSystem

$fs : Floors \circledast$
 $ls : Lifts \circledast$
 $contr : Controller \circledast$

$MAIN \hat{=} \parallel (fs \xrightarrow{enter} contr \xrightarrow{select, check} ls \xrightarrow{service} fs)$

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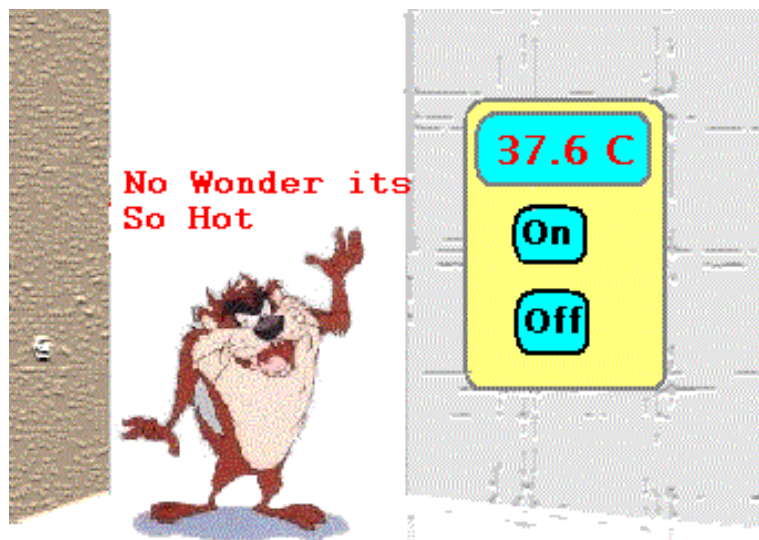
Sensors and Actuators — Control Systems



- × CSP channel mechanism is discrete
- × CSP channel mechanism is synchronous

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Example: Digital Temperature Display



63

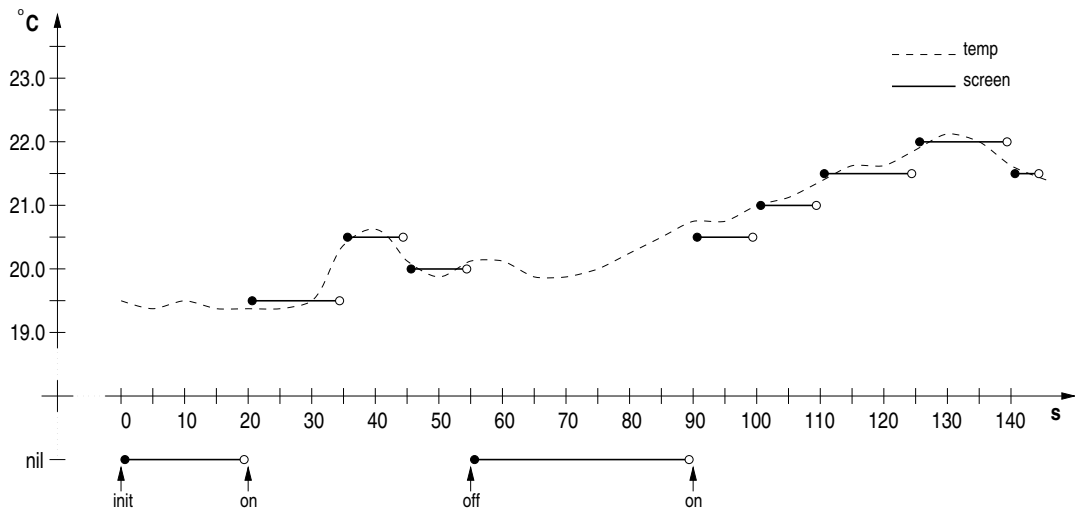


Figure 3: The office communication scenario.

64

$temp : \mathbb{R}^{\circ C} \text{ sensor}, \quad == \quad temp : \mathbb{R} s \rightarrow \mathbb{R}^{\circ C}.$

Internally, $temp$ takes the syntactic role of a CSP channel. Whenever a value v is communicated on the internal channel at a time t , $temp(t) = v$.

$screen : Display \text{ actuator},$

where

$Display ::= Temp \langle \langle \mathbb{N} * 0.5^{\circ C} \rangle \rangle \mid nil.$

The internal role is that of the local state variable.

65

DTD

$temp : \mathbb{R} \text{ sensor}$
 $screen : \text{Display actuator}$
 $on, off : \text{chan}$

INIT
 $screen = nil$

SetScreen

$\Delta(screen)$
 $t? : \mathbb{R}^\circ \text{C}$

$\exists dt : \mathbb{N} * 0.5^\circ \text{C} \bullet$
 $dt = t \pm 0.5^\circ \text{C} \wedge$
 $screen' = Temp(dt)$

$Show \hat{=} ([t : \mathbb{R}^\circ \text{C}] \bullet temp?t \rightarrow SetScreen; DEADLINE 5 \text{ s}; WAITUNTIL 5 \text{ s}; Show)$
 $\nabla off \rightarrow NoShow$

$NoShow \hat{=} screen := nil; on \rightarrow Show$

$MAIN \hat{=} on \rightarrow Show$

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Asynchronous active object

Synchronous active objects

- have discrete interfaces, synchronous channels;
- are highly dependent.

Asynchronous active objects

- have analog interfaces, asynchronous sensor/actuators;
- are highly independent;
- can be further classified into **periodic** and non-periodic objects.

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Exercise: a calendar clock

A typical periodic object: a calendar clock ticks every second ...

$CalendarTime == N_{yr} \times N_{mn} \times N_{dy} \times N_{hr} \times N_{min} \times N_s.$

$Convert : N_s \rightarrow CalendarTime$

...

[detail of function omitted]

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Solution

Clock

$per == 1\text{ s}$
 $gain == 50\text{ ms}$

$total : N_s$

Δ

$display : CalendarTime$ **actuator**

$display = Convert(total)$

Inc

$\Delta(total)$

$total' = total + 1$

$MAIN \hat{=} \mu C \bullet Inc; DEADLINE\ gain; WAITUNTIL\ per; C$

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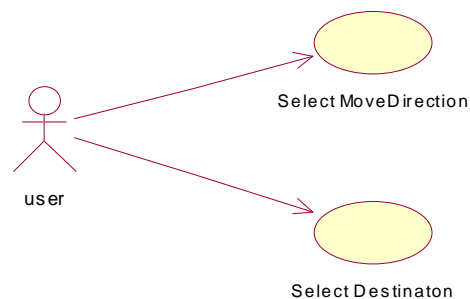
UML

- UML stands for Unified (?) Modeling Language
- The UML combines/collects Data Modeling concepts (Entity Relationship Diagrams), Business Modeling (work flow), Object Modeling, and Component Modeling
- The UML is the OMG standard language for visualising, specifying, constructing, and documenting the artifacts of a software-intensive system
- UML consists of use case, class, statechart, collaboration diagrams ...

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Use Case Diagram

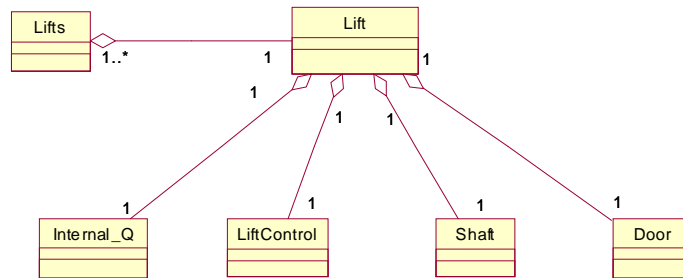
- Use case is a pattern of behavior the system exhibits Each use case is a sequence of related transactions performed by an actor and the system in a dialogue. Actors are examined to determine their needs. Use case diagrams are created to visualise the relationships between actors and use cases



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Class Diagram

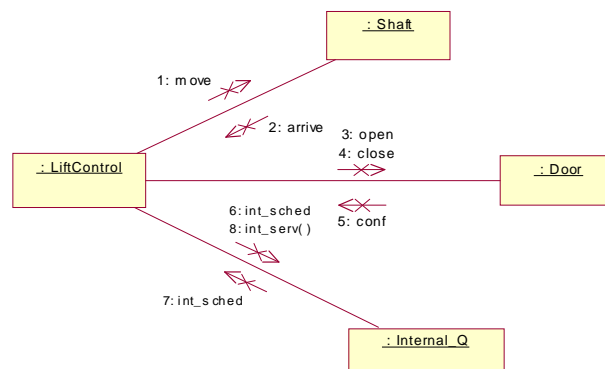
- A class diagram shows the existence of classes and their relationships in the logical view of a system. It consists of classes and their structure and behavior, association, aggregation, dependency, and inheritance relationships, multiplicity and navigation indicators, and role names



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Collaboration Diagram – dynamic behavior, message-oriented

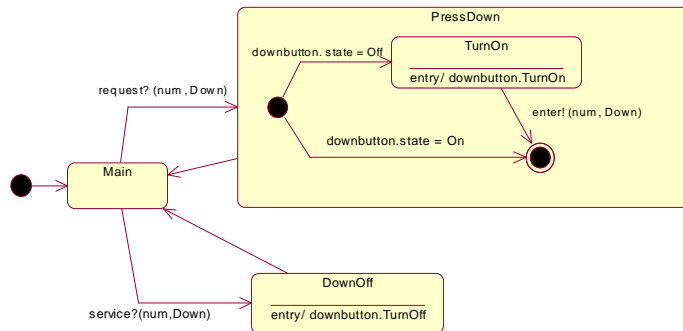
- A collaboration diagram displays object interactions organised around objects and their links to one another



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Statechart Diagram – dynamic behavior, event-oriented

- A statechart diagram shows the life history of a given class, the events that cause a transition from one state to another, and the actions that result from a state change



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Shortcomings of UML

- There is no unified formal semantics for all those diagrams. There are a few approaches to formalize a subset of UML, e.g. (Evans and Clark, 1998, Kim and Carrington, 1999) concentrated on class diagram semantics. Therefore, the consistency between diagrams is problematic; and
- There are limited capabilities for precisely modeling timed concurrency. For example, (in a new feature that has been added to the UML 1.3) synchronisation between concurrent substates of a single statechart diagram can be captured using a synch state link. However there is no facility to precisely model synchronous interactions between states in two different statechart diagrams.

75

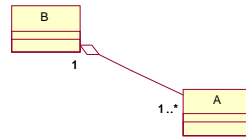
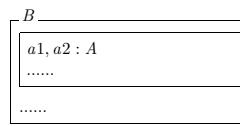
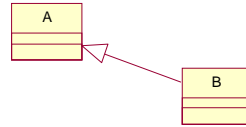
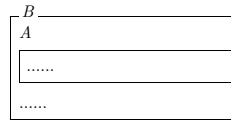
Linking TCOZ and UML

- Syntactically, UML/OCL (Object Constraint Language) is extended with TCOZ communication interface types — chan, sensor and actuator. Upon that, TCOZ sub-expressions can be used (in the same role as OCL) in the statechart diagrams and collaboration diagrams.
- Semantically, UML class diagrams are identified with the signatures of the TCOZ classes. The states of the UML statechart are identified with the TCOZ processes (operations) and the state transition links are identified with TCOZ events/guards.
- Effectively, UML diagrams can be seen as the viewpoint visual projections from a formal TCOZ model.

Combination Process of TCOZ and UML

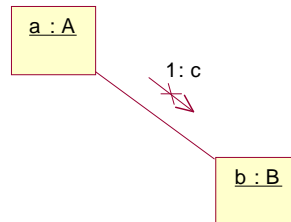
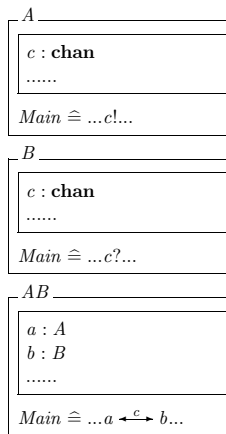
1. Firstly, the UML use-case models (user-case and collaboration diagrams) are used to analyse system requirements so that main classes and operations will be identified (e.g. classification of the boundary and control classes). Communication links of the collaboration diagrams guide the design of communication interfaces of the TCOZ model (synchronisation — channel, synchronisation — sensor/actuator).
2. Then, the UML class diagrams are used to capture the static structure of the system, in which class/object relationships can be captured.
3. Based on UML class diagrams, detailed TCOZ formal models are constructed in a bottom-up style. The states, timing and concurrent interactions of the system objects are captured precisely in the TCOZ models.
4. Finally, UML state diagrams are used to visualize the behaviors (process states and events) of essential components of the system, which are closely associated with the behavior parts of the TCOZ model.

Class



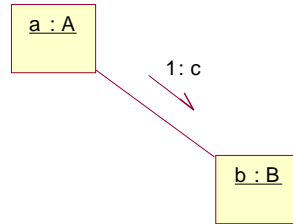
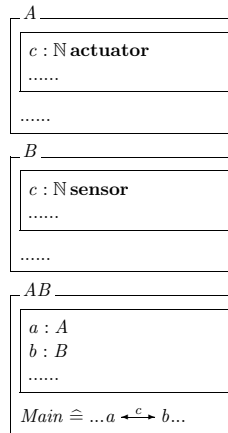
78

Synchronisation



79

Asynchronisation



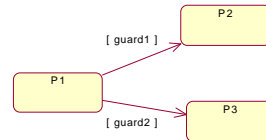
80

Dynamic Behavior

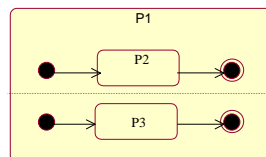
$P1; e \rightarrow P2$



$P1; ([guard1] \bullet P2 \square [guard2] \bullet P3)$



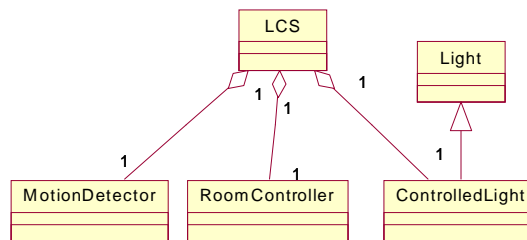
$P1 \hat{=} P2 ||| P3$



81

Light Control System (LCS)

In most existing light control systems, all lights are controlled manually. Electrical energy is wasted by lighting rooms that are not occupied and by not adjusting light levels relative to need and daylight. LCS is an intelligent control system. It can detect the occupation of the building, then turn on or turn off the lights automatically. It is able to tune illumination in the building according to the outside light level. It gains input from sensors and actuators.



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Illumination == 1..10000 lux
Percent == {0} ∪ 10..100

MotionDetector

motion : chan
md : (*Move* | *NoMove*) sensor [motion sensor]

NoUser ≡ *md?Move* → *motion!1* → *User* □
md?NoMove → WAIT 1 s; *NoUser*
User ≡ *md?NoMove* → *motion!0* → *NoUser* □
md?Move → WAIT 1 s; *User*
 MAIN ≡ *NoUser*

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Light

$dim : \text{Percent}$ **actuator** [dim value]
 $on : \mathbb{B}$

$\text{TurningOn} \hat{=} dim := 100; on := true$

$\text{TurningOff} \hat{=} dim := 0; on := false$

ControlledLight

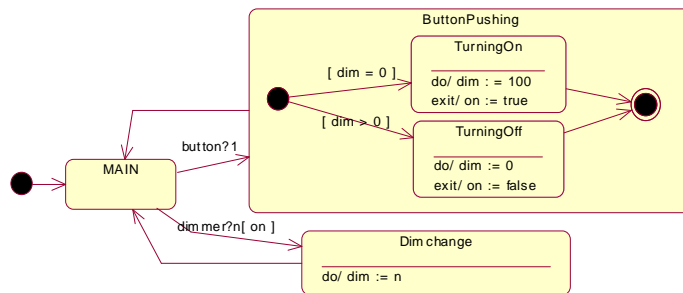
Light

$button, dimmer : \text{chan}$ [control channels]

$\text{ButtonPushing} \hat{=} button?1 \rightarrow$
 $(([dim > 0] \bullet \text{TurningOff} \sqcap [dim = 0] \bullet \text{TurningOn}))$

$\text{DimChange} \hat{=} [n : \text{Percent}] \bullet dimmer?n \rightarrow$
 $([on] \bullet dim := n \sqcap [\neg on] \bullet \text{SKIP})$

$\text{MAIN} \hat{=} \mu N \bullet (\text{ButtonPushing} \sqcap \text{DimChange}); N$



| $satisfy : Percent \leftrightarrow Illumination$

RoomController

dimmer, motion : chan
odsensor : Illumination sensor
absenT : T
olight : Illumination

Adjust
dim! : Percent on dimmer
dim! satisfy olight

$Ready \hat{=} motion?1 \rightarrow On$

$Regular \hat{=} \mu R \bullet [n : Illumination] \bullet$

$\quad odsensor?n \rightarrow olight := n; Adjust; dimmer!dim \rightarrow R$

$On \hat{=} Regular \nabla motion?0 \rightarrow OnAgain$

$OnAgain \hat{=} (motion?1 \rightarrow On) \triangleright \{absenT\} Off$

$Off \hat{=} dimmer!0 \rightarrow Ready$

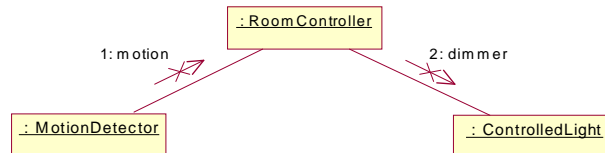
$MAIN \hat{=} Off$

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LCS

m : MotionDetector
l : ControlledLight
r : RoomCtrller

$MAIN \hat{=} \parallel (m \xrightarrow{motion} r \xrightarrow{dimmer} l)$



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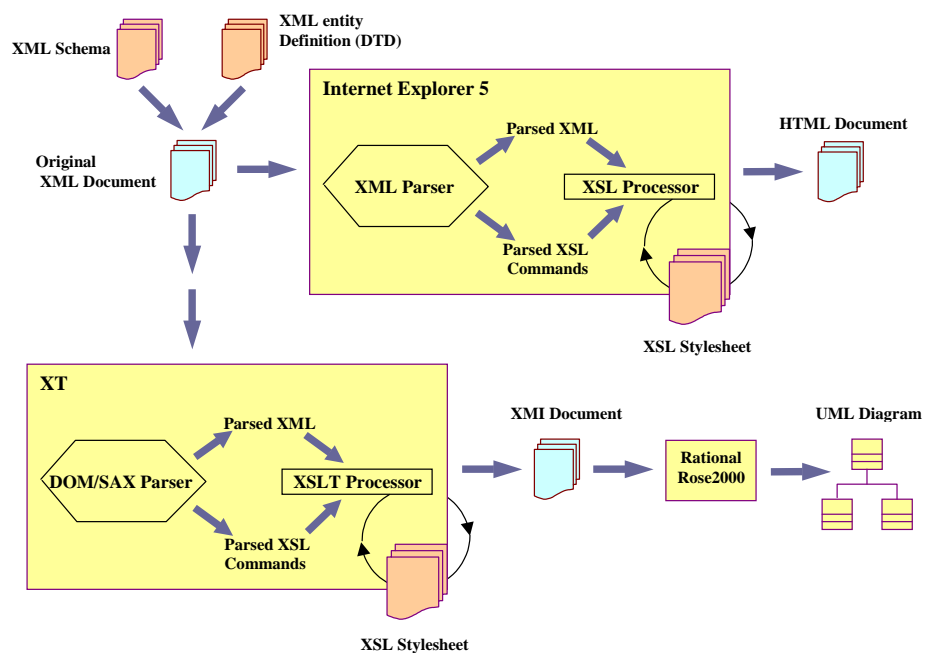
Z Family on the Web with their UML Photos

- Use eXtensible Markup Language (XML) to develop web environment for Z family languages
 - share design models
 - hyperlinks among models
 - advance browsing facilities

<http://nt-appn.comp.nus.edu.sg/fm/zml/>

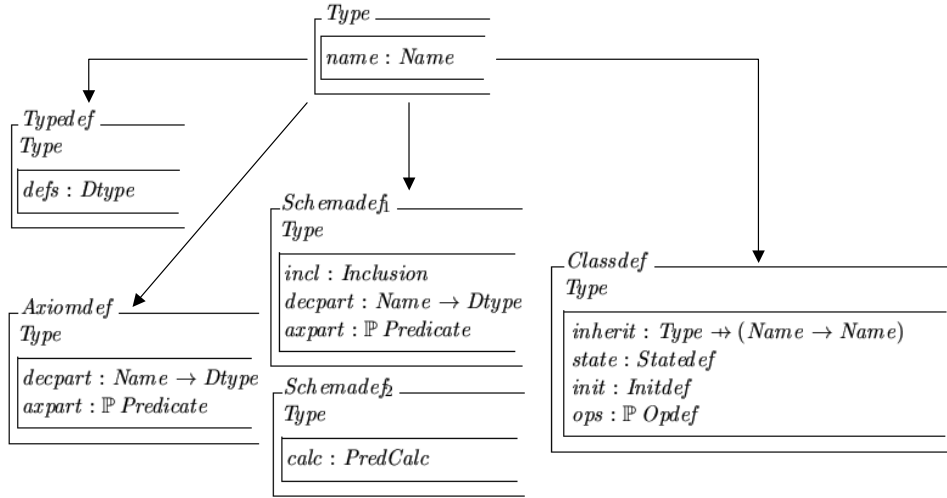
- Develop techniques for projecting (object-oriented) Z models to UML diagrams, based on XML Metadata Interchange (XMI).
- Use Object-Z to specify and design the essential functionalities of the ZML environment

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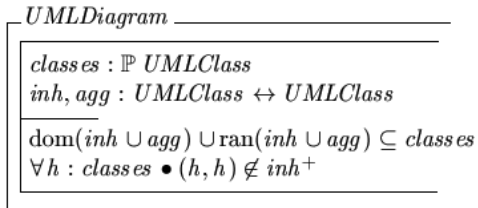
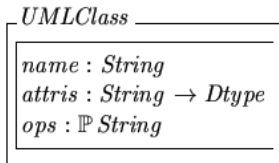


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Formal Object Design of ZML



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$project : \mathbb{P} \text{Classdef} \rightarrow \text{UMLDiagram}$

$\forall (oz, uml) : project \bullet$
 $\{c : oz \bullet c.name\} = \{c : uml.classes \bullet c.name\} \bullet$
 $\forall c_1, c_2 : oz \bullet$
 $\exists_1 c' : uml.classes \bullet$
 $c'.name = c_1.name$
 $c'.attris = \{cls : oz \bullet cls.name\} \triangleleft c_1.state.decpart$
 $c'.ops = \{o : Opdef \mid o \in c_1.ops \bullet o.name\}$
 $c_2.name \in \{t : ran c_1.state.decpart \bullet t.name\} \Rightarrow$
 $\exists_1 (c'_1, c'_2) : uml.agg \bullet c'_1.name = c_1.name \wedge c'_2.name = c_2.name$
 $c_2.name \in \{inh : dom c_1.inherit \bullet inh.name\} \Rightarrow$
 $\exists_1 (c'_1, c'_2) : uml.inh \bullet c'_1.name = c_1.name \wedge c'_2.name = c_2.name$

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Basic Implementation Ideas

- ZML: Define a customized XML for Z family languages for web-browsing purpose
- UML tool: Rational Rose 2000 supports XMI import/export according to UML.DTD
- Translation rules are applied using XSLT techniques to automatically translate Object-Z/TCOZ model(XML) to UML diagrams(XMI) and vice versa

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Syntax definition

```
<ElementType name="op" content="eltOnly" order="seq">
  <element type="name" minOccurs="1" maxOccurs="1"/>
  <element type="delta" minOccurs="0" maxOccurs="1"/>
  <element type="decl" minOccurs="0" maxOccurs="*/>
  <element type="predicate" minOccurs="0" maxOccurs="*/>
  ...
</ElementType>
<ElementType name="classdef" content="eltOnly">
  <element type="state" minOccurs="1" maxOccurs="1"/>
  <element type="init" minOccurs="0" maxOccurs="1"/>
  <element type="op" minOccurs="0" maxOccurs="*/>
  ...
</ElementType>
```

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XSL Transformation

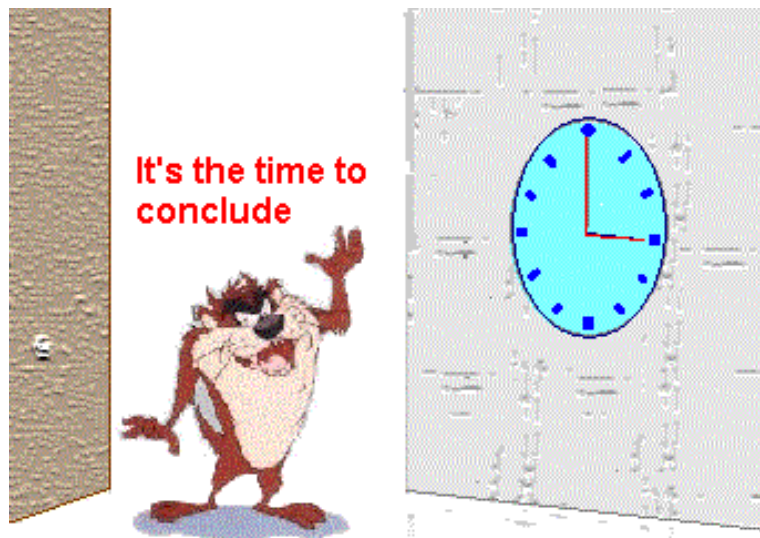
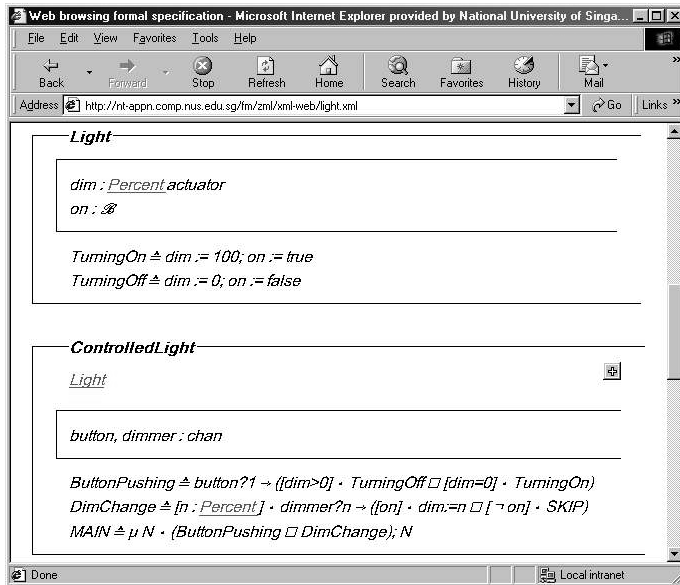
```
<xsl:template match="classdef[@layout='simpl'] classdef[@layout='gen']">
<html>
  ...
  <a><xsl:attribute name="name"><xsl:value-of select="name"/></xsl:attribute></a>
  ...
  <xsl:apply-templates select="state"/>
  <xsl:apply-templates select="init"/>
  <xsl:apply-templates select="op"/>
  ...
</html>
</xsl:template>
```

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Light example

```
<classdef layout="simpl" align="left">
  <name>Light</name>
  <state>
    <decl>
      <name>dim</name>
      <dtype><type>Percent</type><type>&actuator;</type></dtype></decl>
    <decl>
      <name>on</name>
      <dtype><type>&bool;</type></dtype></decl>
    </state>
    <op layout="calc">
      <name>TurningOn</name>
      <predicate>dim := 100; on := true</predicate> </op>
    ...
  </classdef>
```

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Conclusion and Further Work

- State-based (Object-Z), Event-based (Timed CSP), Graph-based (UML)
- TCOZ
 - combines the modelling powers from Object-Z and Timed CSP
 - distinguishes the notion of **active** and **passive** objects
- Further research
 - applications to the specification of
 - * software architectures
 - * parallel distributed systems
 - tools support
 - Hoare and He's UTP to TCOZ semantics
 - TCOZ refinement rules

TCOZ papers

- * J. Sun, J.S. Dong, J. Liu and H. Wang. Object-Z Web Environment and Projections to UML. *WWW-10: 10th International World Wide Web Conference*, ACM Press, May 2001.
- J. Liu, J.S. Dong and J. Sun. TRMCS in TCOZ, *10th International Workshop on S/W Specification and Design (ISSWD'00)*, San Diego, USA, IEEE Press, Nov, 2000.
- * J. Liu, J.S. Dong, B. Mahony and K. Shi. Linking UML with Integrated Formal Techniques, *UML: Systems Analysis, Design, and Development Issues*, 2000.
- * B. Mahony and J.S. Dong. Timed Communicating Object Z *IEEE Transactions on Software Engineering*, 26(2):150-177, Feb 2000.
- J.S. Dong, B. Mahony and N. Fulton, Capturing Concurrent Interactions of Mission Computer Tasks, *The 6th Asia-Pacific S/E Conference (APSEC'99)*, IEEE Press, Dec, 1999.
- * B. Mahony and J.S. Dong. Sensors and Actuators in TCOZ. *World Congress on Formal Methods (FM'99)*, Lecture Notes in Computer Science, Springer-Verlag, Toulouse, France, Sep 1999.
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